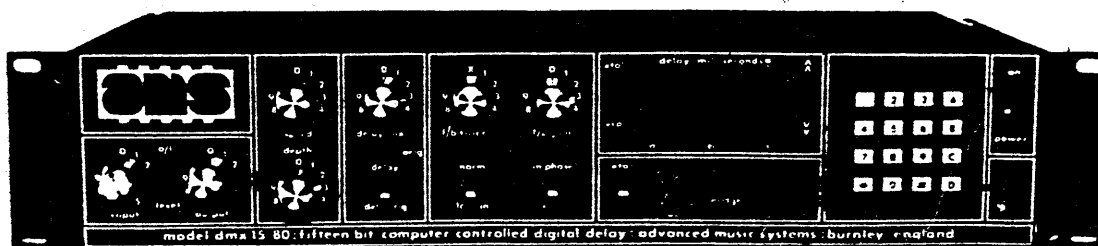
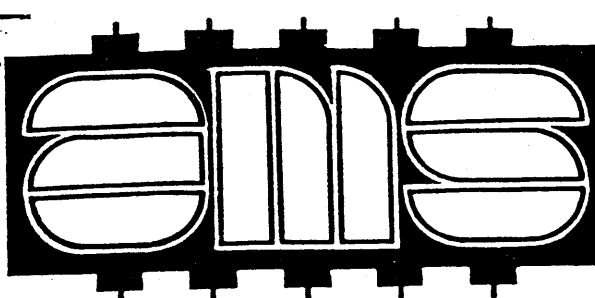


advanced music systems

model dmx15-80

8 CIRCUIT DIAGRAMS





Advanced Music Systems,
66 Ightenhill Park Lane,
Burnley,
Lancashire,
England.

Telephone : 0282 . 36943

The AMS DMX 15-80 has microprocessor driven data entry, display and effects control. This system allows for two-channel operation of delay and one other effect per channel. Nine stores are provided; each store contains the complete settings of both channels and the choice of effects selected for these channels. Below is a description of the functions of the various controls of the unit.

KEYPAD

KEYS A,B - These select audio channel A or B, depressing either will cause the delay setting for that channel to be displayed in the LED numeric display (milliseconds), and relevant channel identifier LED immediately below the display to be lit (A or B).

KEY C. - Depressing this key after A or B comes the special function on that channel to be displayed - either pitch ratio or reverbation program. In this case LED C will glow in addition to the channel identifier A or B.

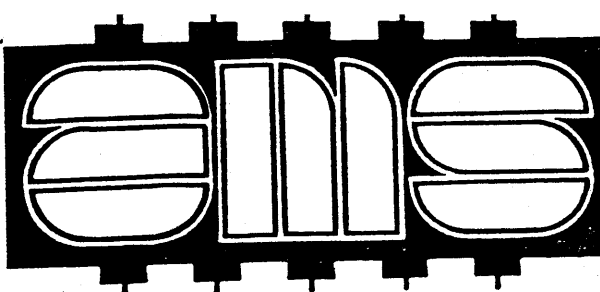
NUMERIC KEYS 0 to 9. - Keying in a delay or effects setting will cause entry into the numeric display. Until this data has been entered into the unit (by depressing the # key), the channel identifier LEDs will flash, indicating temporary data; the displayed data only reflects the contents of the DDL once the # key has been depressed and the LEDs have stopped flashing.

KEY *. - This key clears the numeric display to zero, in case of erroneous data. The correct data may now be entered.

KEY D. - This key causes the current store number to be displayed in the format Str.8. To change the current store, a single numeral should be typed in the range of 1 to 9. No enter (#) keystroke is required. The system will immediately enter into the delay and effects sections the contents of that store, and all subsequent entries will be retained in that store.

If instead of a numeral, A or B is depressed the system will display delay contents for that channel (see above) and the store number will remain unaltered.

.....P.T.O.



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.....CONTINUED

NOTES.

1. If more than four numeric characters are typed, or the user attempts to enter a delay, pitch ratio or reverberation program outside the limits of the unit, the message " Err " will be displayed to warn the operator. The correct entry may then be typed.
2. On powering up the unit or reset, the unit will be set to: delay only, (channels A and B); delay 0ms; Store 1.
3. Keys depressed as described above which refer to effects not contained within the unit will be ignored.

NUDGE BUTTONS.

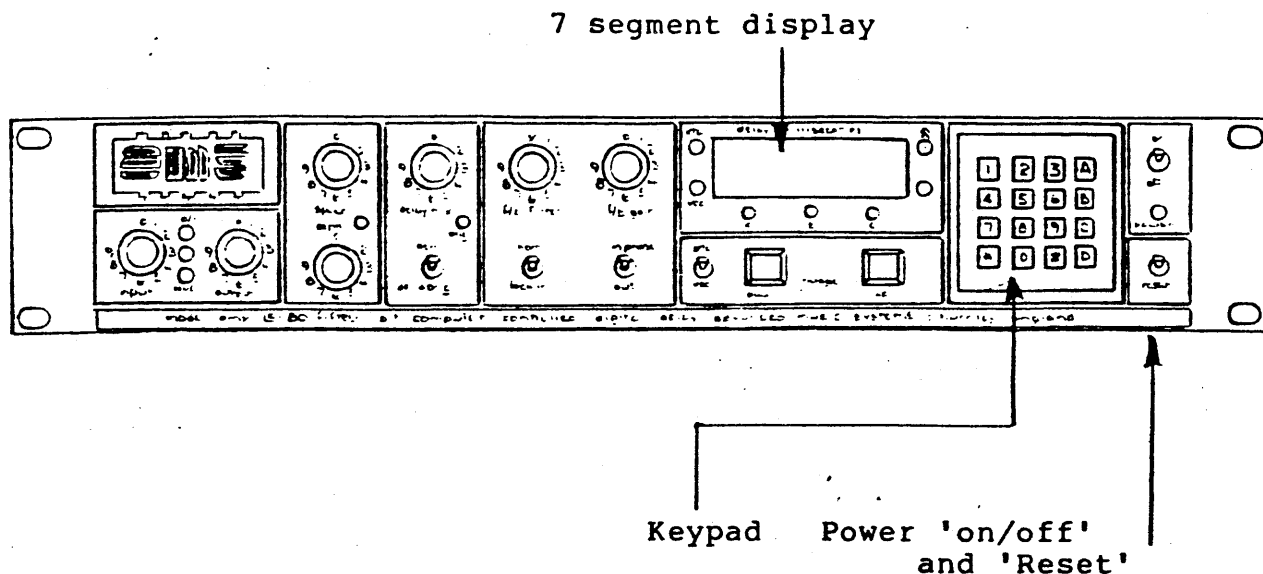
To edge the selected delay up or down, the nudge buttons may be depressed; these cause the delay line contents to be updated and also affect the value in the current store.

OTHER CONTROLS.

V.C.O. SECTION. The V.C.O./XTAL switch is used to select an accurate, crystal-locked delay or a cyclically varying delay generated by a voltage-controlled oscillator which has both speed and depth controls.

LEVEL CONTROLS. Input and Output levels are infinitely variable, and the operating level of the unit is indicated by "traffic signal" LEDs. The red LED is illuminated when 6dB headroom is left. In normal operation input level should be adjusted so that the yellow LED is lit - this occurs 6dB below the red LED threshold; the green LED lights at a level of 6dB lower still.

.....P.T.O.



2.4.7 COMPUTER CONTROL

Computer control is effected by the use of the keypad:

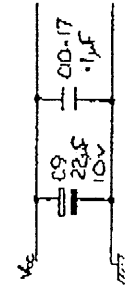
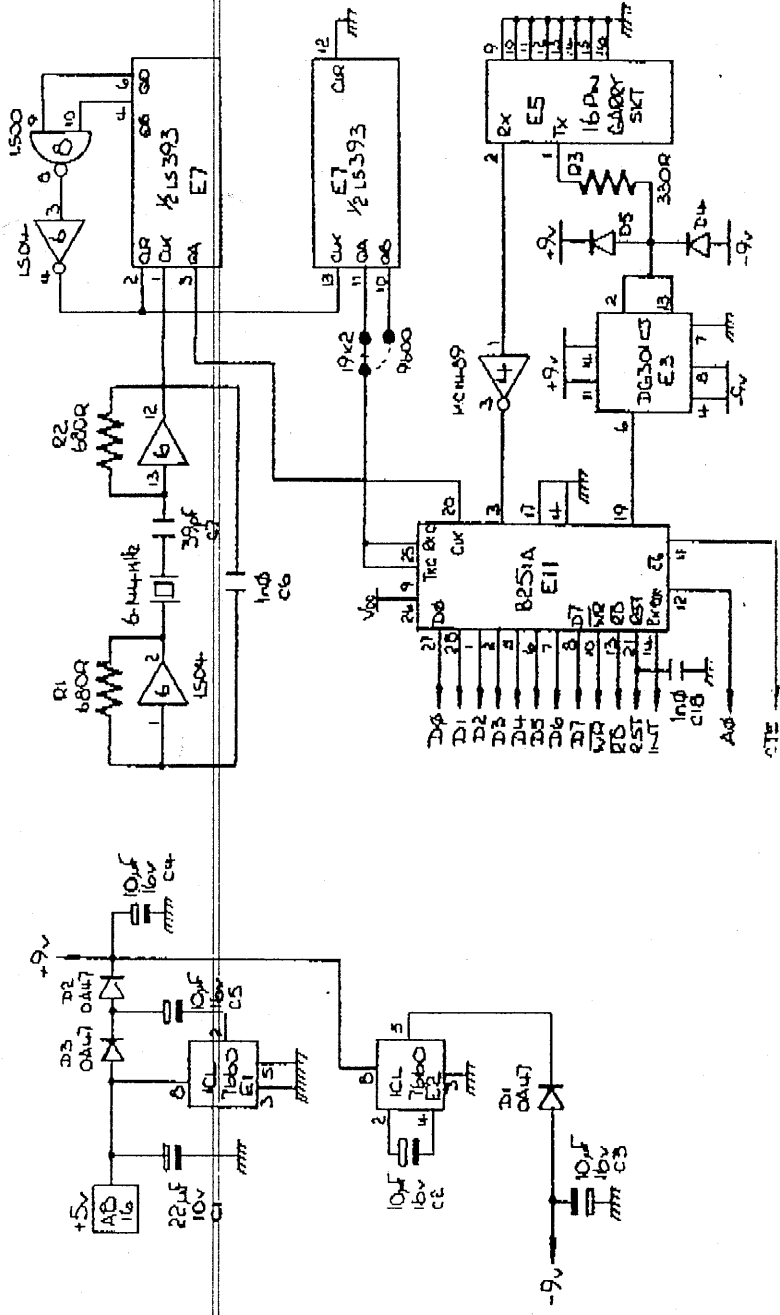
KEYS A and B These select the audio channel to be processed either 'a' or 'b', depressing either will cause the delay setting for that channel to be displayed (in milliseconds) and the relevant identifier LED immediately below the display to be lit (a or b).

KEY C This key is inoperative unless the Pitch Change or Reverberation option is included within the unit. If either or both of these options are included then depressing this key after A or B causes the special function on that channel to be displayed: either Pitch Change Ratio or Reverberation Program Number. In this case LED c will glow in addition to the channel identifier LED a or b.

KEYS 0 to 9 Keying in a delay or effects setting will cause entry into the display only and will not affect the programme until the data is entered into the unit by depressing the # key. Until the # key is depressed the channel identifier LED will flash indicating temporary data. The display only reflects the contents of the ddi once the # key has been depressed and the channel identifier LED has stopped flashing.

KEY * This key has the function of 'Clear Last Entry' on units without Pitch Change. If however Pitch Change is chosen as an option, the key then functions as the decimal point.

KEY D Depressing this key causes the current store number to be displayed in the format 'Str.5'. To change the current store a single numeral is typed



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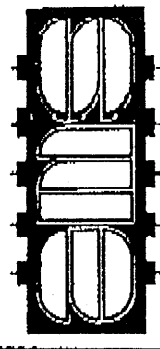
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DRAWN
12

DATE
11.7.84

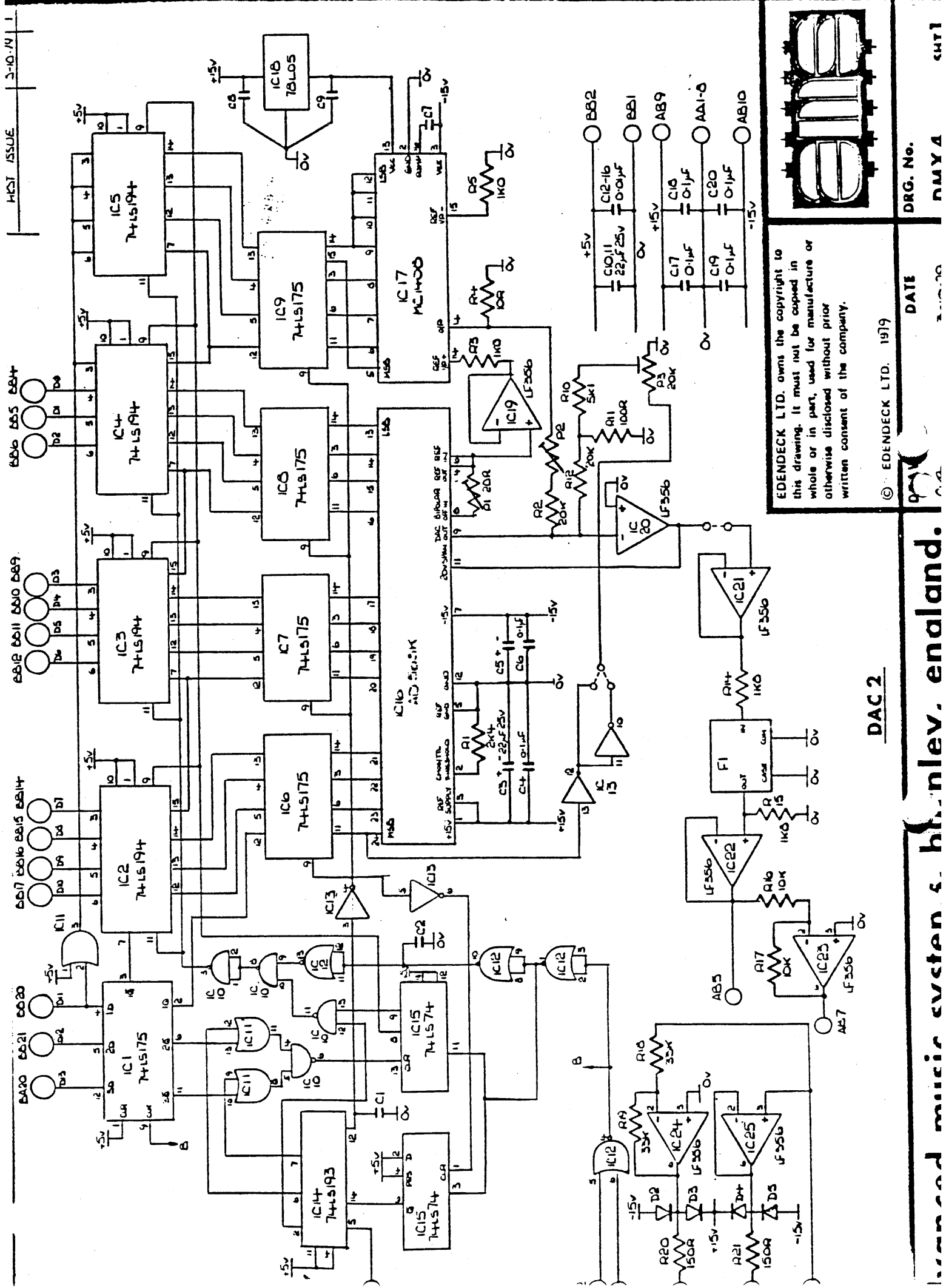
DRG. No.
DMX88.1

SHT 2



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FIRST ISSUE	1.1.84	1

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DAC 2

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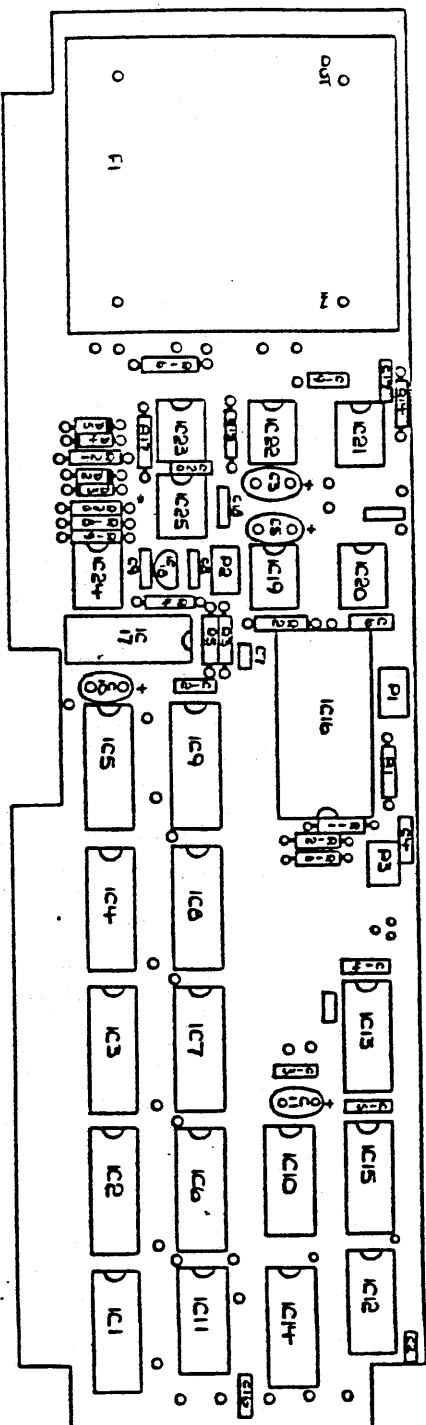
NYA 4

CHY 1

Standard micro system & technology. enland.

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DAC 2 BOARD



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3.10.79

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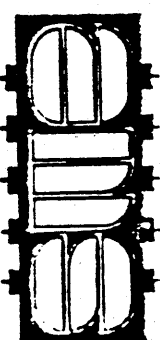
DMX 4

SH 72

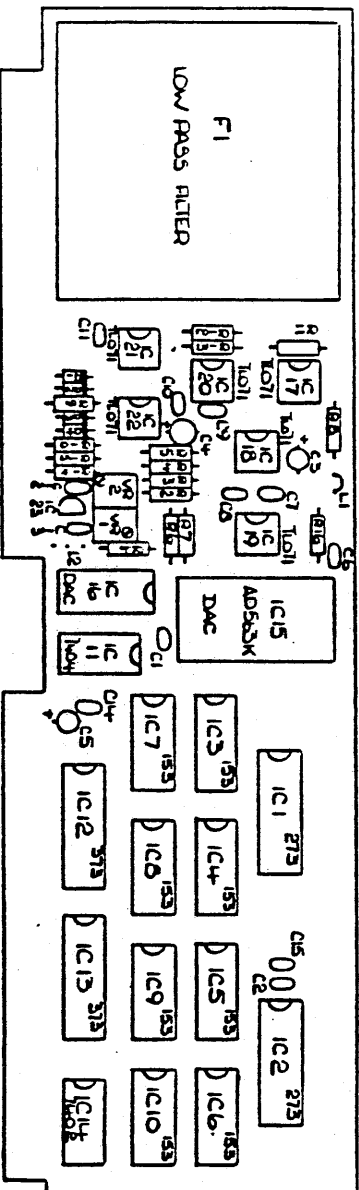
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1



DAC2 BOARD MOD1



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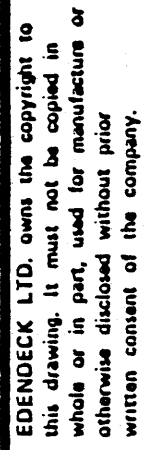
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DRG. No.

DMX12

SHEET



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advanced music systems **turnkey** and **turnkey**

DATE _____

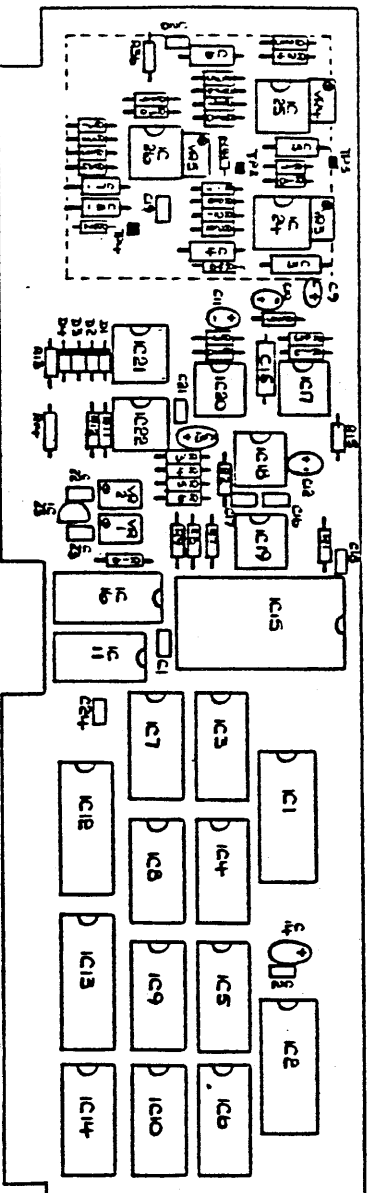
DRG. No.

5HT 2

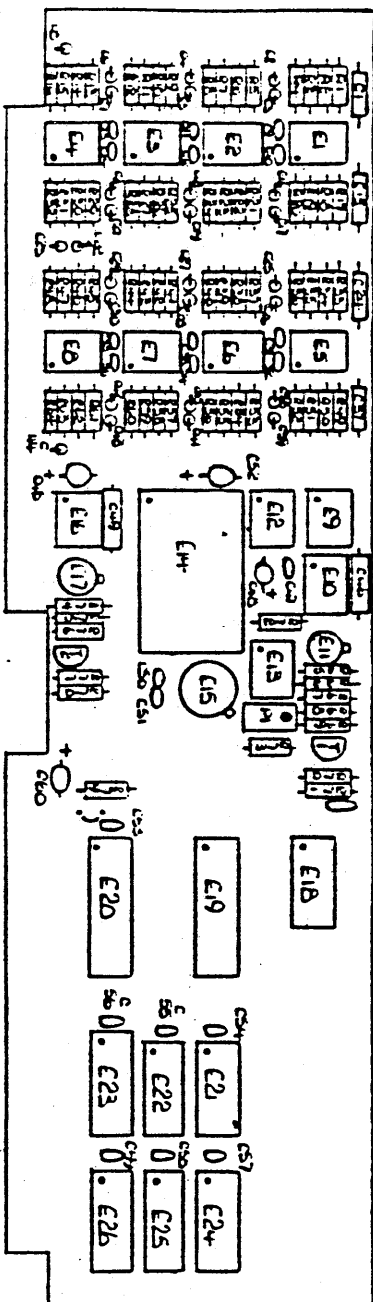
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cell DIAG MAG	10-1-81	6
INACCURATE	8-1-81	4
CORRECTED MAG 13 months		3
RES & COP ADDED FILTER		2
FIRST ISSU	11-11-79	



LINE 1 - W END OF R36 TO EDGE LOW RES AD1



16BIT DAC

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REVISION	NO.05	4-0000	2
FIRST ISSUE		1982	1



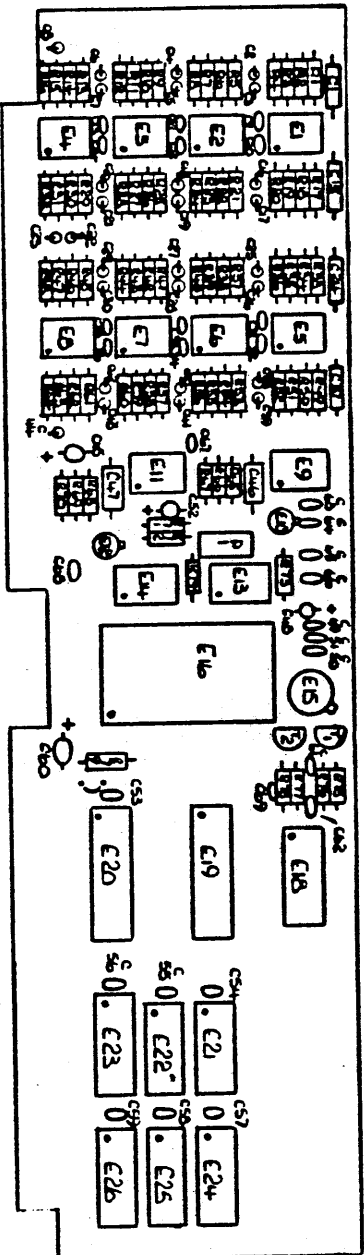
DR. N

DATE

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10BIT DAC



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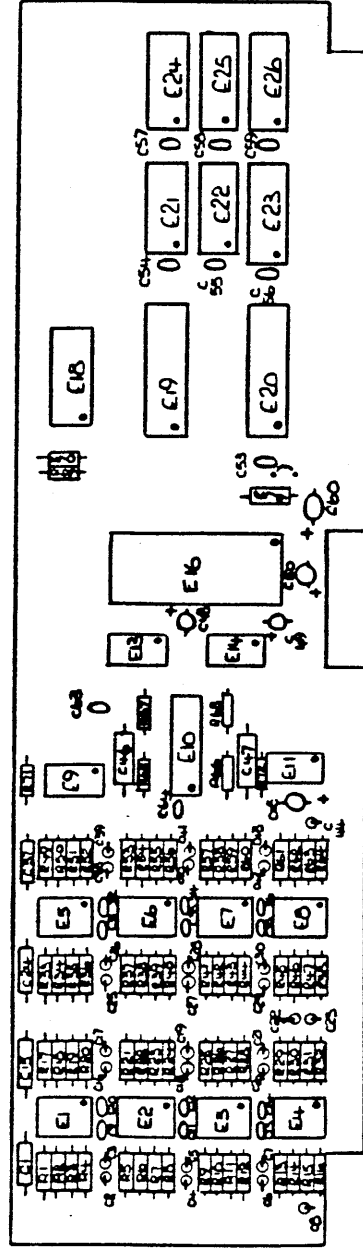
ENG. No.

ENG. No.

SWT 2



REVISED	8802	30.02	3
REVISED	8805	4.08	2
FIRST ISSUE		1982	1



16BIT DAC

DDR 65,67	23.08.4	5
DAC CHANGE	16.12.85	4
REWORK EPR-R	30.02	3
REWORK MODS	4.10.80	2
FIRST ISSUE	7.9.82	1

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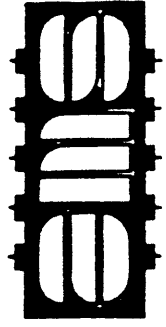
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9.9.82

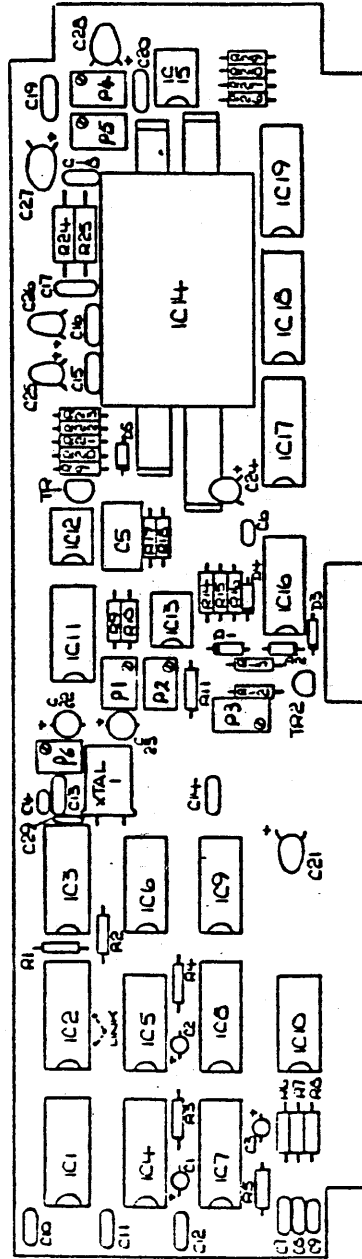
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DMX66.5

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ADC 3 BOARD

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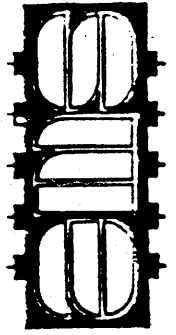
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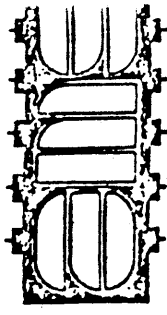
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REVISED	DATE	2
FIRST ISSUE		1

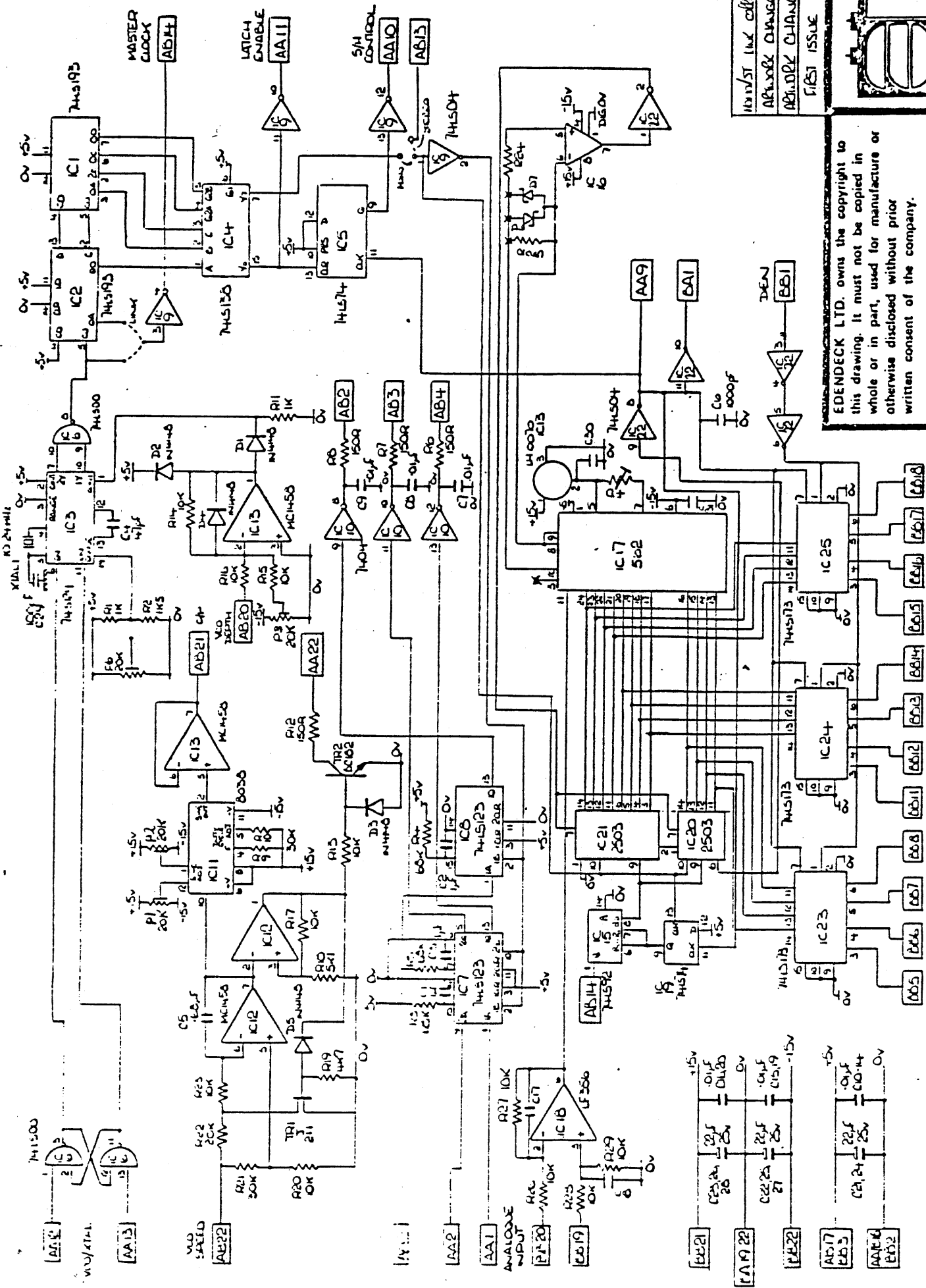


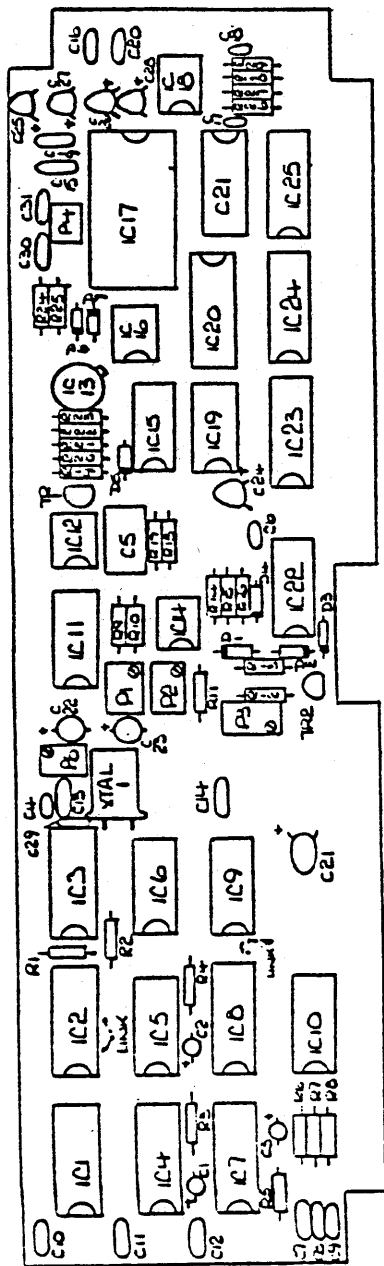
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APR 1979	CHANGE	
FIRST ISSUE		





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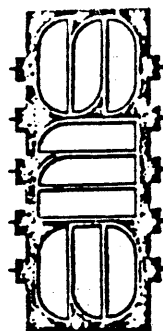
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DOWN

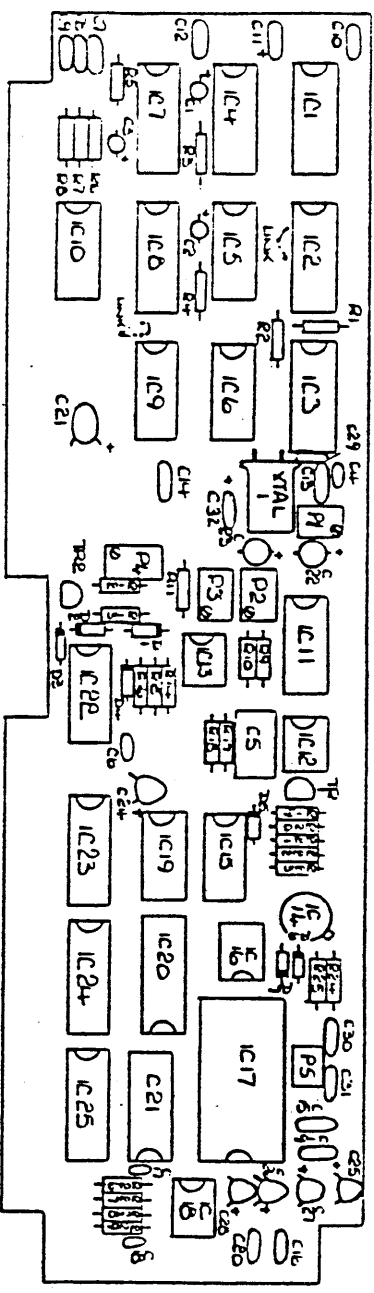
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FIRST ISSUE	9-7-80	3



ADC3 BOARD

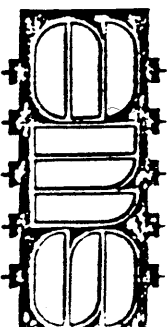


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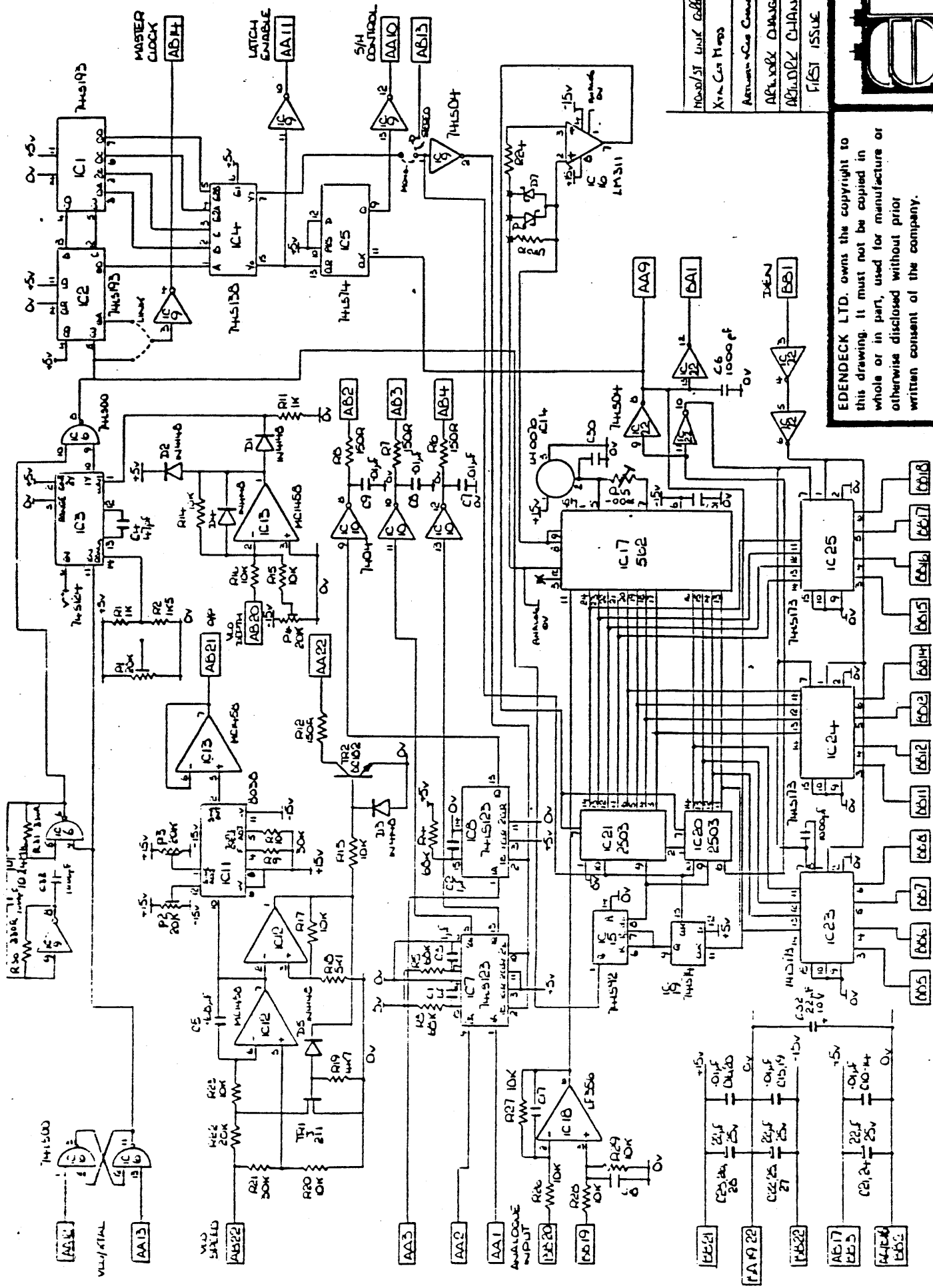
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Completed IC15-16	24.9.80
Final board	
FIRST ISSUE	9.7.80



DRG. No. DMX14.4 SH 7

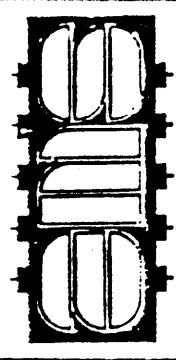


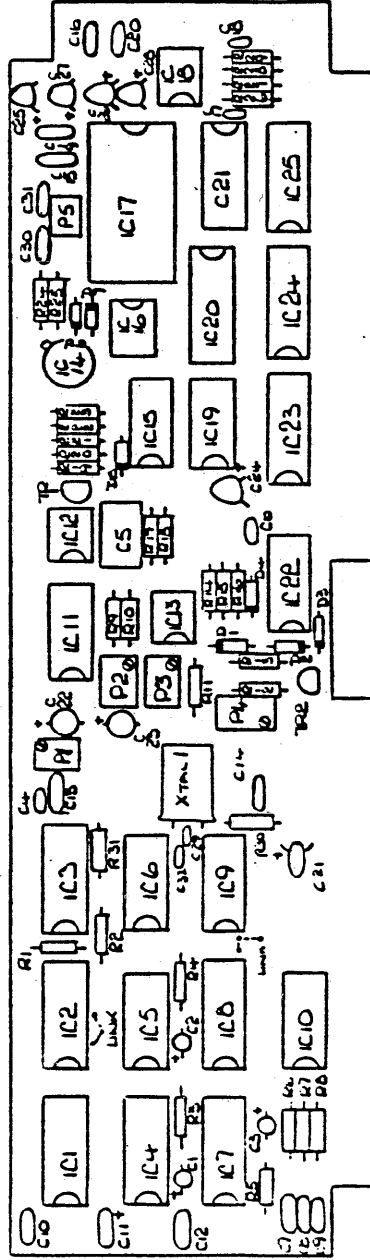
ADC3 BOARD

Model/ST Unit Addressed	26-5-82	6
X-ray Can Maps	3-12-80	5
Antenna Co. Cables	24-9-80	4
ADL-100X CHANGE	9-7-80	3
ADL-100X CHANGE		2
FIRST ISSUE		1

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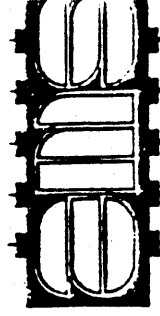
DATE 9-7-80

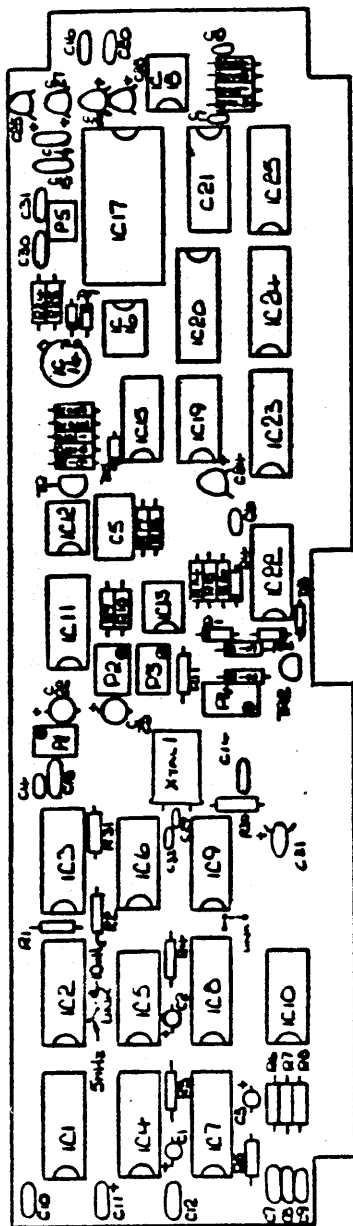
DRG. No.

DMX14.5

SHT 2

MOD/SI MAX ORDERED	28-5-82
MANUFACTURED BY	3-12-80
DESIGNED BY	24-9-80
FIRST ISSUE	9-7-80





540 MHz 10K 340000	19.0.82	7
400000 10K 340000	23.5.02	6
400000 10K 340000	3-12-80	5
400000 10K 340000	24-9-00	4
400000 10K 340000	9.7.80	3

ADC3 BOARD

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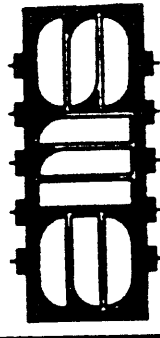
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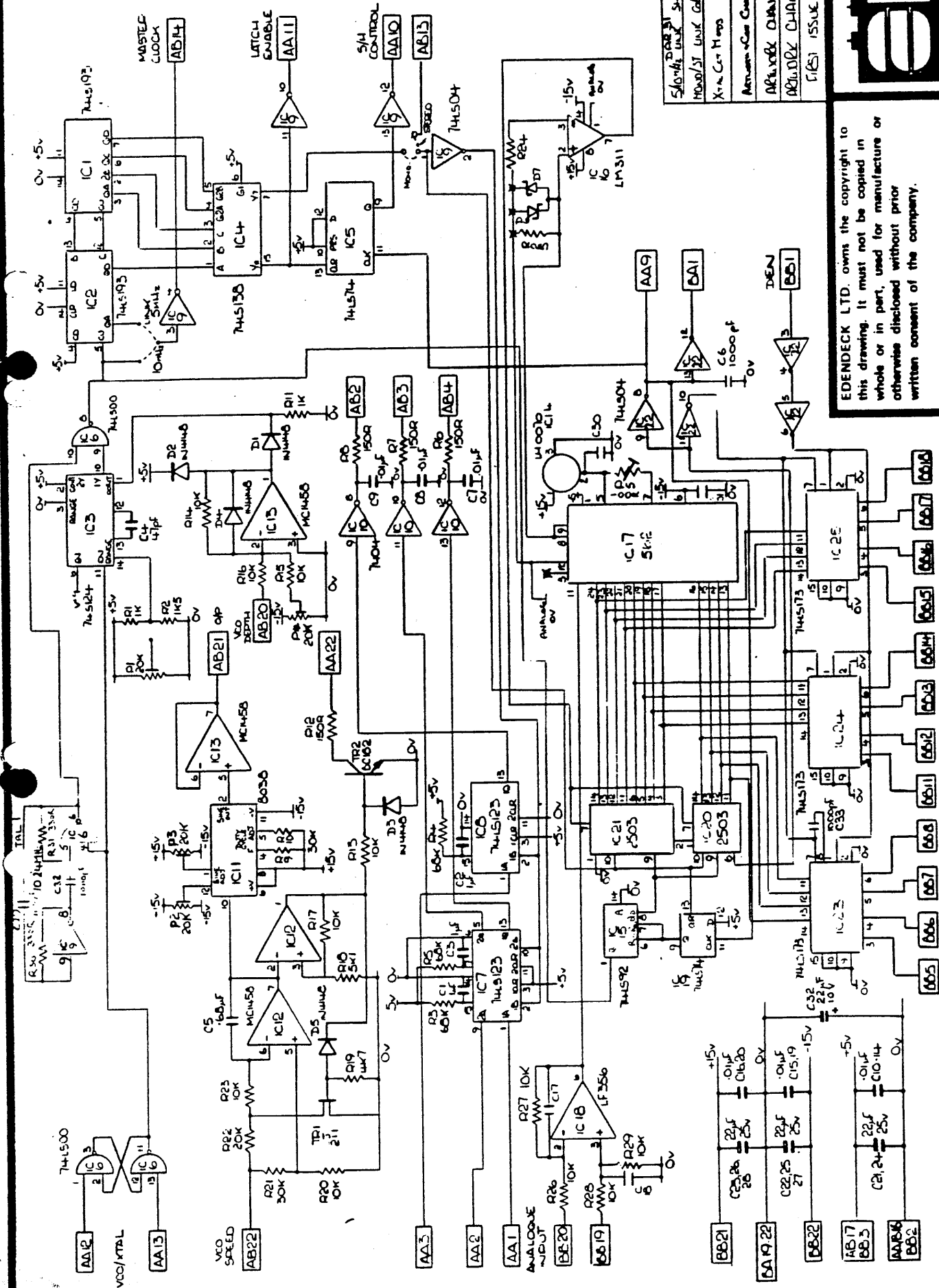
ADC2 BOARD

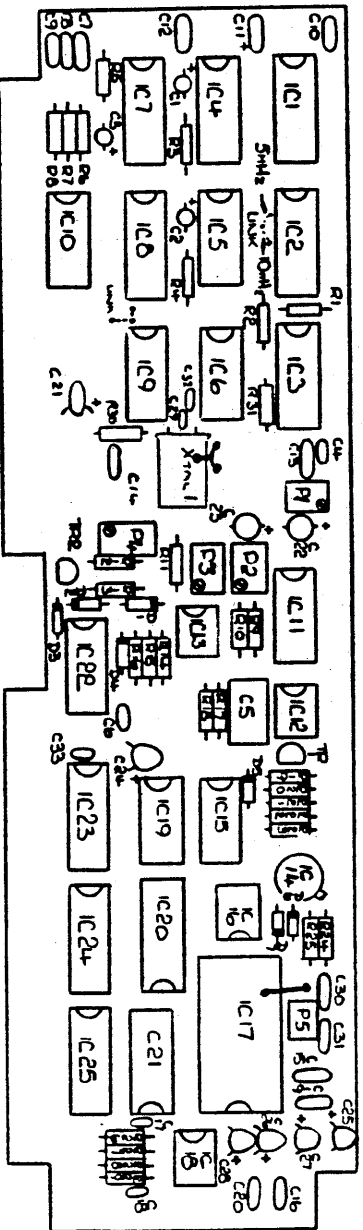


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510Hz unit	0-9-83	7
1000/31 unit	2-5-84	6
X-2 Car Memo	3-12-80	5
Automatic Car Control	24-9-80	4
ALFA ROMEO CHANGE	9-7-80	3
POULDER CHANGE		2
1/63 ISSUE		1





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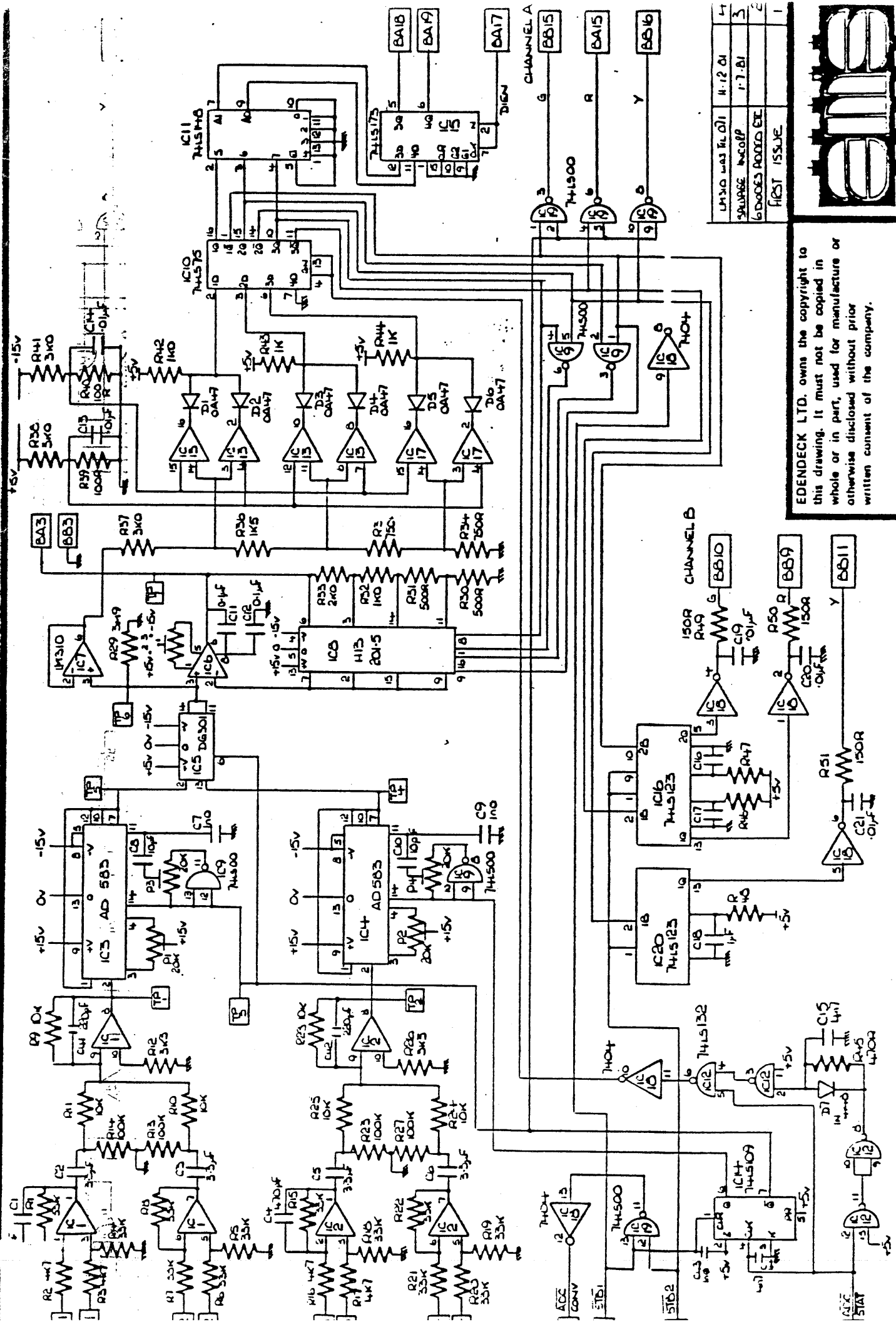
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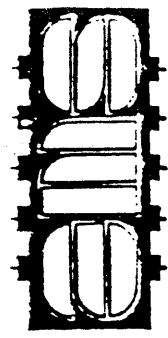


DRG. 31. A0000 4	8.9.83	1
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CS2444 IC15+16	24.9.00	4
1032 10000		
FIRST ISSUE	9.7.80	3

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835	836	837	838	839	840
841	842	843	844	845	846
847	848	849	850	851	852
853	854	855	856	857	858
859	860	861	862	863	864
865	866	867	868	869	870
871	872	873	874	875	876
877	878	879	880	881	882
883	884	885	886	887	888
889	890	891	892	893	894
895	896	897	898	899	900
901	902	903	904	905	906
907	908	909	910	911	912
913	914	915	916	917	918
919	920	921	922	923	924
925	926	927	928	929	930
931	932	933	934	935	936
937	938	939	940	941	942
943	944	945	946	947	948
949	950	951	952	953	954
955	956	957	958	959	960
961	962	963	964	965	966
967	968	969	970	971	972
973	974	975	976	977	978
979	980	981	982	983	984
985	986	987	988	989	990
991	992	993	994	995	996
997	998	999	1000	1001	1002

STEREO SAM ER

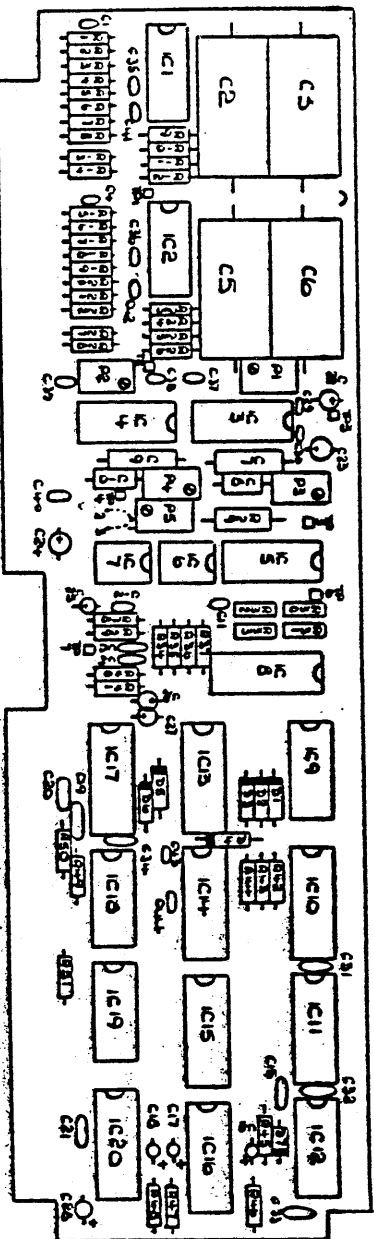
EDENDECK LTD. 19

DRG. No.

DATE

SH

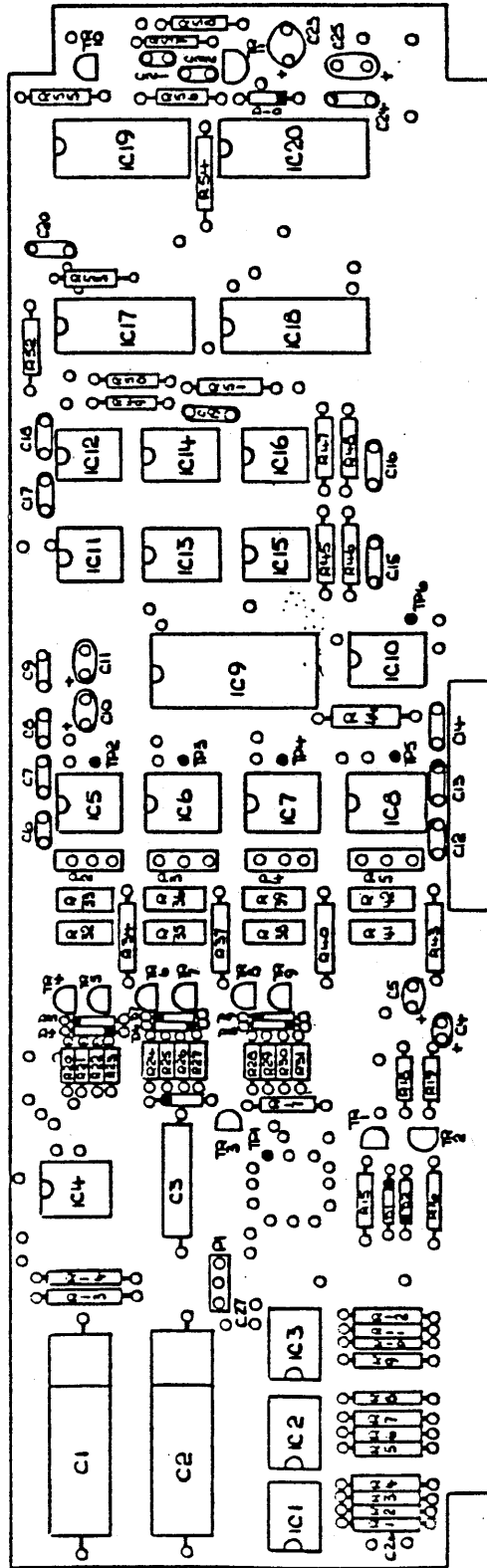
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LA 30 WAS FL 071	11-12-81
C4 & C42 ADDED	1-7-81
6000ES ADDED C7C	
FIRST ISSUE	



Drug No.



SAMPLER BOARD 3

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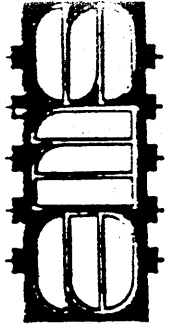
Advanced music systems. Turnley, England.

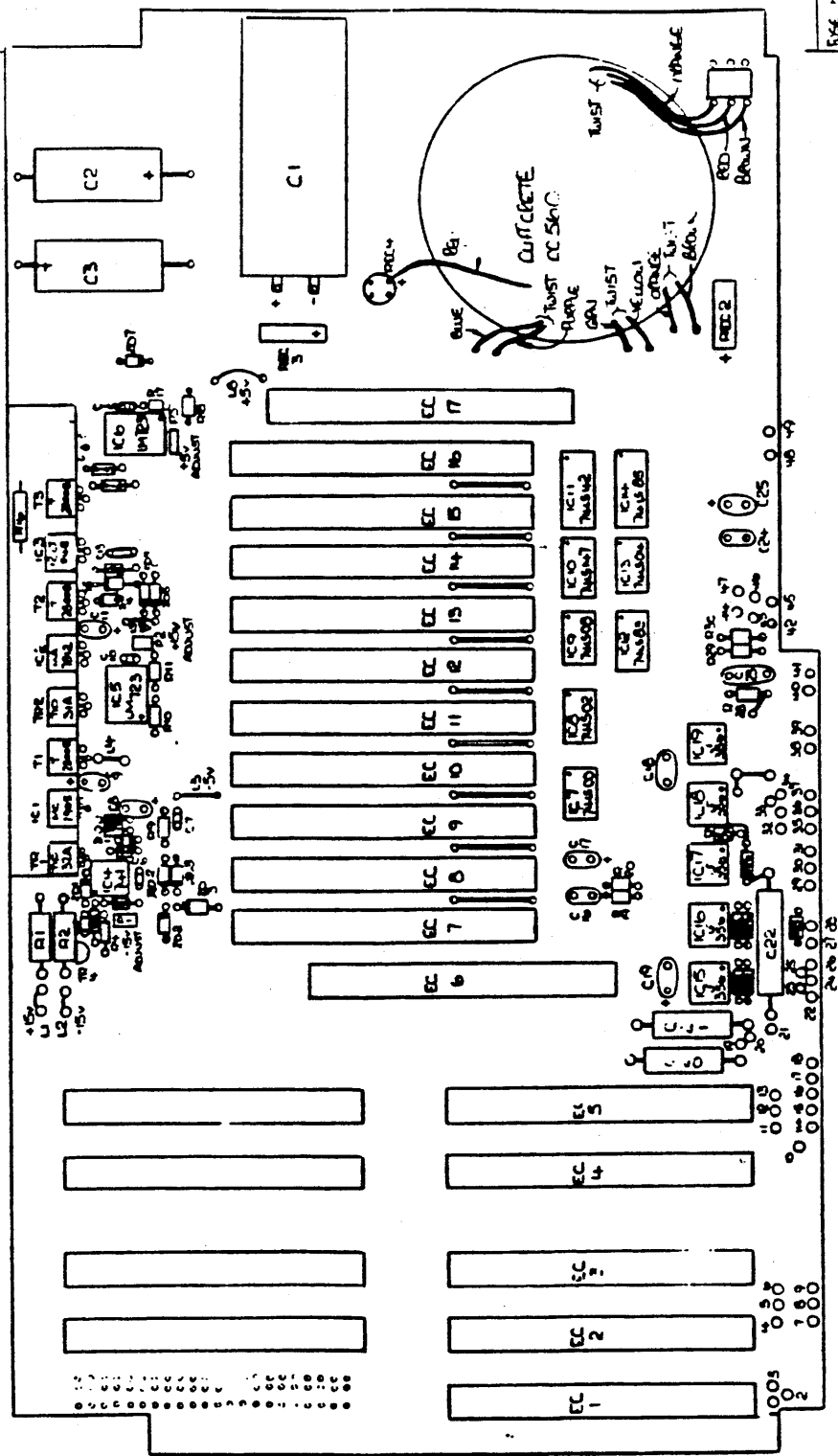
DRG. No.

DATE

OWN

FIRST ISSUE 3-10-79





USE MARKED TO REAR	10 2 83	7
IF'S MARKED TO REAR	6 7-82	6
CHARGES ON SMT'S	6 5 82	5
CHARGES SMT'S	1-7-8	4
CHARGES SMT'S	30 9 80	3
OVERALL SHIP CHARGE	3 2 80	2
FIRST ISSUE	28 9 79	1

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20-9-79

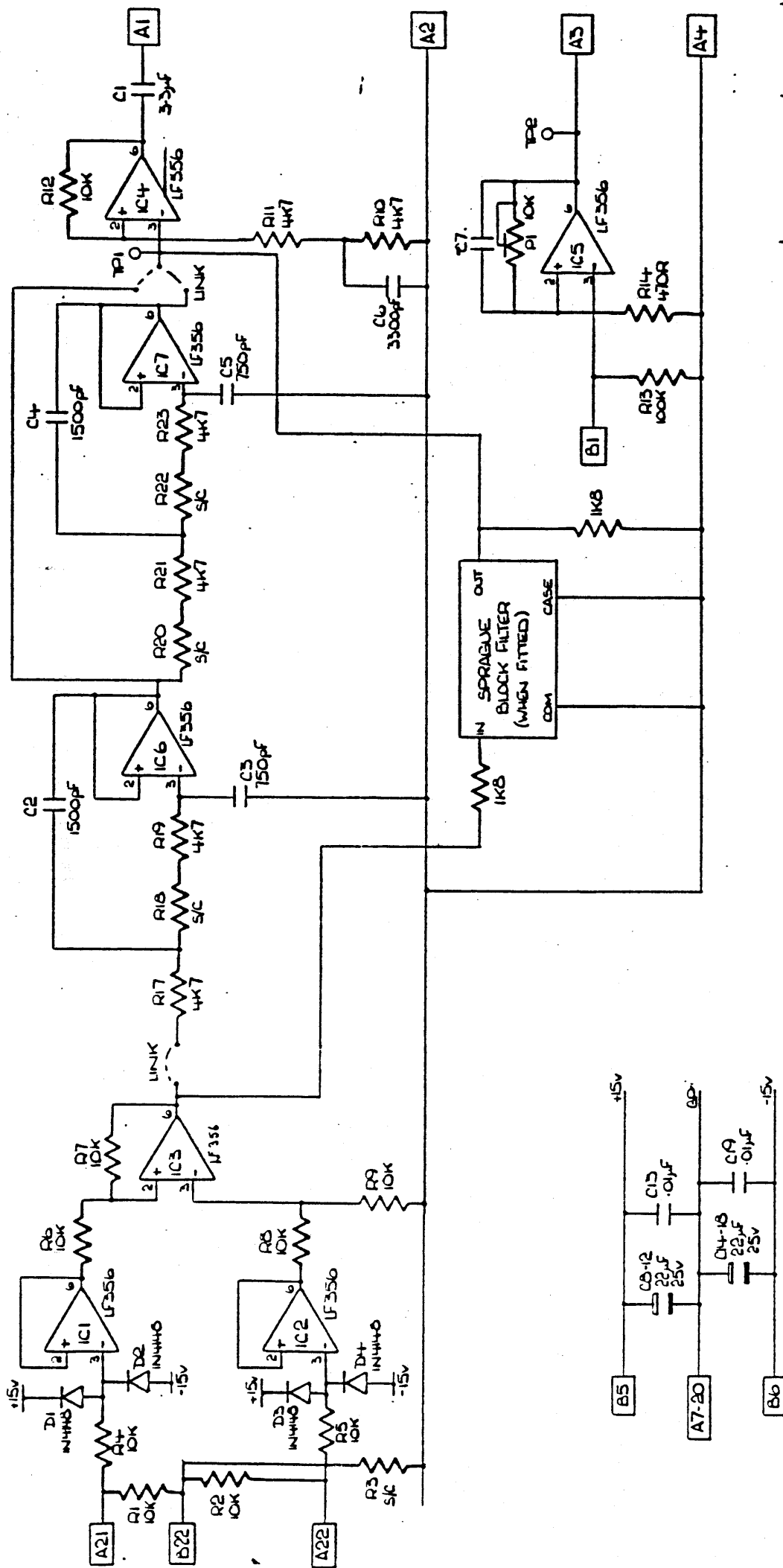
PROG. No.

DMX MB.2 REV 4

BASEBOARD

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INPUT BOARD

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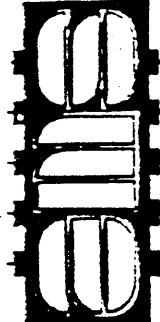
DATE

DRG. No.

FIRST ISSUE

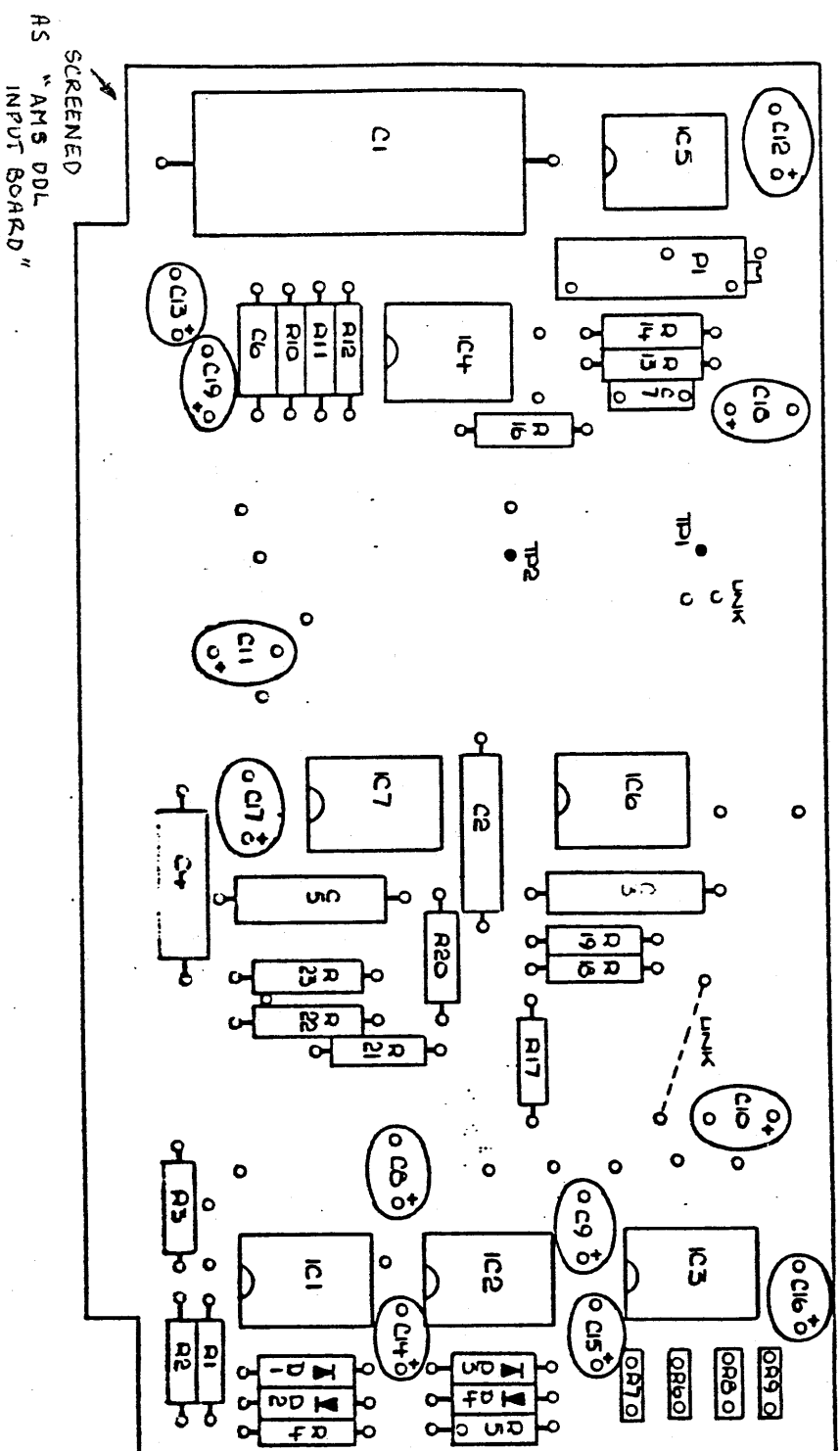
28.9.79

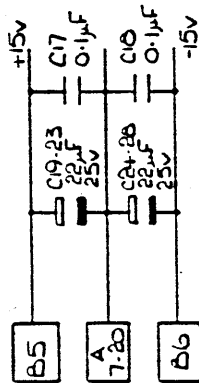
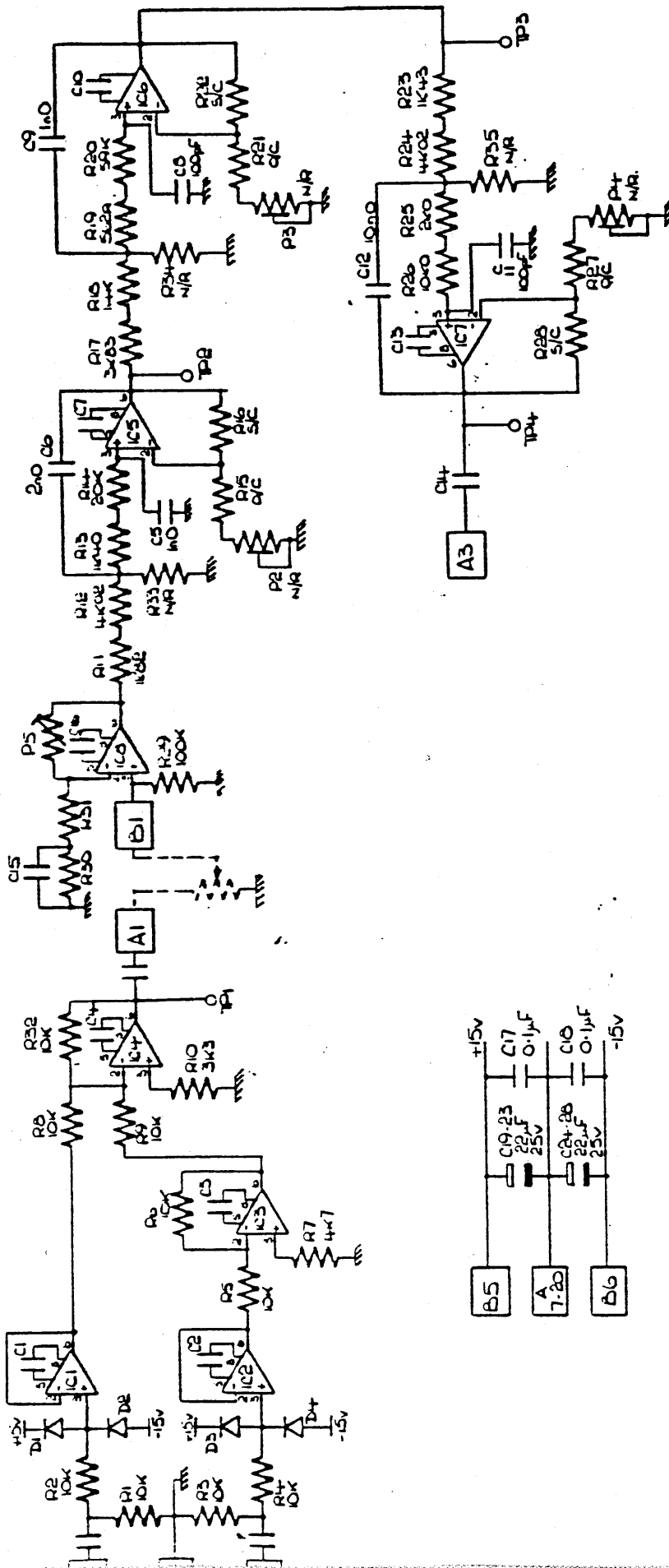
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INPUT BOARD

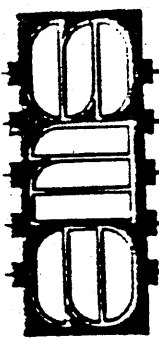
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DRG. No. DMX 1.3

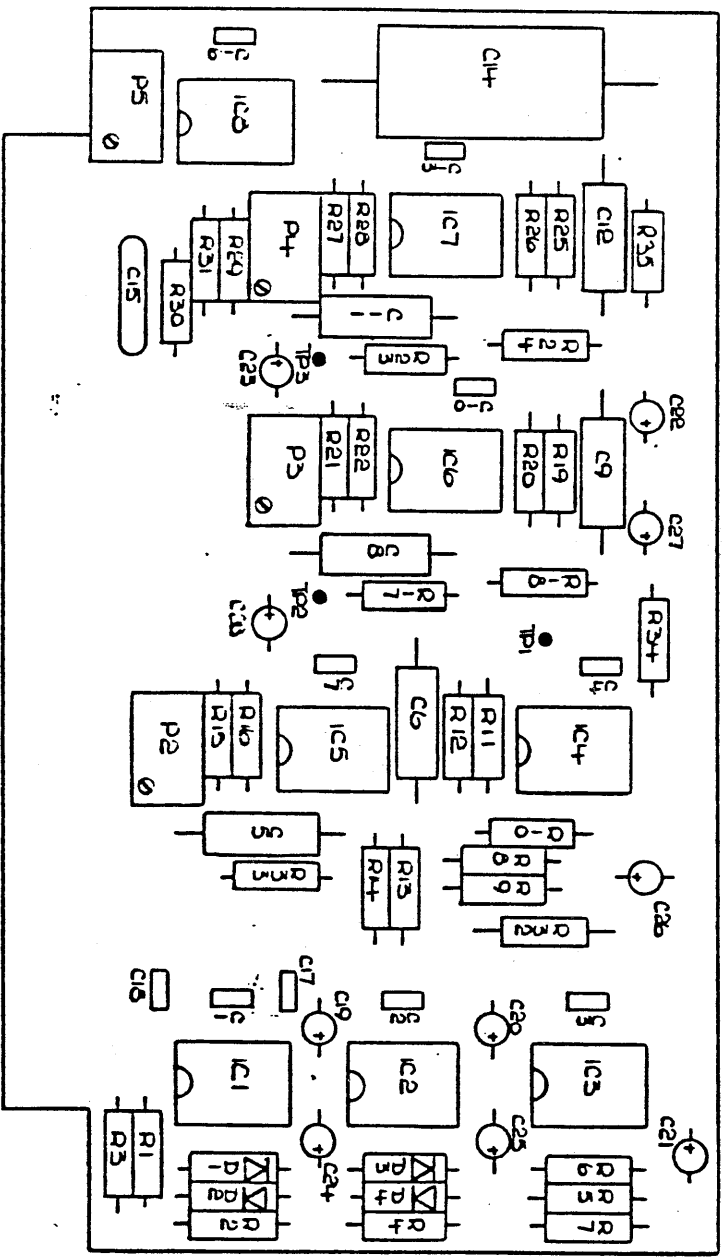
SHT 1



RESISTOR VALUES GIVEN	19.3.81	5
TP1 WAS BOM AUG 6 1983	24.2.81	4
FIRST ISSUE	23.5.80	3

Advanced music systems. Burnley, england.

INPUT BOARD



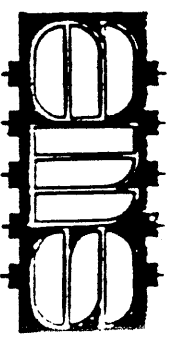
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DATE 23.5.80

DR. J. M. 23.5.80

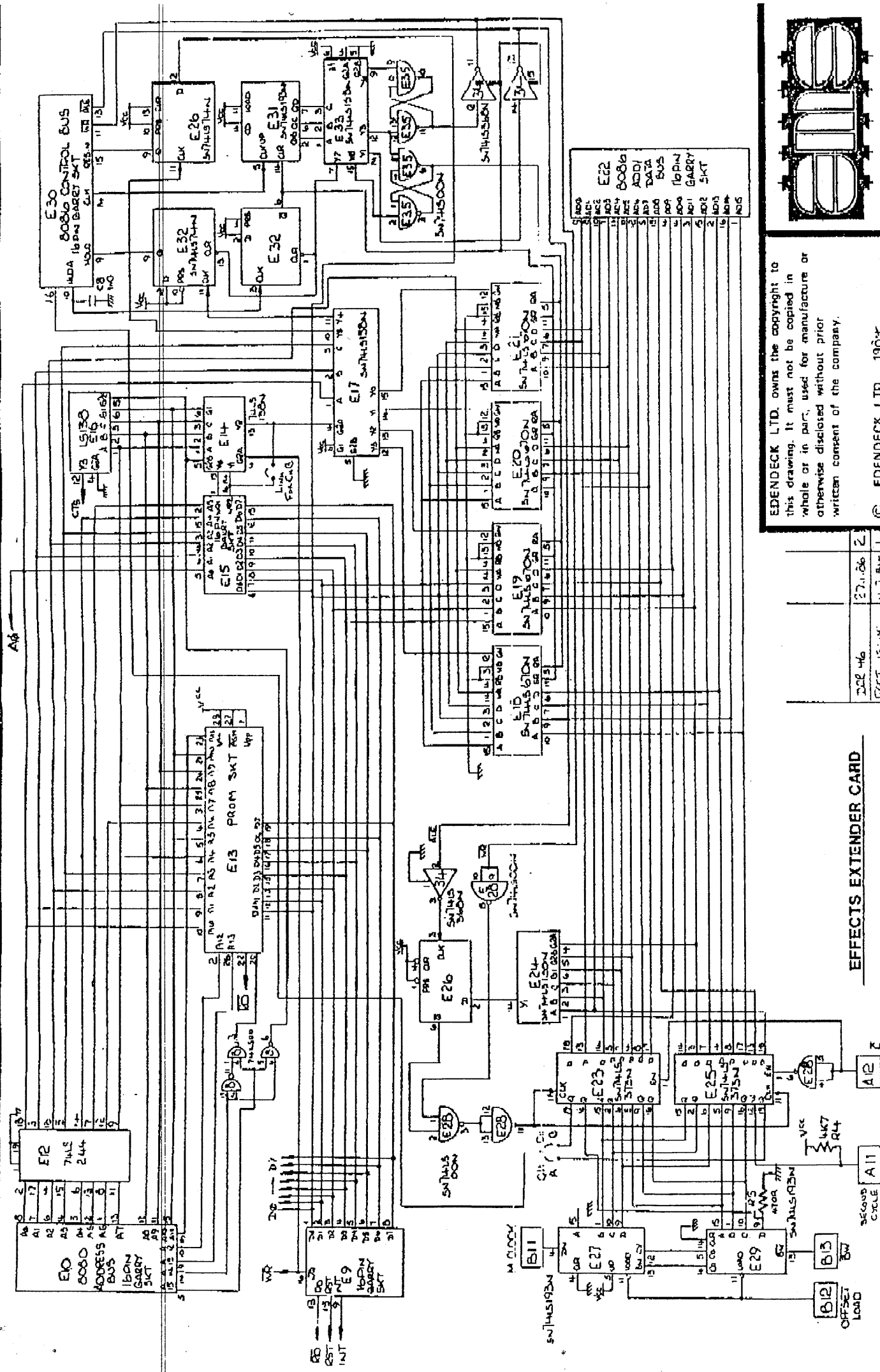
COMP VALUE C15	10-3-81
REST 15515C	23-5-80



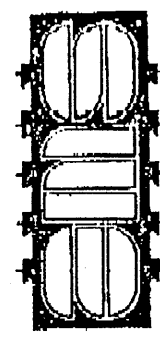
DRG. No.

DMY1 2

cur 2



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EFFECTS EXTENDER CARD

202 46	27.06.72
TEST ISSUE	11.7.84

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DRAWN

DATE

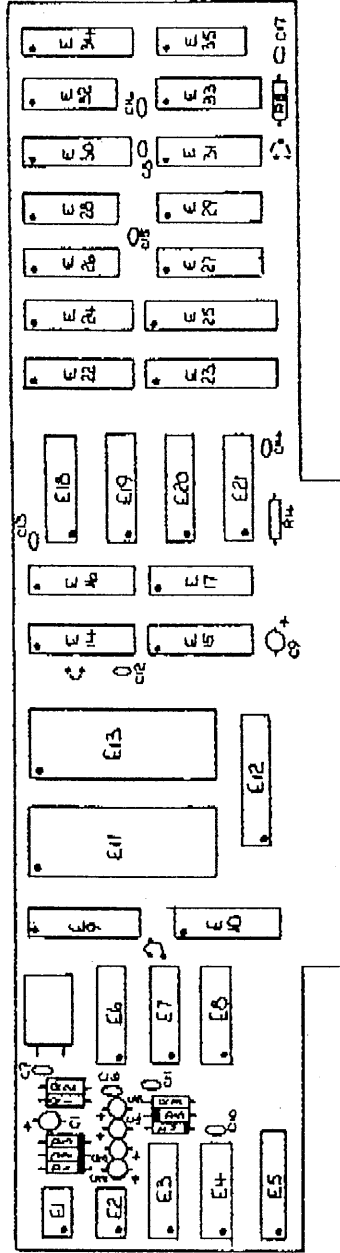
DRG. No.

SHT 1

DMX88.1

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DATE

20.7.84

DRG. No.

DMX88.1

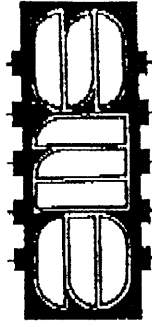
SHT 3

DDR M4

27.1.86

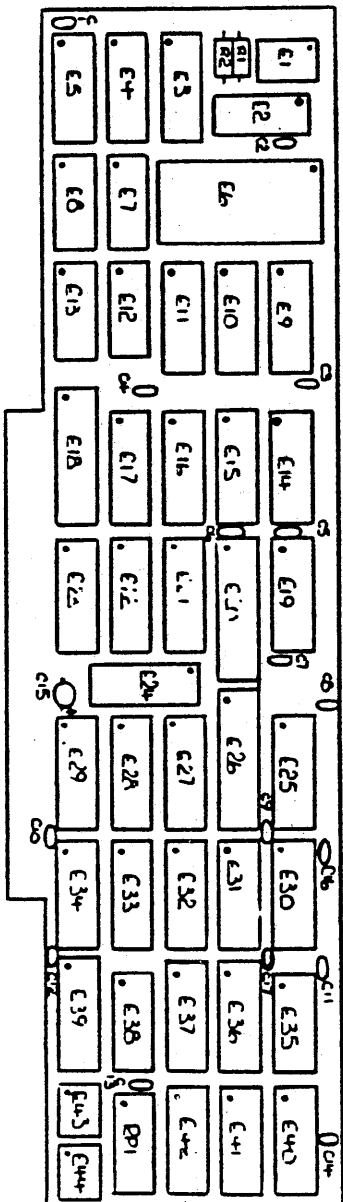
FIRST ISSUE

20.7.84



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E1 HAS 3 CLOSING IN FROM CHANGE
 SHOULD FOR 1 FROM CHANGE



MCB8

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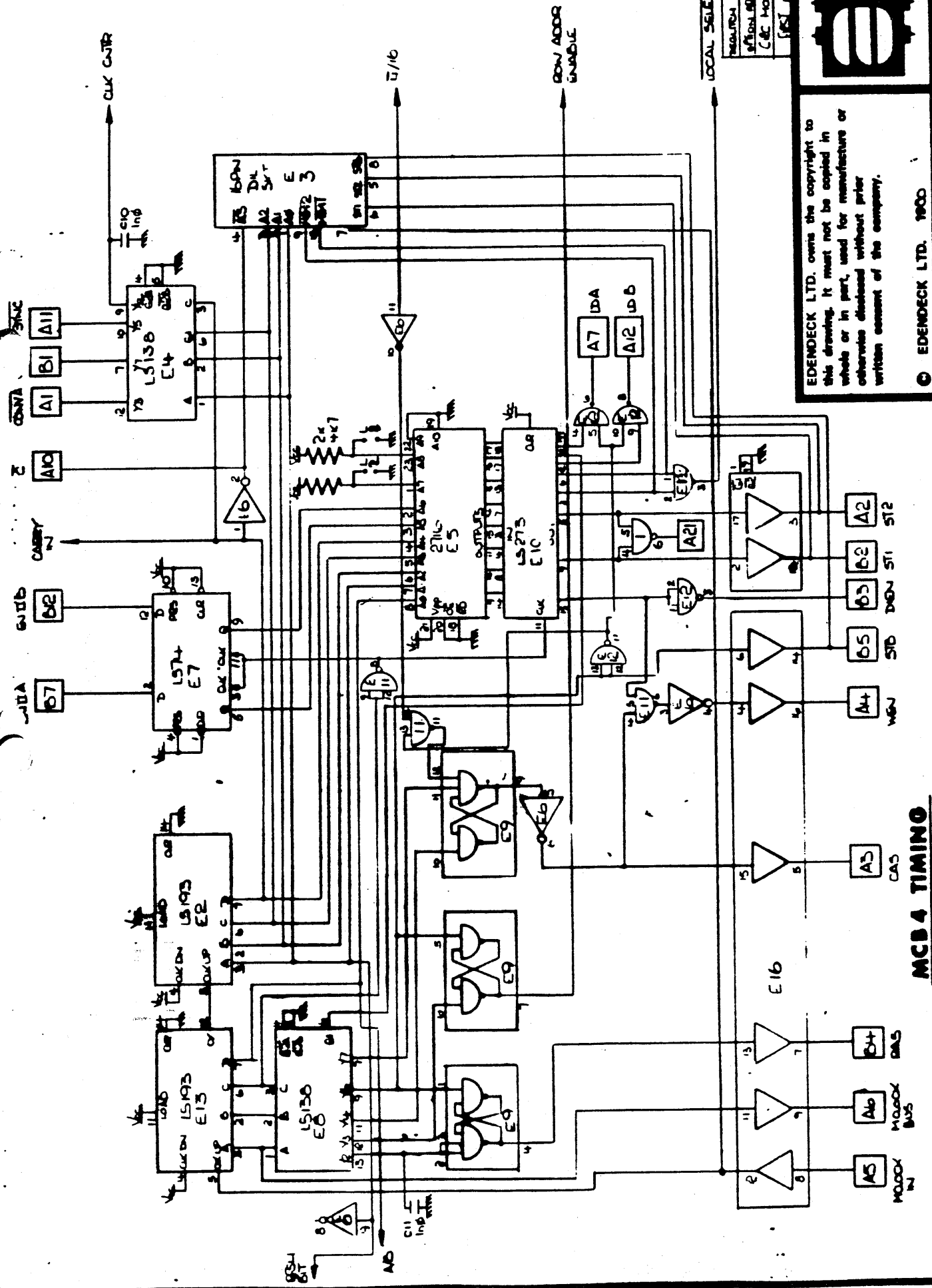
DRAWN DATE
 8 9 82

DRG. No. DWX05.3 SHIT 3



C16 (C17) AD000	7.5.82	3
ROBINA MODS	14.9.82	2
FIRST ISSUE	8.9.82	1

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MCB 4 TIMING

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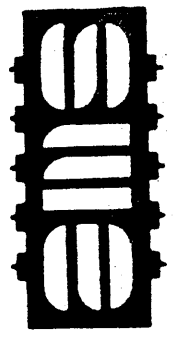
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DRAWN
/15

DATE
15-8-80

DWG. No.
DMX 27.4

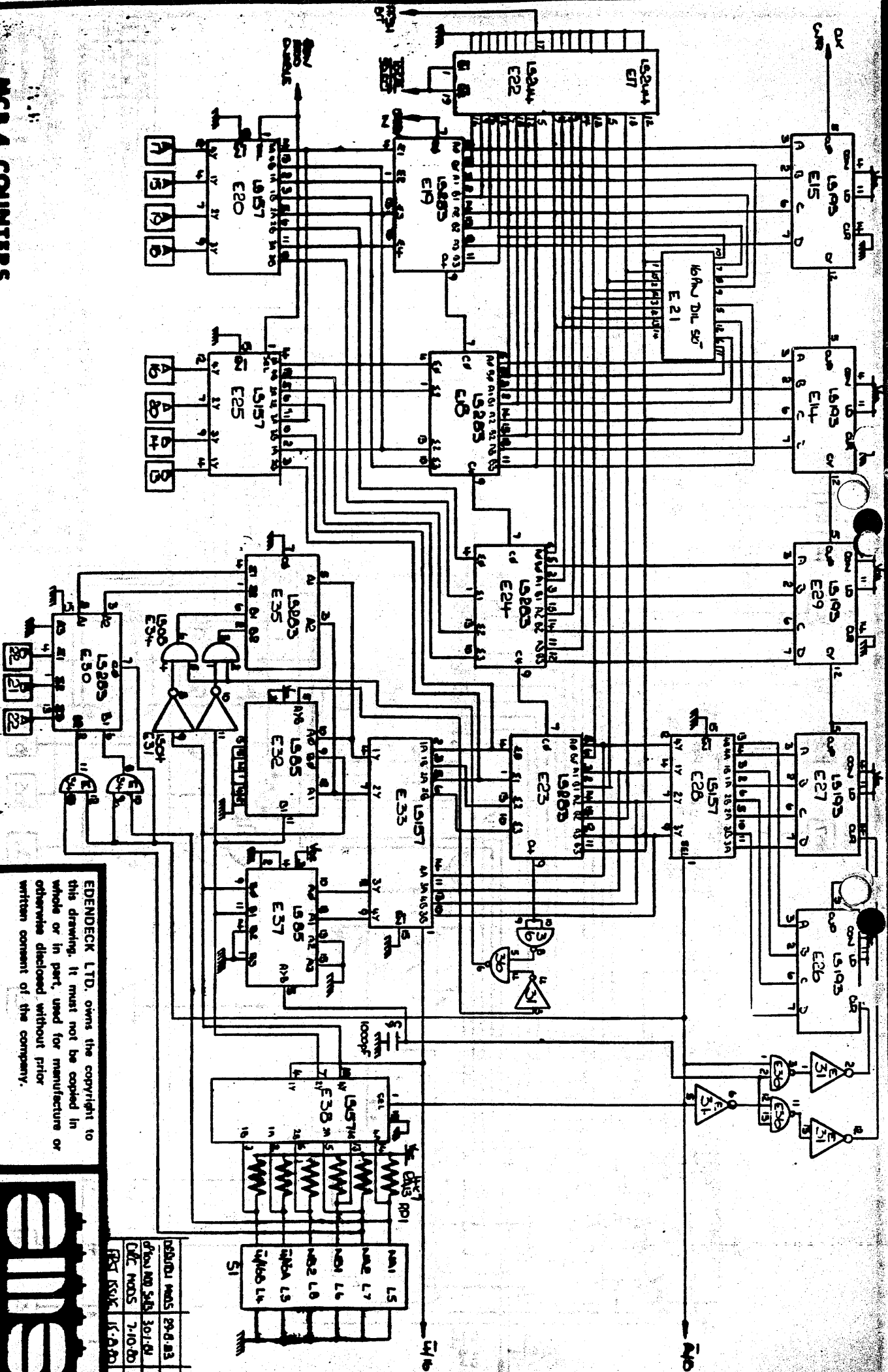
UNIT 2



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MGB 4 COUNTERS



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a.e.60

DMX 27.4



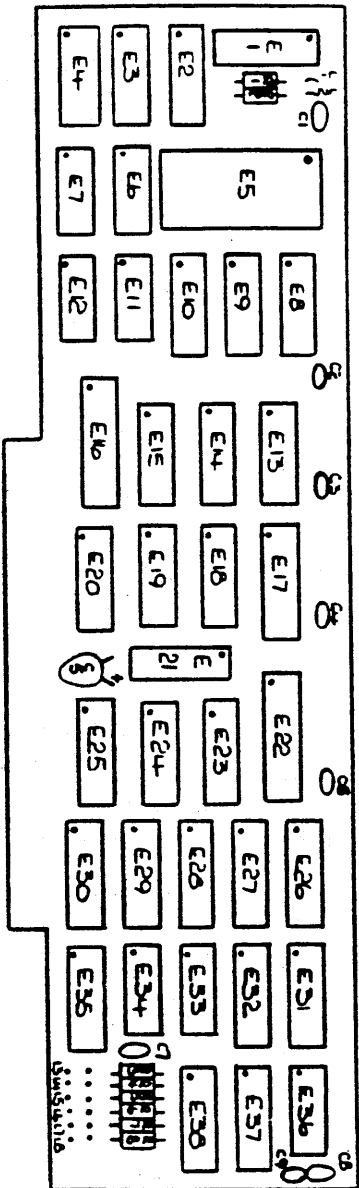
EDENDECK	MOD5	29-0-03	4
EDENDECK	MOD6	30-0-04	3
EDENDECK	MOD7	31-0-05	2
EDENDECK	MOD8	32-0-06	1

advanced music systems, burnley, england.

MEMORY CONTROL BOARD OPTIONS:
 L1 L2
 L2 12 TWO 2 WORKMANS, OUT FOR 1 WORKMAN 12A

MEMORY INCREMENT	Channel A		Channel B	
	L3	L4	L3	L4
102.5 (M15)	IN	IN	IN	IN
108.5 (M15)	OUT	OUT	OUT	OUT

No of MEMORY CARDS	Channel A		Channel B	
	L5	L7	L6	L8
1	IN	IN	IN	IN
2	OUT	IN	OUT	IN
3	IN	OUT	IN	OUT
4	OUT	OUT	OUT	OUT



MCB4

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DRAWN

DATE

18

7.10.80

ENG. No.

DMX 27.3

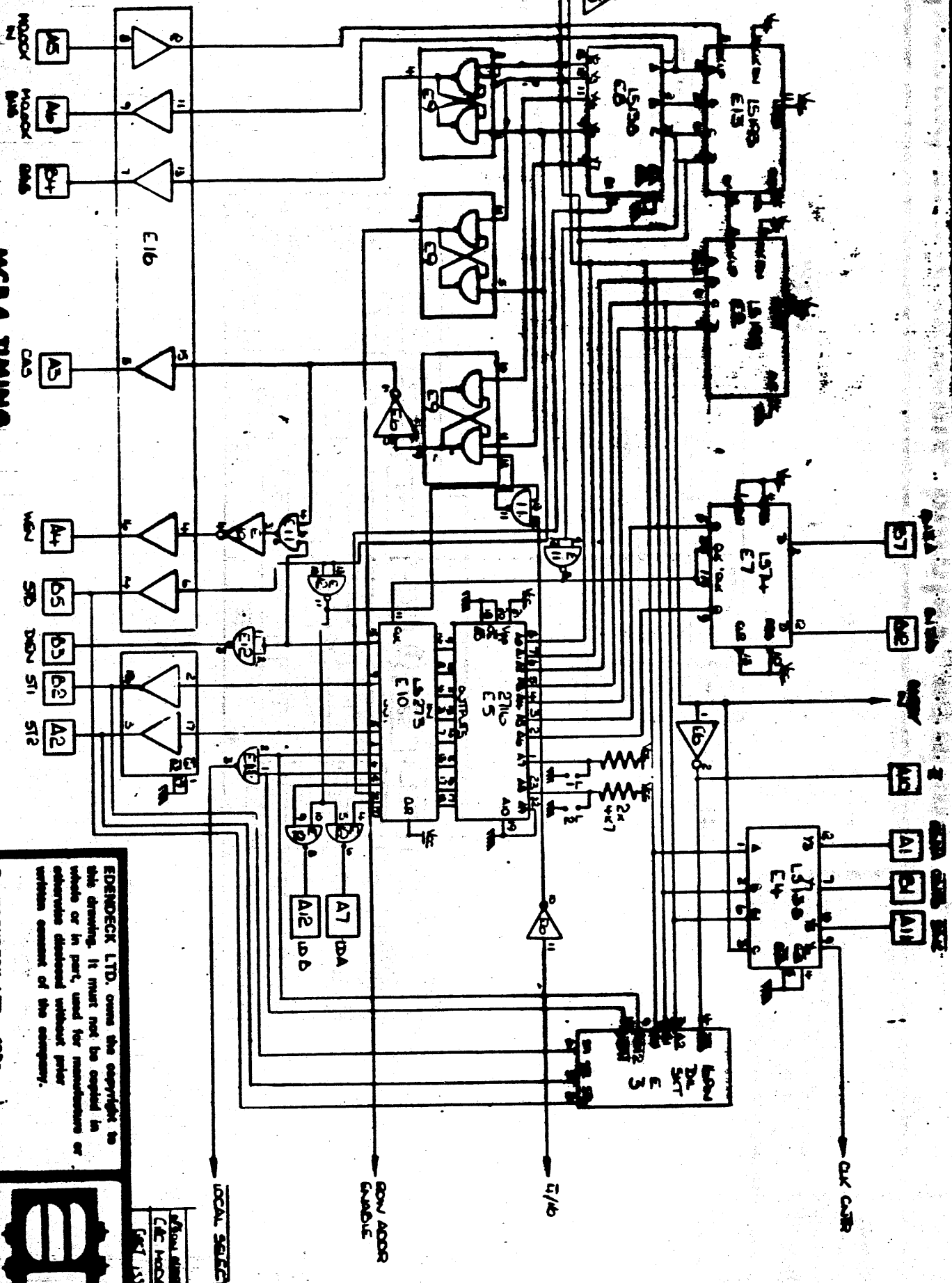
SWT 3



OPTION	TRADE MARK	30-1-81	3
WIDE & CLE CHASE	1000	2	
FOR 155 E	15400	1	

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MCD 4 TIMING



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FROM STOCK	20/1/80	3
C&C MCD	7/10/80	2
GET ISSC	15/1/81	1

LOCAL SELECT

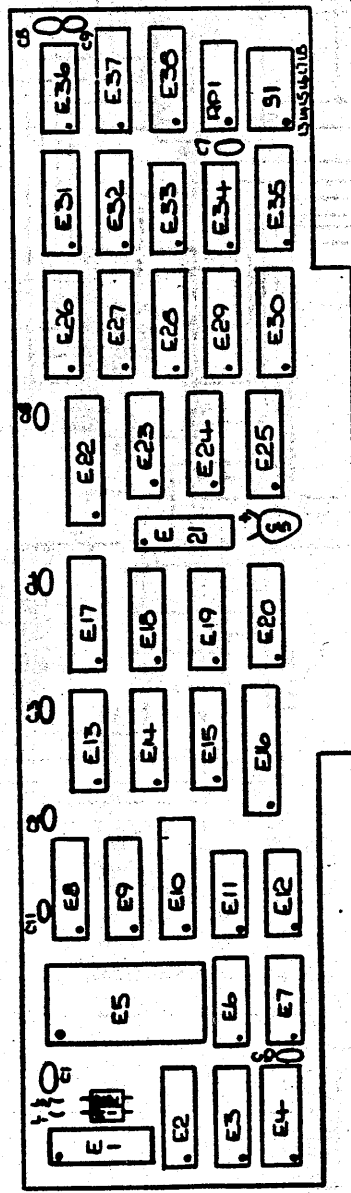
ROW ADDR CABLE

U/40

CX CDR

DAVEN

DMX 27.5 WIT 2



MEMORY CONTROL BOARD OPTION

- L1 1W
- L2 1W FOR 2 MONITORS, OUT FOR 1 MONITOR

MEMORY	CHANNEL A	CHANNEL B
INCREMENT	L3	L4
102-3 (UNIT)	IN	IN
102-3 (UNIT)	OUT	OUT

NO OF MEMORY CARDS	CHANNEL A		CHANNEL B	
	L5	L7	L6	L8
1	IN	IN	IN	IN
2	OUT	IN	OUT	IN
3	IN	OUT	IN	OUT
4	OUT	OUT	OUT	OUT

MCB 4

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DWG. No.

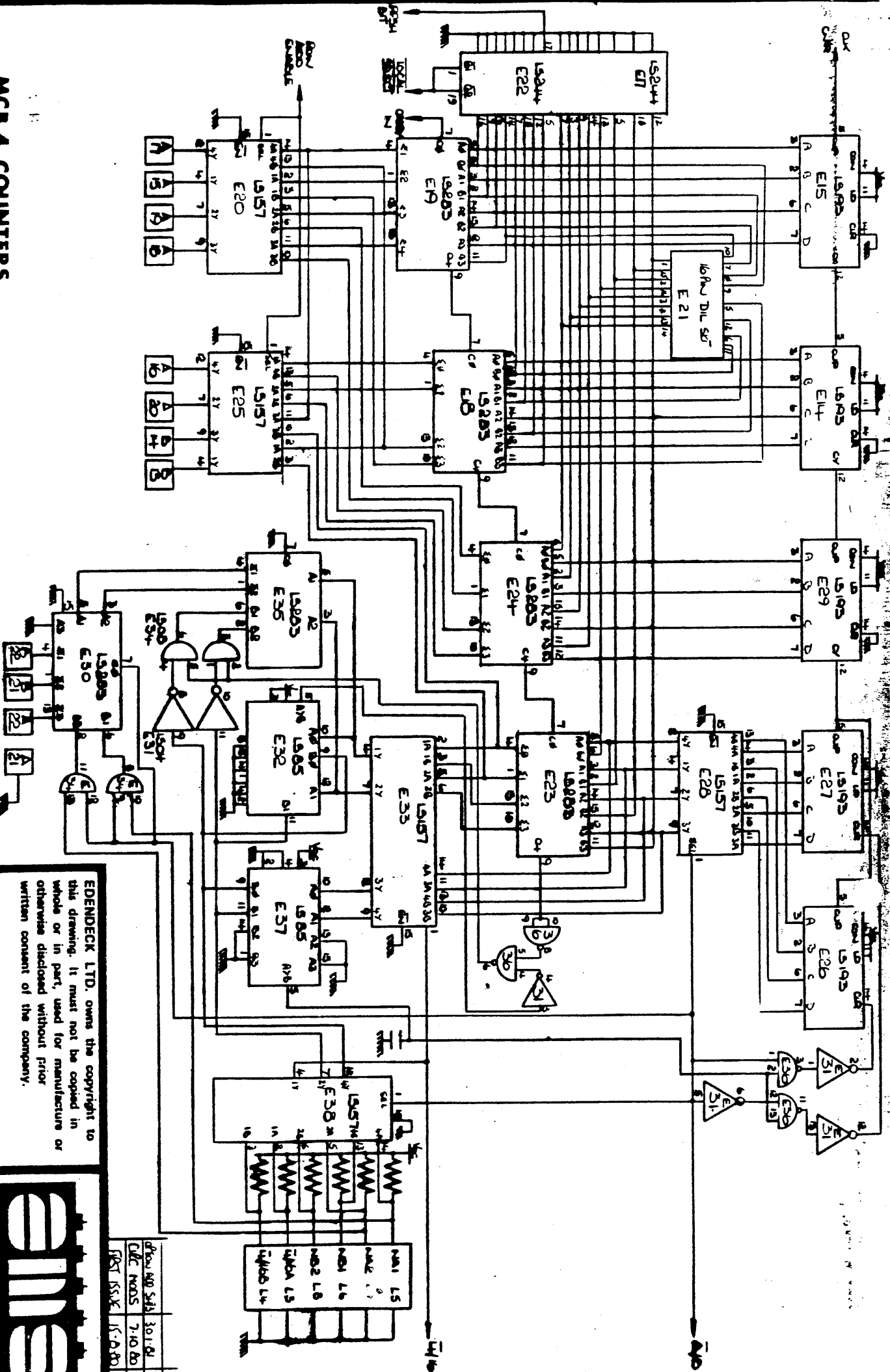
DMX 27.14

SMT3

DESIGNED	PROS	29.8.83	4
OPTION TABLE	AVR20	30.1.81	3
DATE	CH. CHANGE	1.10.80	2
FILE	ISSUE	15.8.80	1



MGB 4 COUNTERS



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DESIGN

DATE

13.8.80

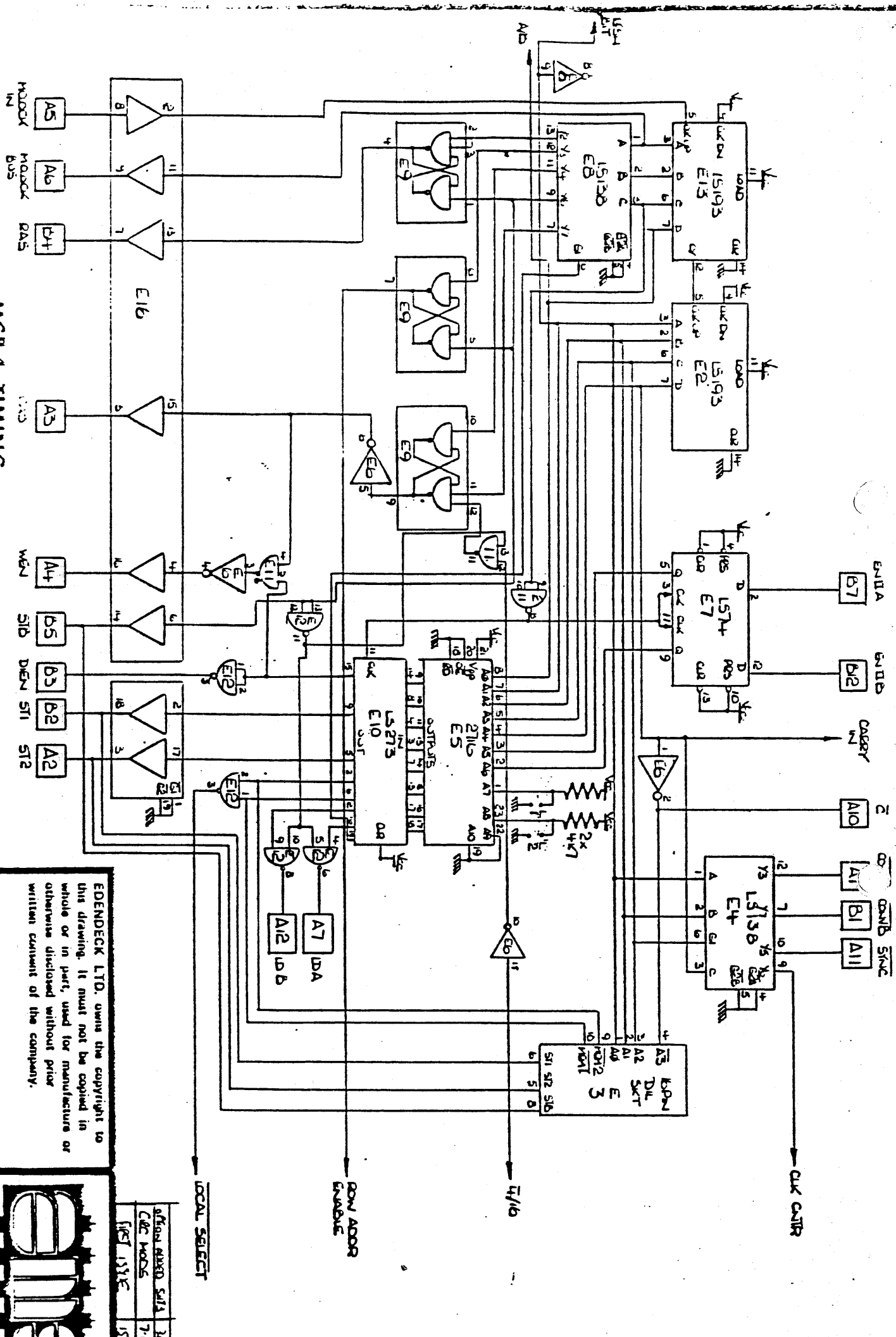
DES. No.

DMX 27.3

SWT 1



Drawn	13.8.80	1
Check	13.8.80	2
Test	13.8.80	3



MCB 4 TIMING

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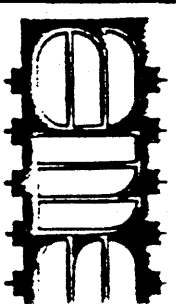
DATE

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15

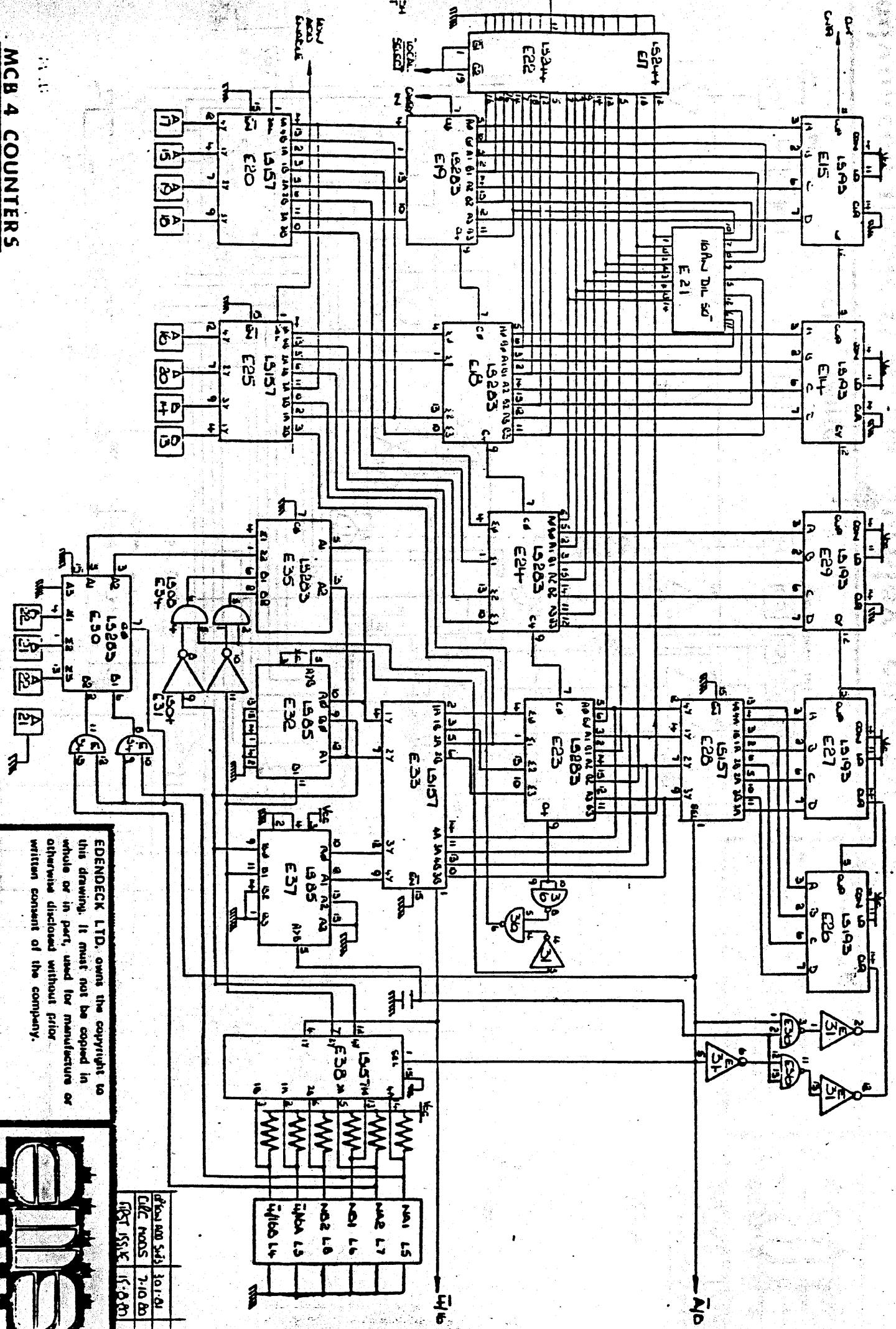
15.8.80

DRG. No. DMX 27.2



OFFICE	15.8.80
DESIGN	15.8.80
TEST	15.8.80
PROD	15.8.80

MCB 4 COUNTERS



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13-8-60

DRG. No.
DMX 2

SHY



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2

• 1944 •

DMX 16.3

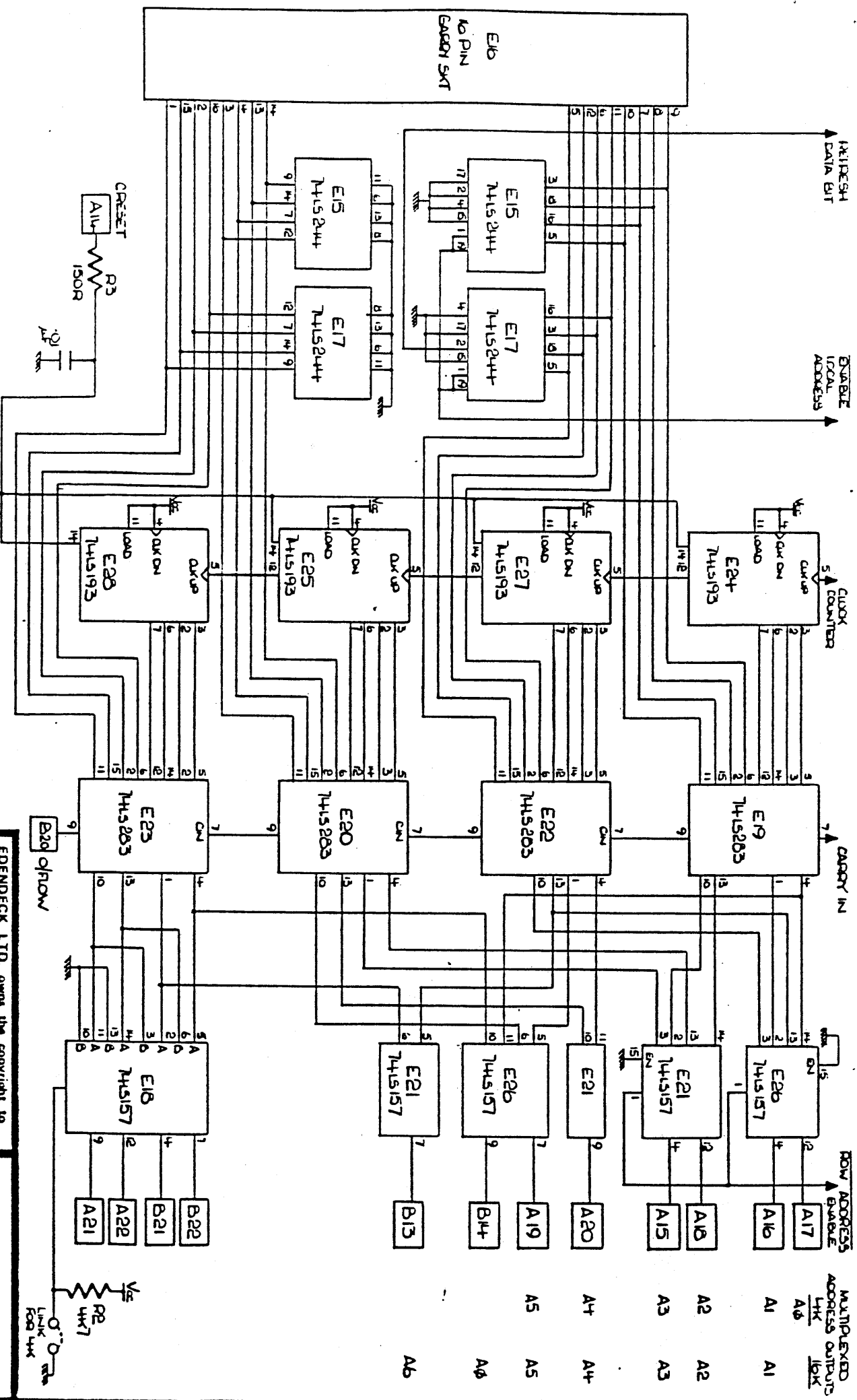
• 1944 •



215

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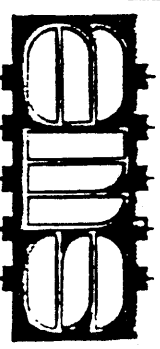
[illegible]



MEMORY CONTROL BOARD 2

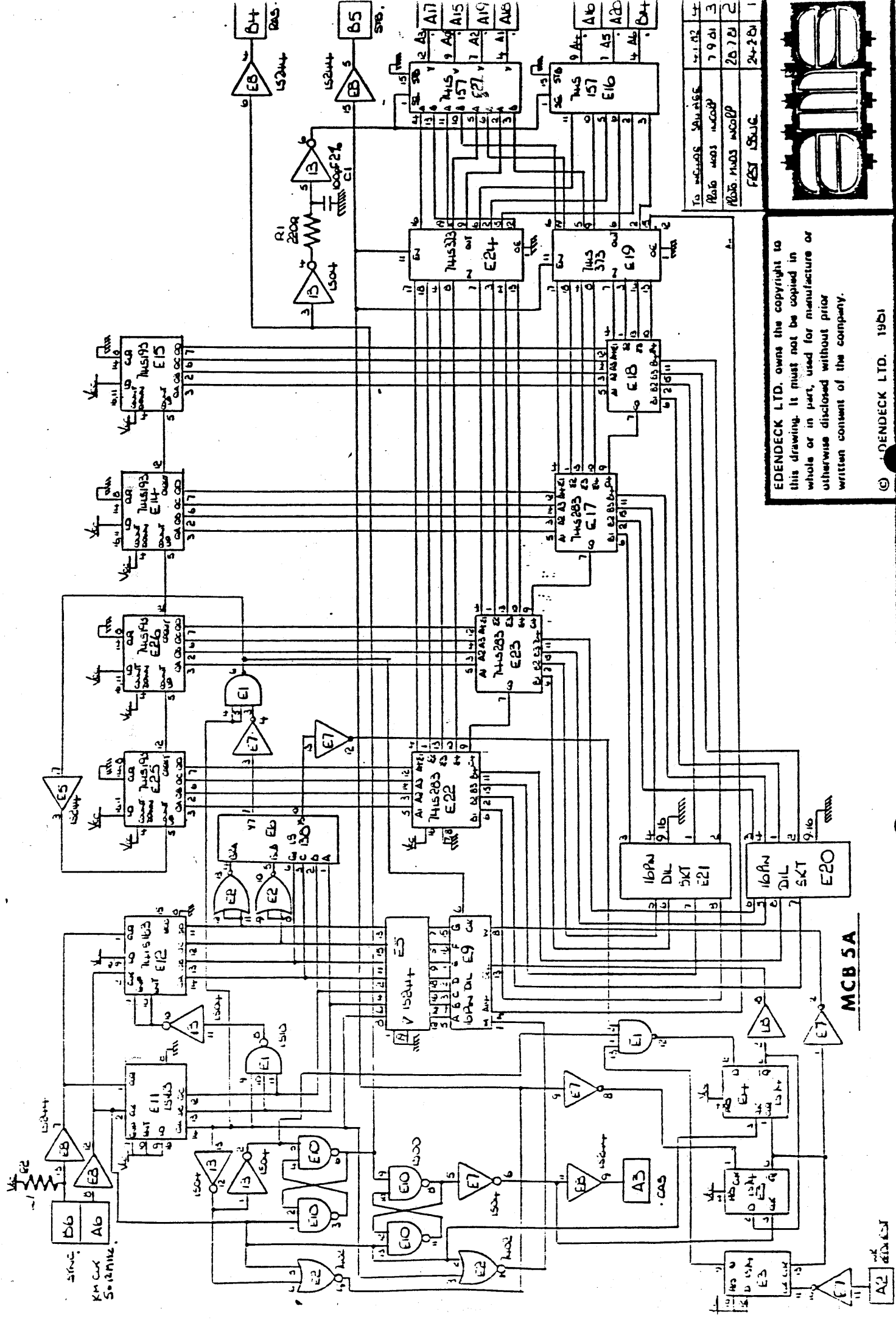
DATE	REV	ISSUED
23.5.80	1	2.10.79

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DATE 2.10.79
 DRG. No. DMX11.4
 SHT 1



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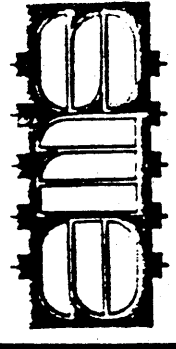
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DATE

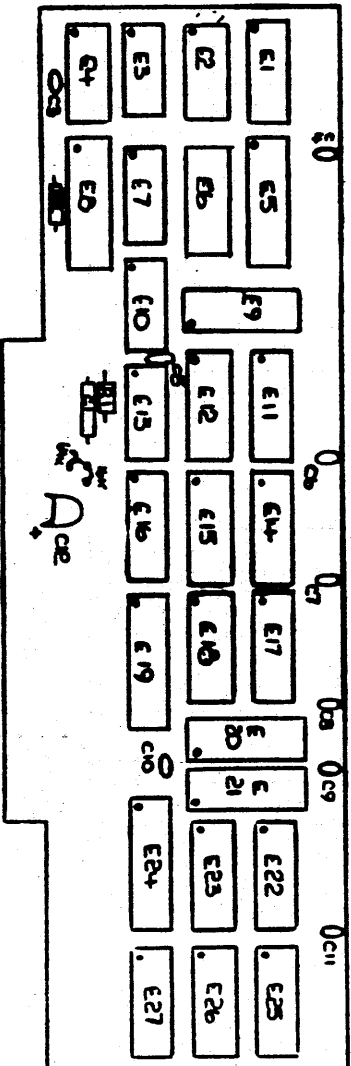
ORG. No.

1 MAY 30 4 1961

Advanced micro systems Curlew, enland.



TO: MCDOS	500000	4
FROM: MCDOS	500000	3
FROM: MCDOS	500000	2
FROM: MCDOS	500000	1



MCD 5A

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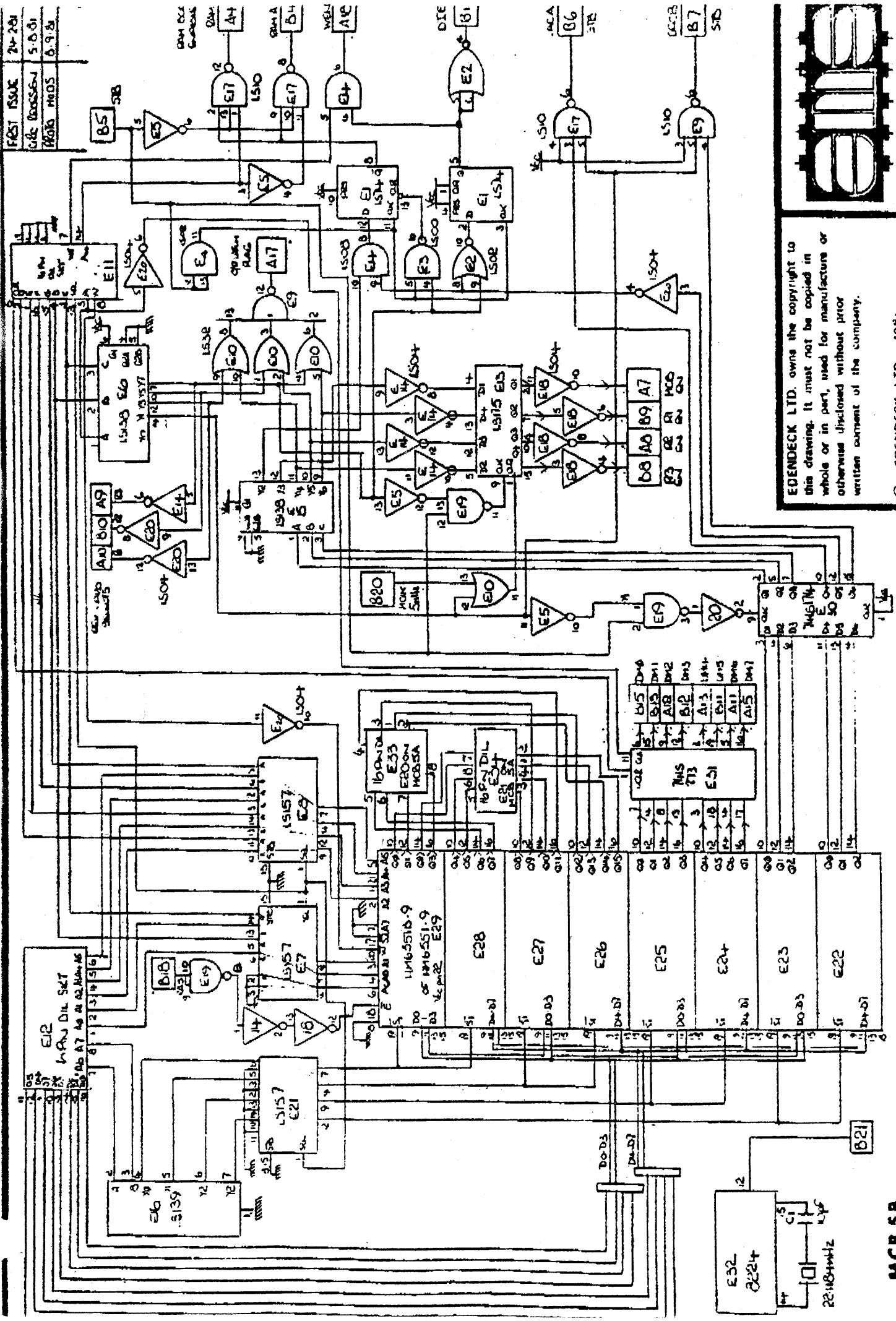
DAWN
K
0101




BRILLIANT OFFICE	1.6-03	51
E. MOUNT. MOUNT.	4.1.02	4
RE. MOUNT. C. MOUNT.	7.9.01	3
RE. MOUNT. MOUNT.	20.7.01	2
RE. MOUNT. MOUNT.	17.3.01	1

DAWN
DMX 91.2
SWITZ

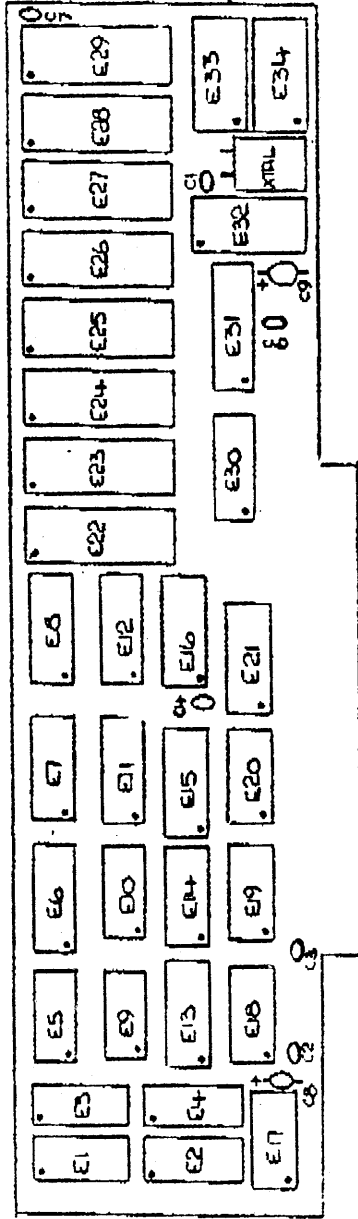
FIRST ISSUE	24.2.61
DATE REVISION	5.6.61
REVISION	0.9.61





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MCB 5B

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DATE

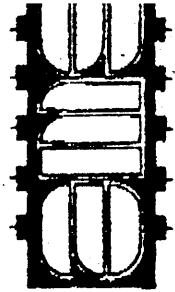
27.5.01

DRG. No.

DMX 33.3

SHT

RELAT/PE MODS	8 9 01
CHK REVISION	5 8 01
TEST DATE	27.5.01

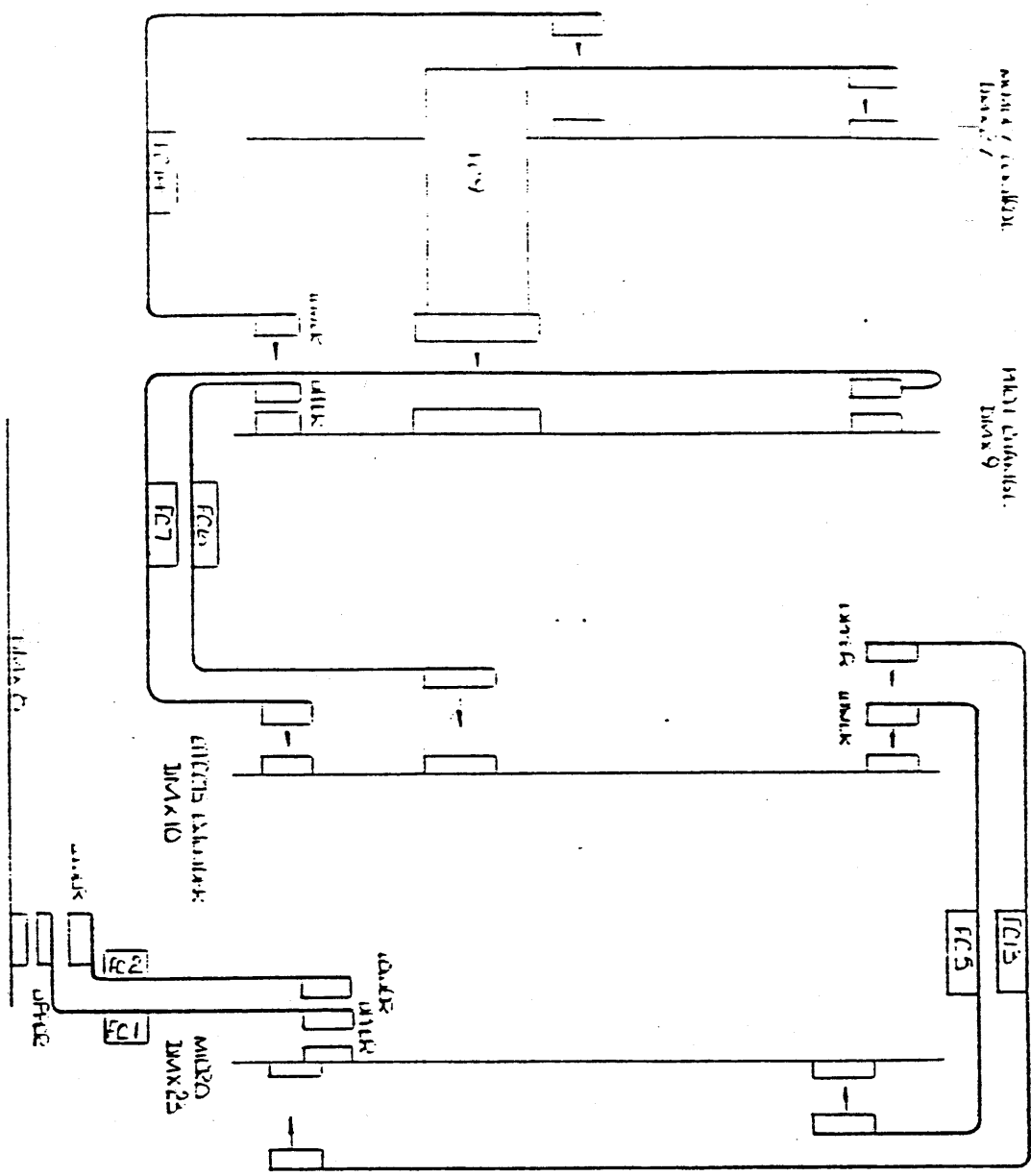


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ARMED / Unarmed
Block 7

ARMED / Unarmed
Block 9



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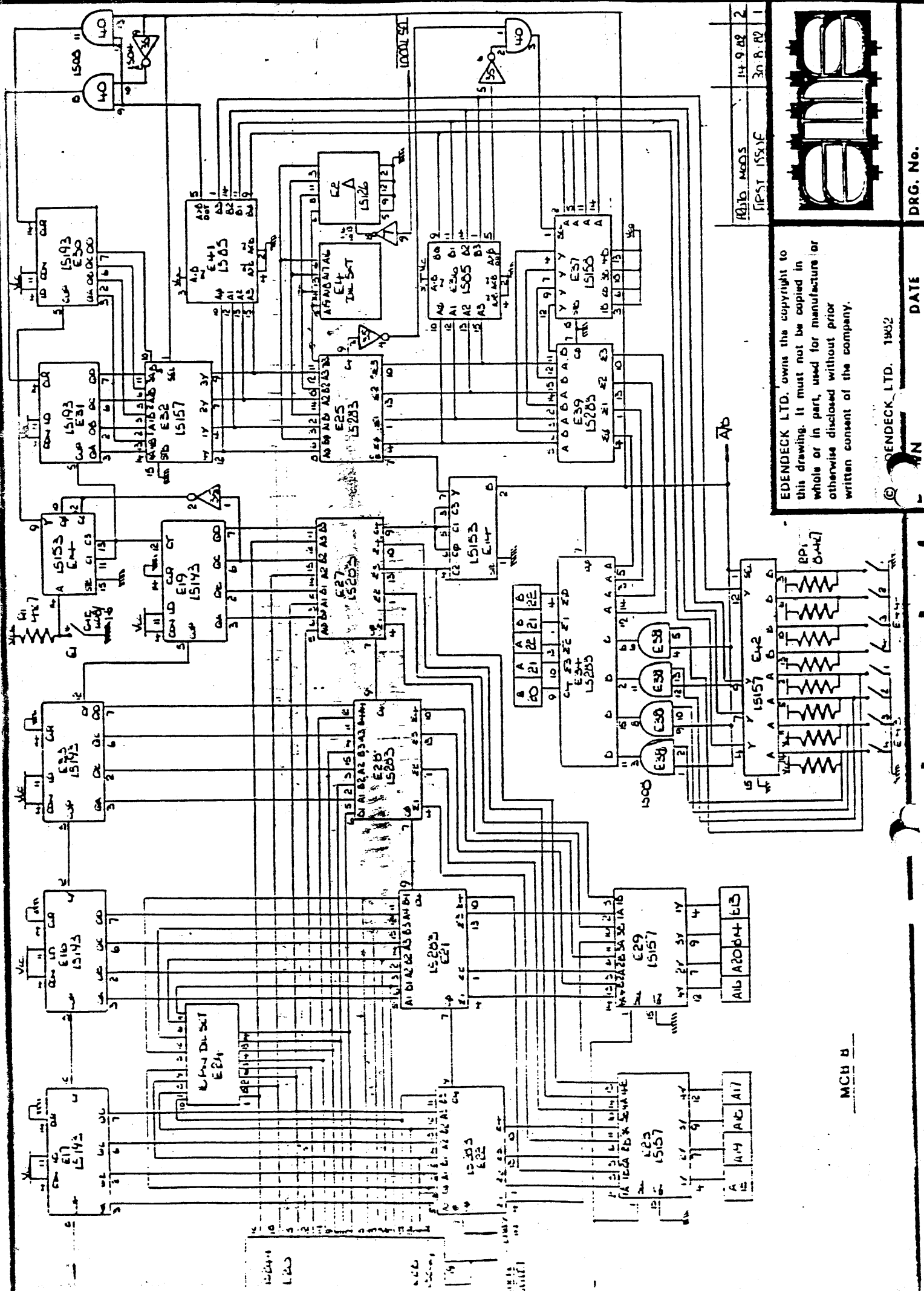
DATE

DRG. No.

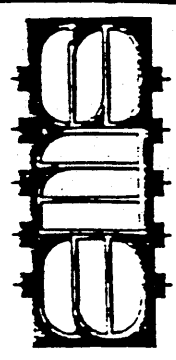
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DATE

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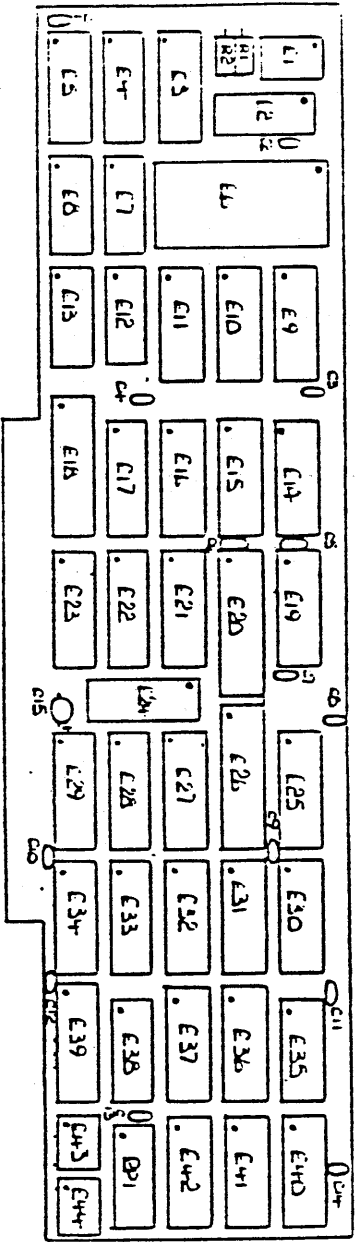


DRG. No. DATE

1010	MODS	14 9 82	2
1010	15516	30 8 82	1

MCH B

LI 1003 (UNLESS FOR 2 FROM CATHODE
DRAWN FOR 1 FROM CATHODE



MCB8

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DATE

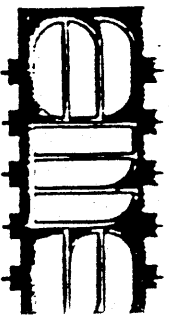
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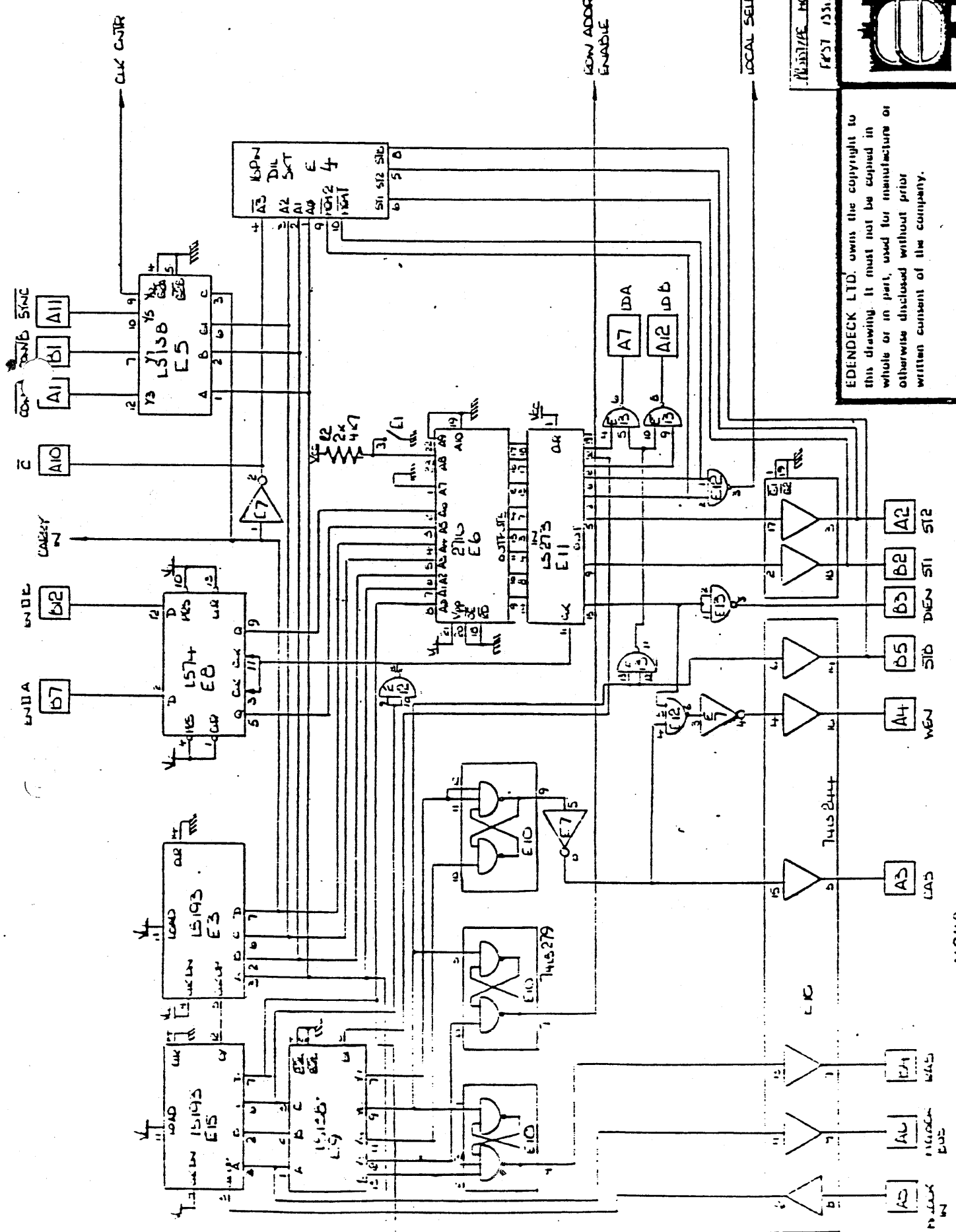
18.9.62

DMX65.2

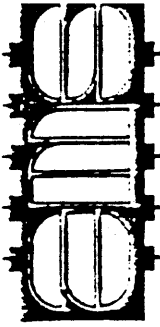
SHT 1

MOBILE 1003
11011 1501E
A.9.62





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1/10/77/FE	HOOS	4-9-82
1/10/77/FE	HOOS	30-0-82

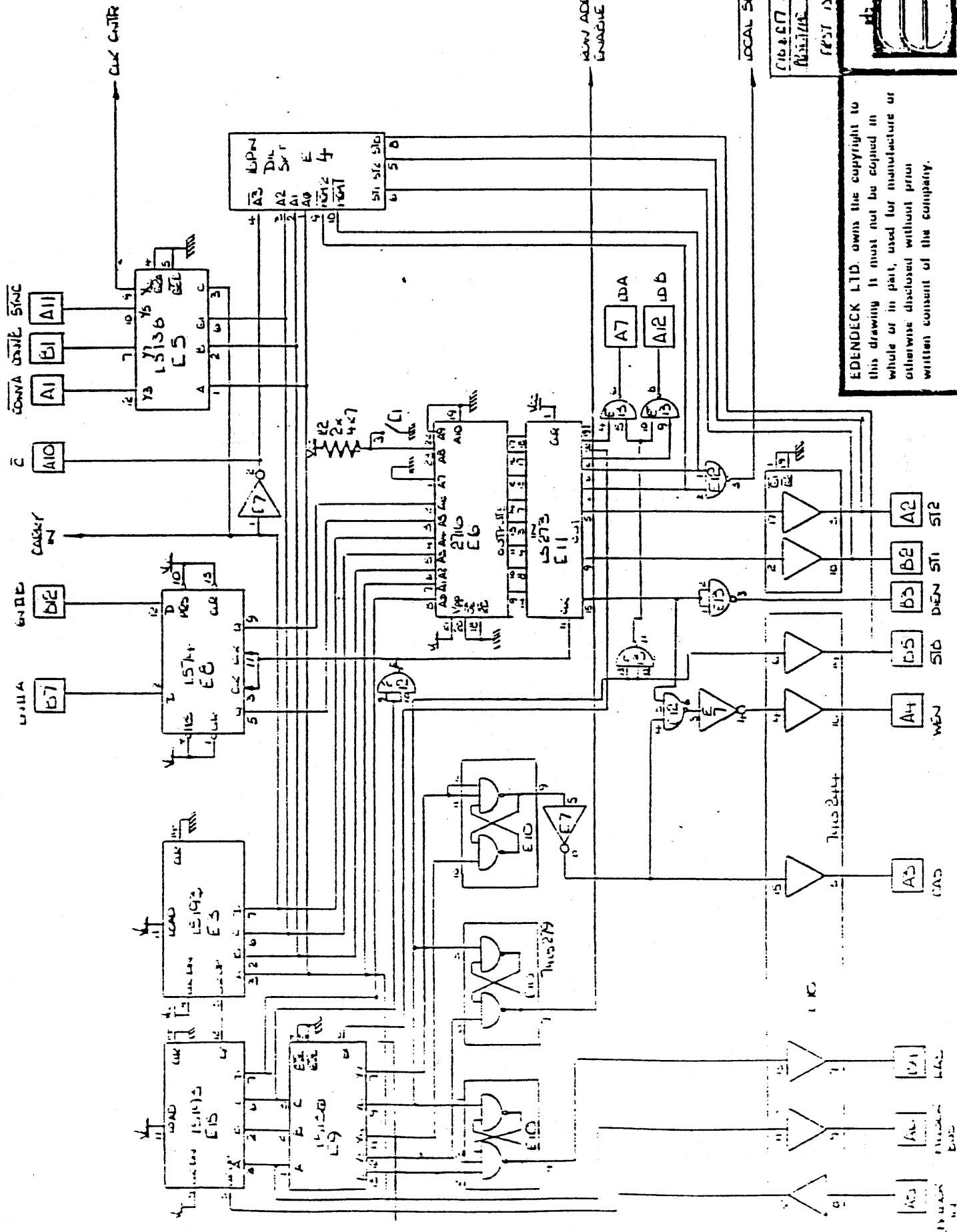
MCBB

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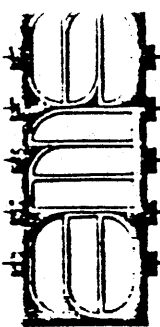
DRG. No.

DATE



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FILE E17	REVISED	1 10 82
REVISION	MODS	14 9 84
TEST	ISSUE	20 0 82



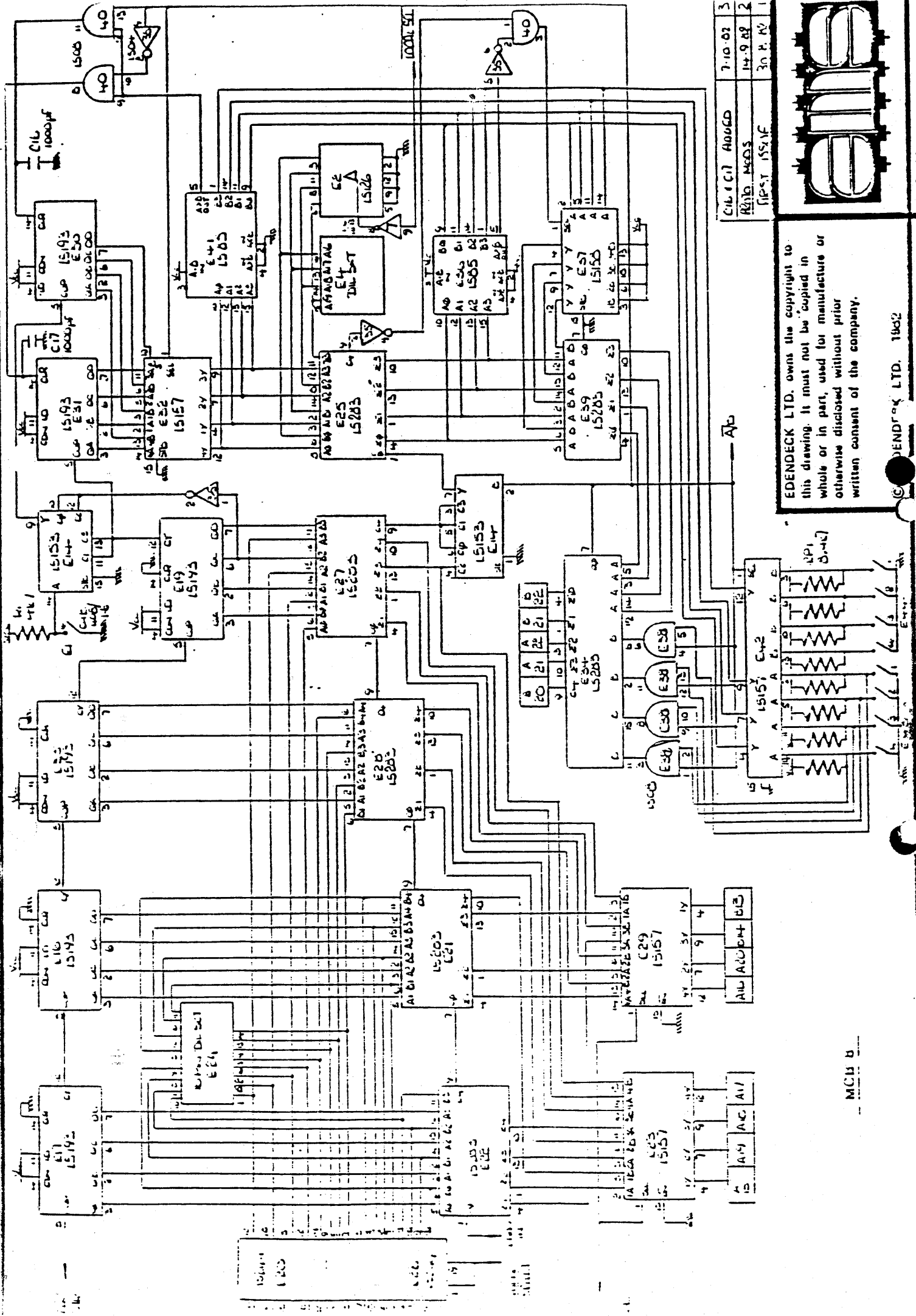
MCB

EDENDECK LTD. 1982

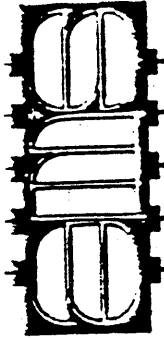
DRG. No.

DATE

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CIL (C1) AUGCD	7-10-02	3
REVIS MECS	14-9-08	2
REVIS ISSUE	30-8-02	1

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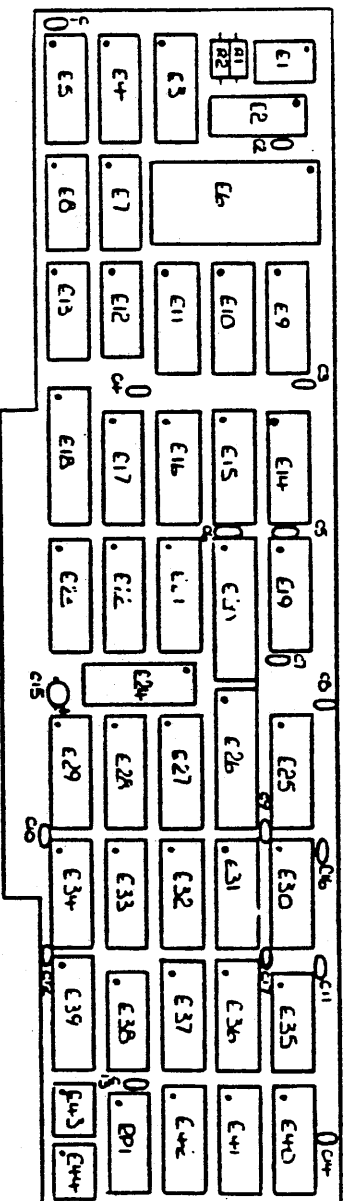
DRG. No.

DATE

100 / N

MCB B

E1 INC 3 CLOSING IN - FROM CHANGE
 SHOULD FOR 1 FROM CHANGE



MCB8

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DRAWN

DATE

18

8 9 82

DRG. No.

DMX88.3

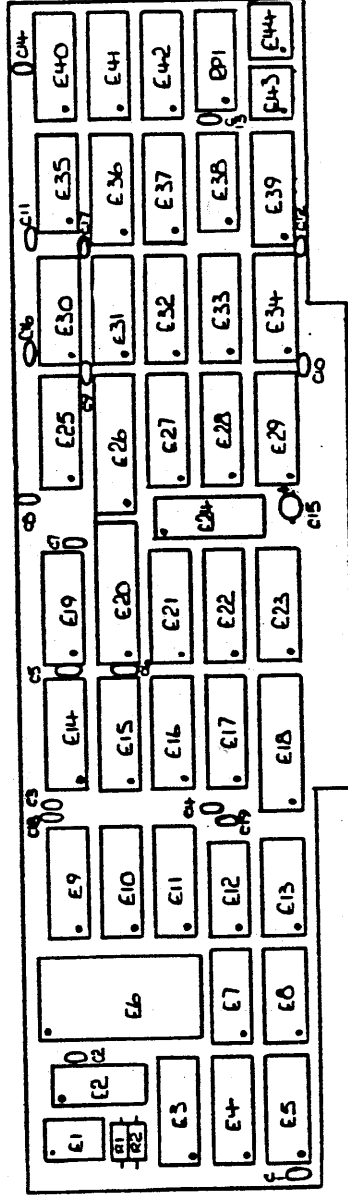
SWT 3

C16 (C17) 400G	7.3.82	3
ROSTER 1005	14.4.82	2
POST ISSUE	16.9.82	1



advanced music systems, burnley, england.

E1 POS 3 CLOSED FOR 2 PITCH CHANGE
 OPENED FOR 1 PITCH CHANGE



MCBS

DR29	6.9.82	4
C16 + C17 ADDS	7.10.82	3
MODS	14.9.82	2
FIRST ISSUE	18.9.82	1

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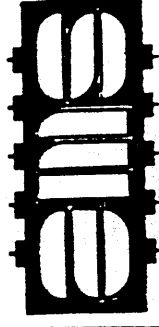
DRAWN

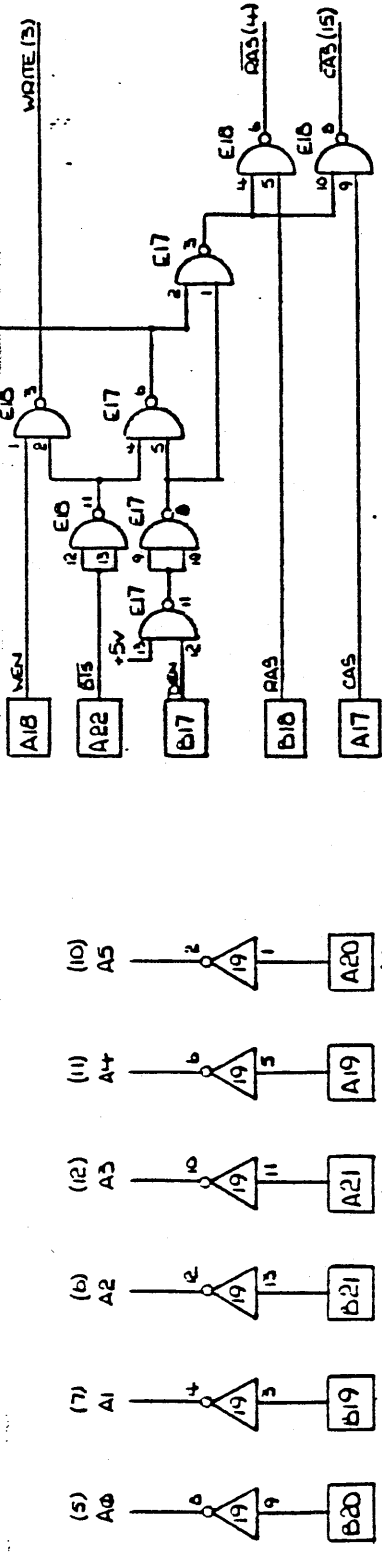
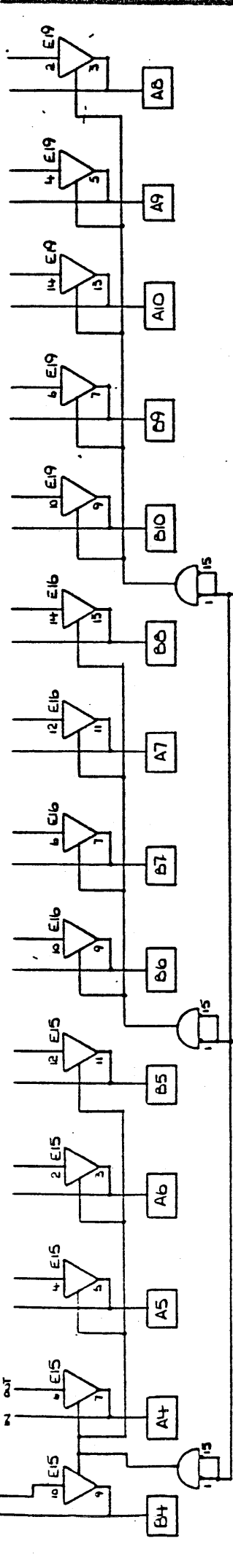
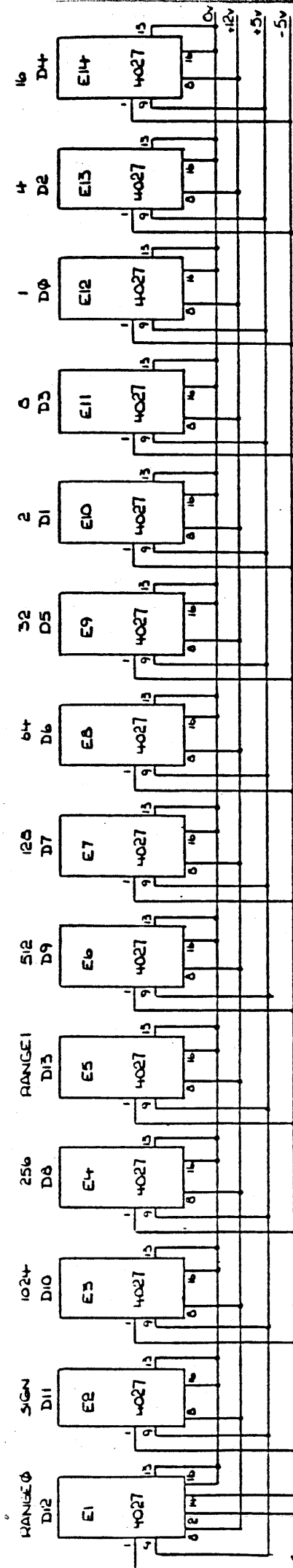
DATE

DRG. No. DMX68-4

SHT 3

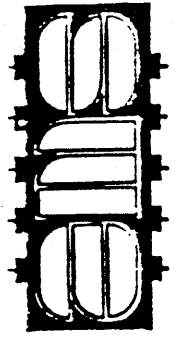
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EST 155 DE 3-10-79 1

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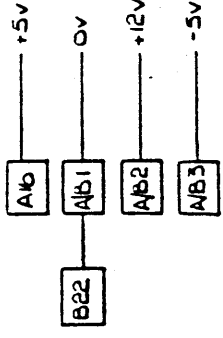


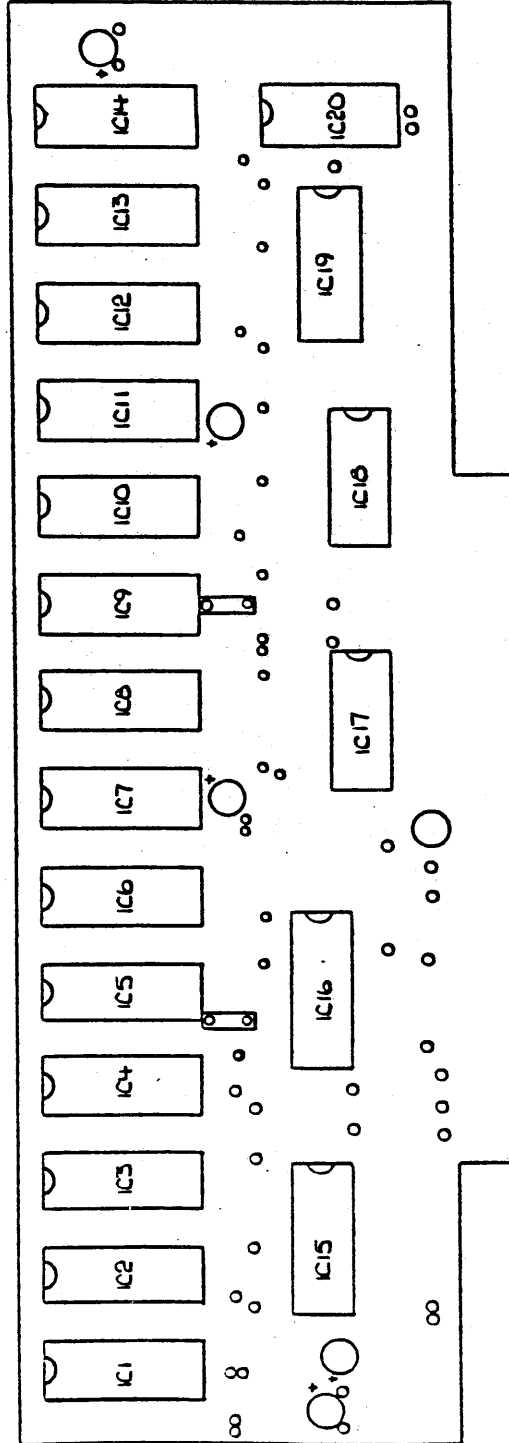
RAM CARD

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DRG. No.

DATE





RAM BOARD

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DATE

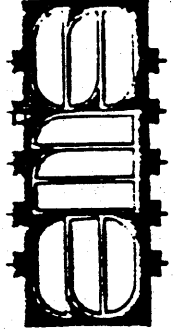
DRG. No.

Advanced micro systems K. rnley. enland.

REV. ISSUE

3-10-79

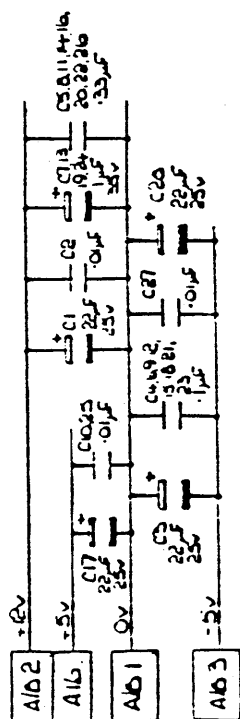
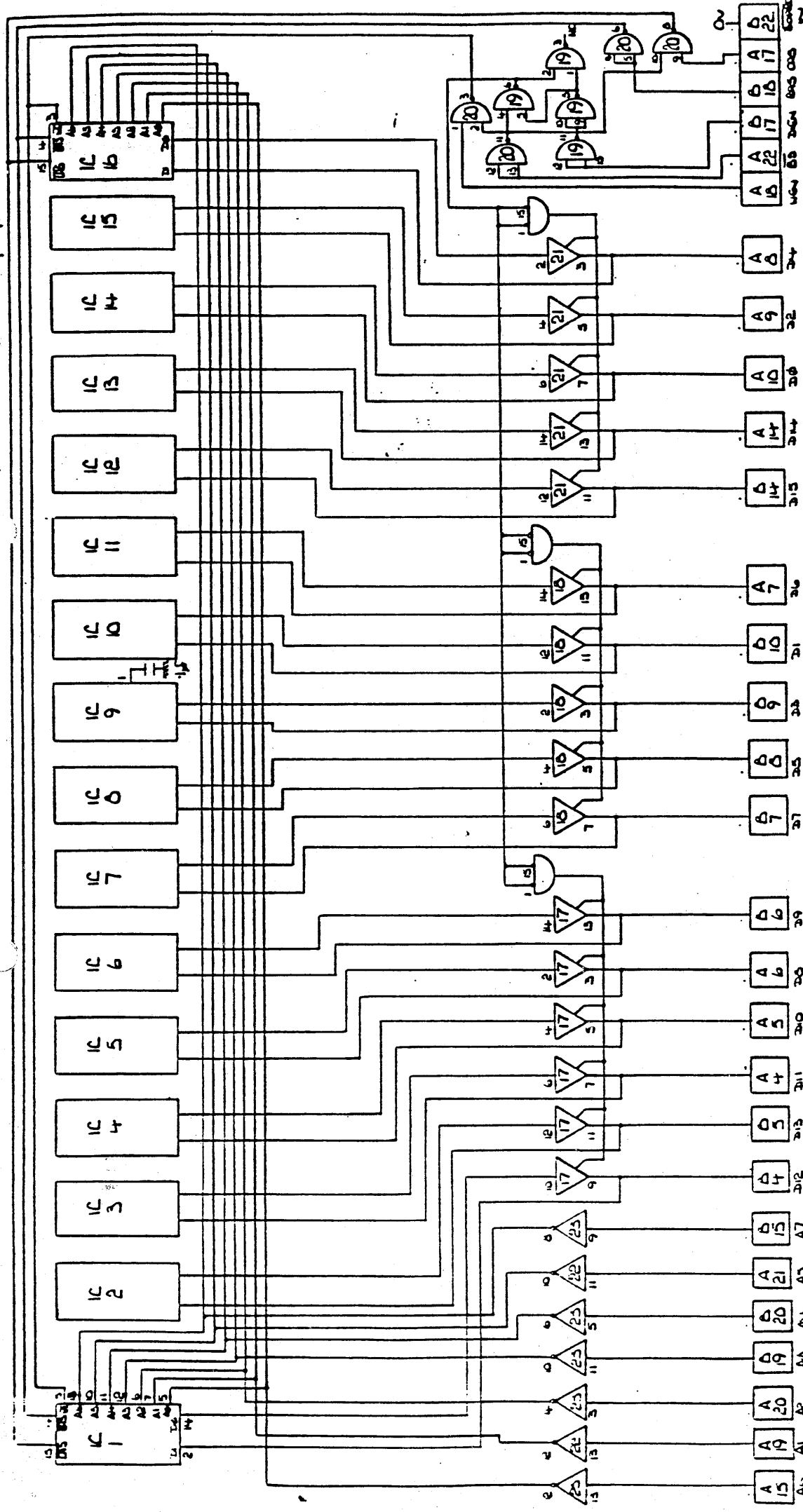
1



DATE

DRG. No.

DATE



RAM 2 BOARD

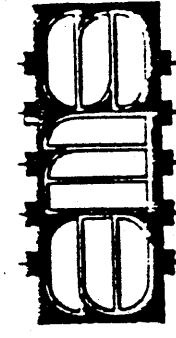
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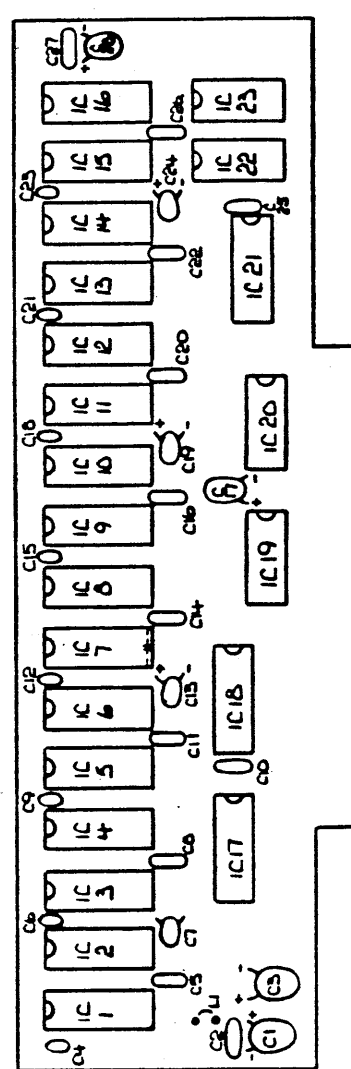
DATE 10.11.80

DRG. No. DMX13.3

SHT 1



POST ISSUE	1
WBSA MODS	2 5 00
WBSA SAVINGS	12 10 81
DATE of DMS	23 2 83



NOTE

1. FOR 2110 DMS - NO MODS & 041T C1, 2, 3, 4, 6, 9, 12, 15, 18, 21, 23, 27, 28
2. FOR 4116 DMS - WIRE LINK (L1) & C01

TRACE CONNECTING PINS 19 OF IC7
ON SOLDER SIDE

RAM 2 BOARD

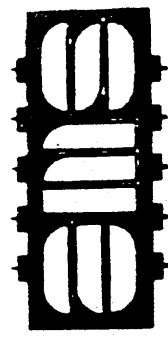
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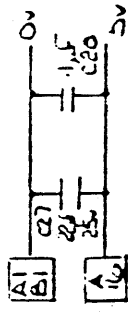
DRAWN DATE 10-4-80

PROG. No.

DMX13.4 SHT 2



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RAM 3 BOARD

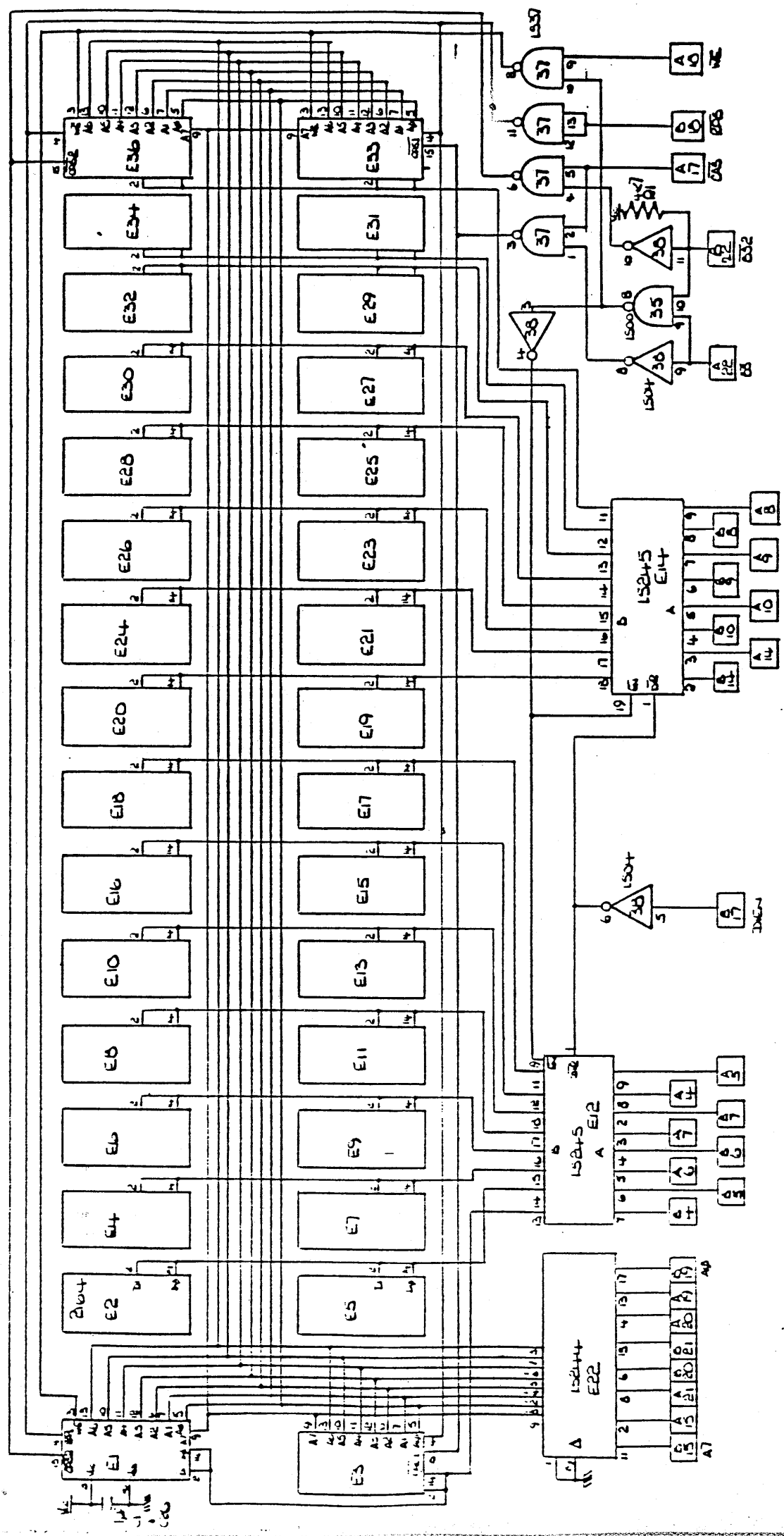
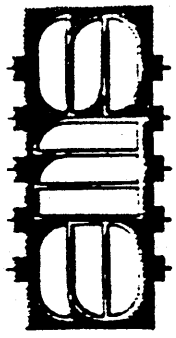
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Rev	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100

DRAWN

DATE

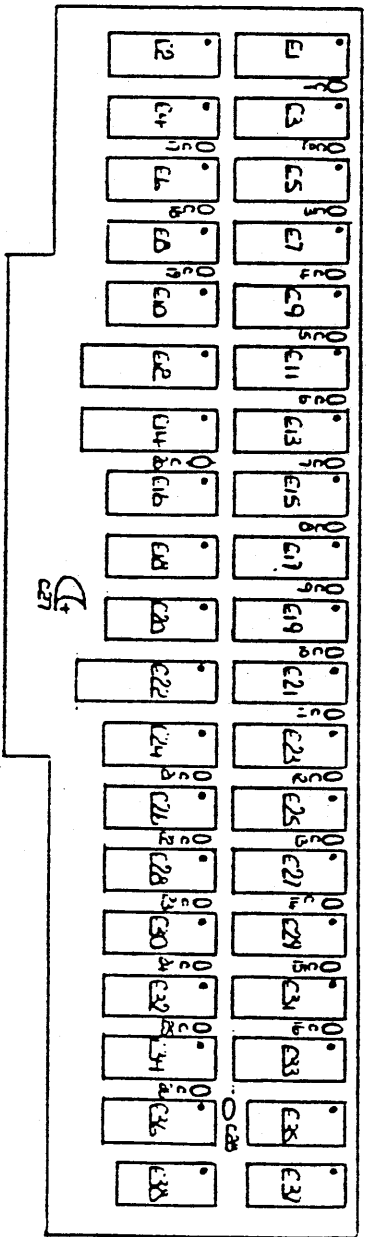
DRG. No.

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RAM 3 BOARD

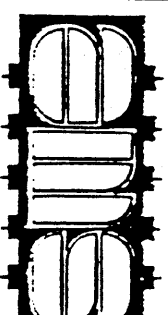


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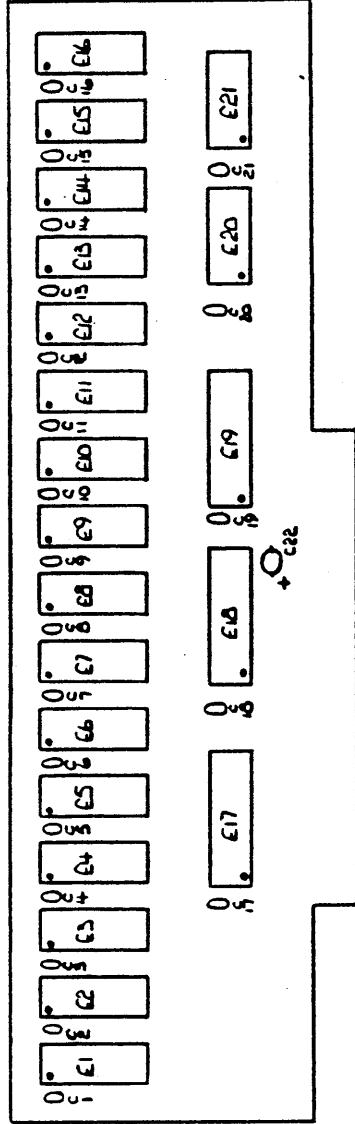
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DL JN DATE

DRG. No.



UNIT A 447	27.9.82
OWN 022 was A12	8-2-82
REPT ISSUED	9.10.81



RAM 4

FIRST ISSUE	30.11.82
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DRAWN

DATE

30.11.82

DRG. No.

DAMEA.1

SHT 2

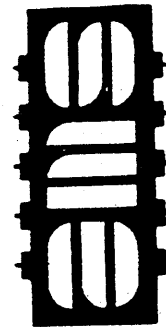
advanced music systems, burnley, england.

E1	E6	E11	E16	E21	E26	E31	E36	E41	E46
E2	E7	E12	E17	E22	E27	E32	E37	E42	E47
E3	E8	E13	E18	E23	E28	E33	E38	E43	E48
E4	E9	E14	E19	E24	E29	E34	E39	E44	E49
E5	E10	E15	E20	E25	E30	E35	E40	E45	E50

DEGLITCH CARD

ROTARY MODS	6602	2
FIRST ISSUE	28-11-82	1

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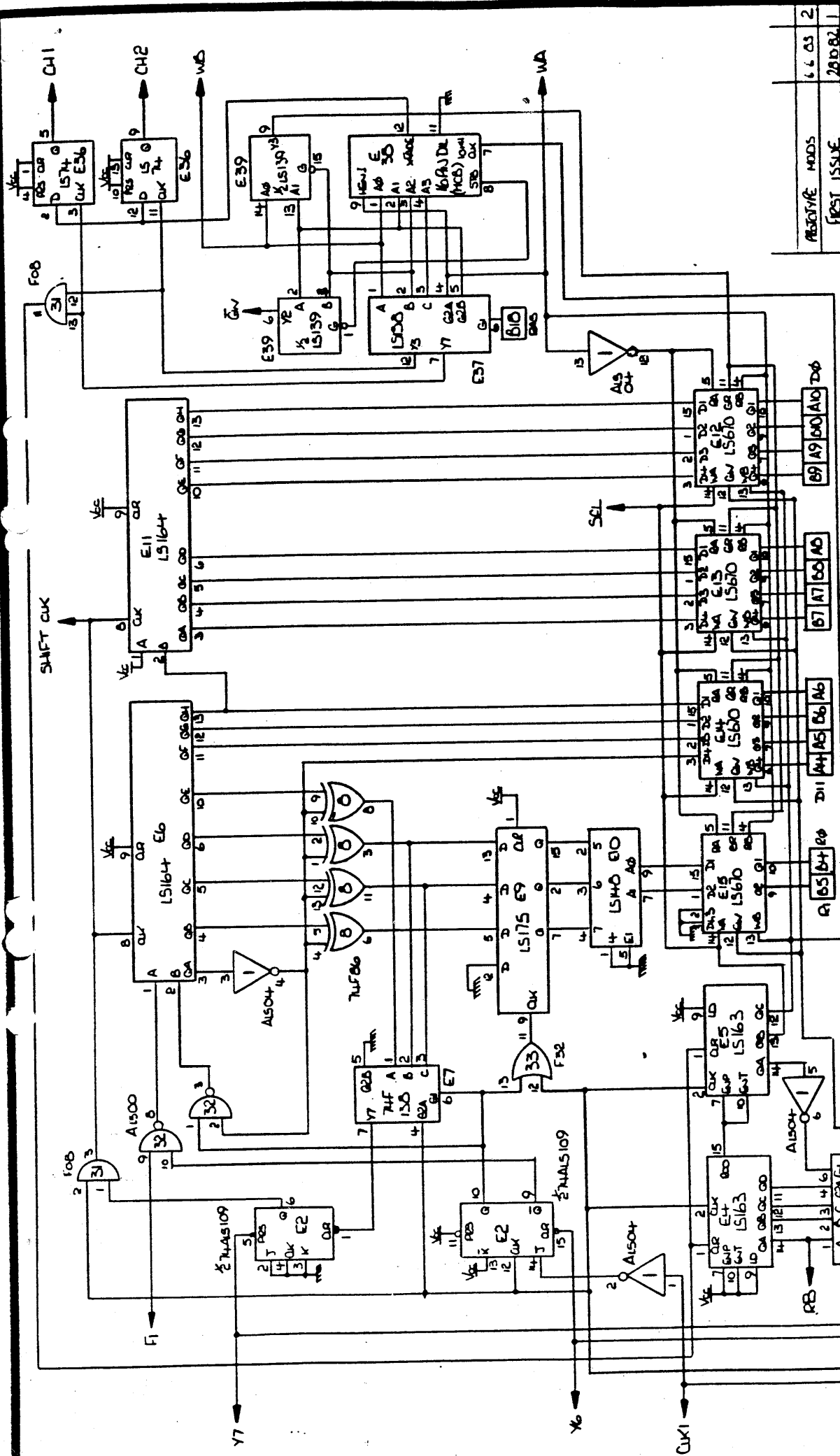
1/2

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DWG. No.

2

2078



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REV	DATE	BY
1	1982	2008

DEGLITCH CARD

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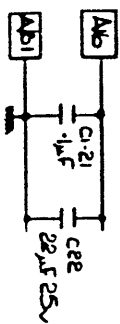
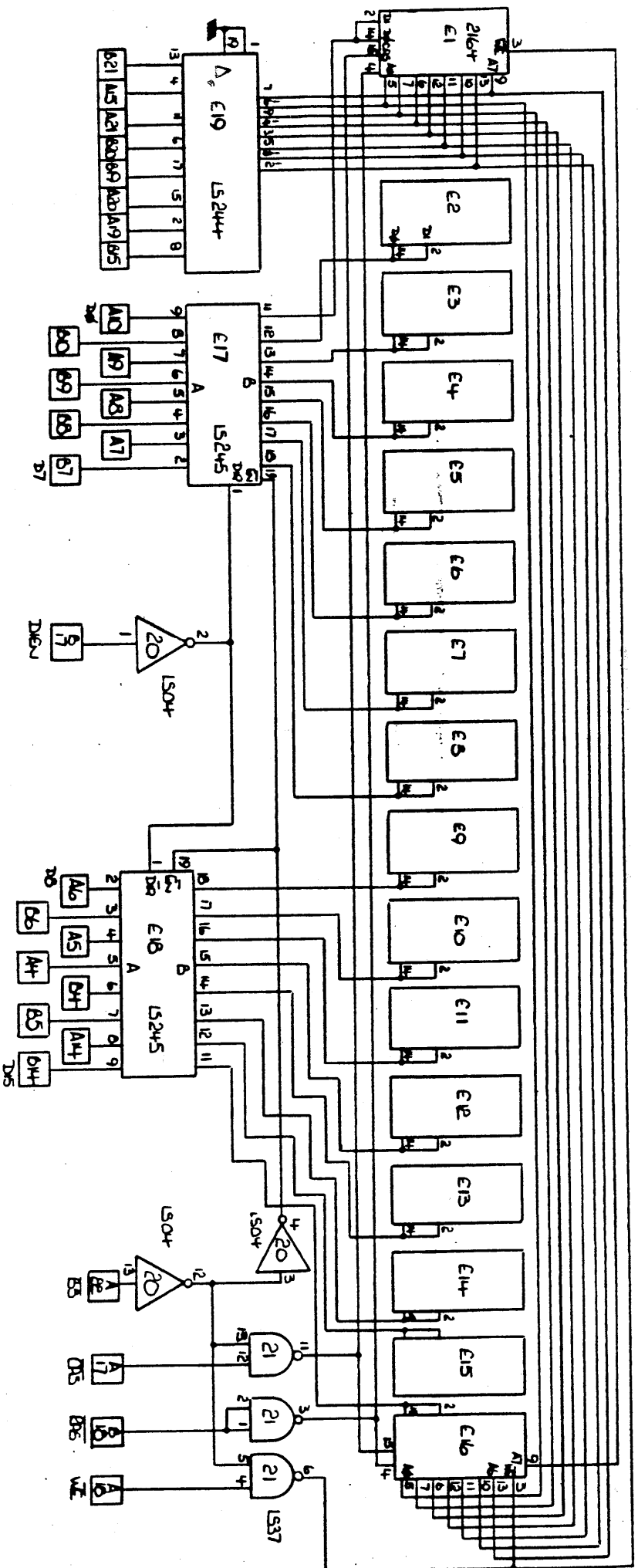
DRAWN

DATE

PRO. No.

200002

SHT 1



RAM4

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DRAWN

DATE

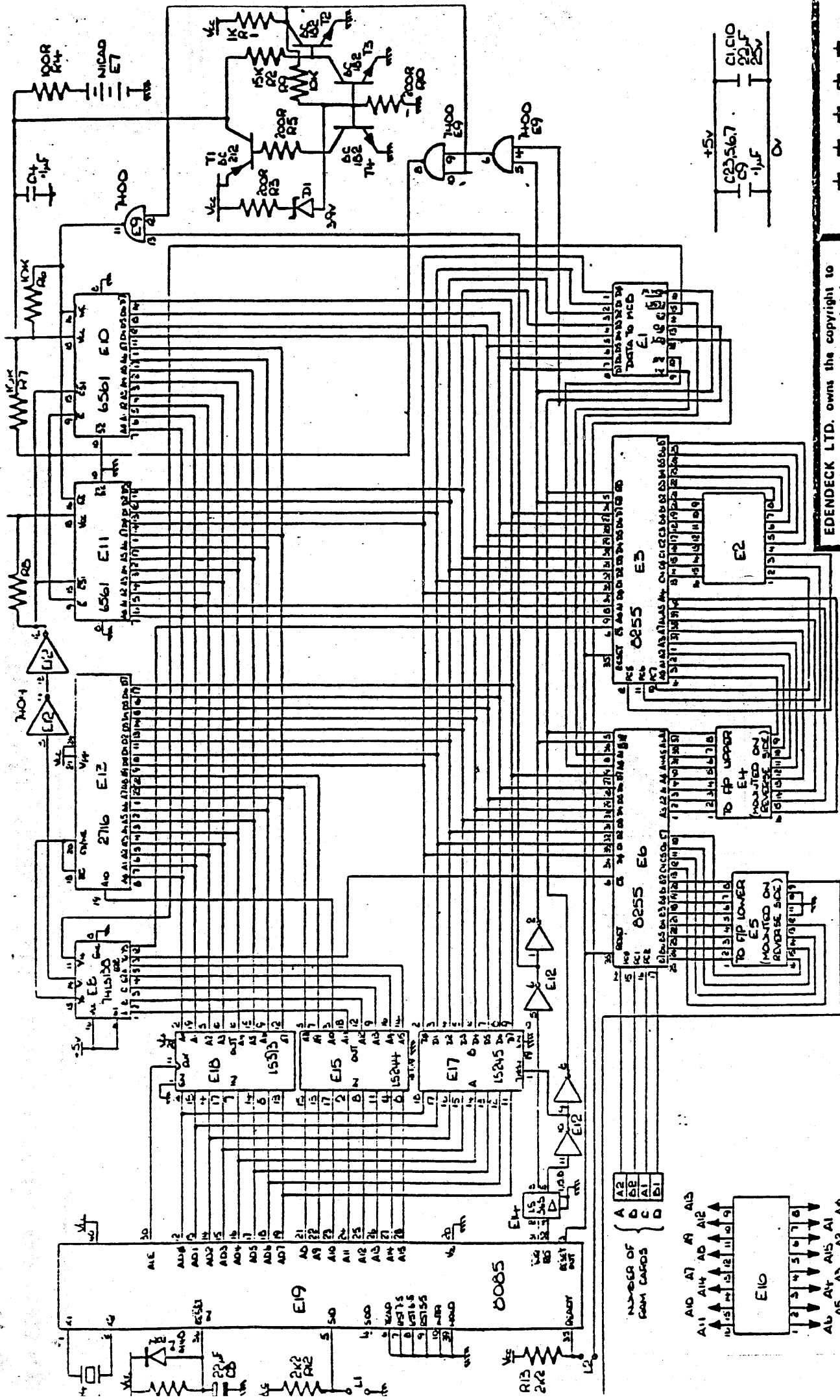
DES. No.

PAK00.1

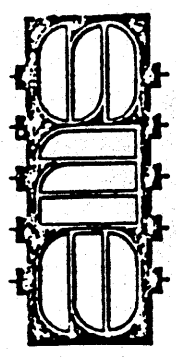
SHT 1



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PROCESSOR BOARD

FIRST ISSUE 2.5.80

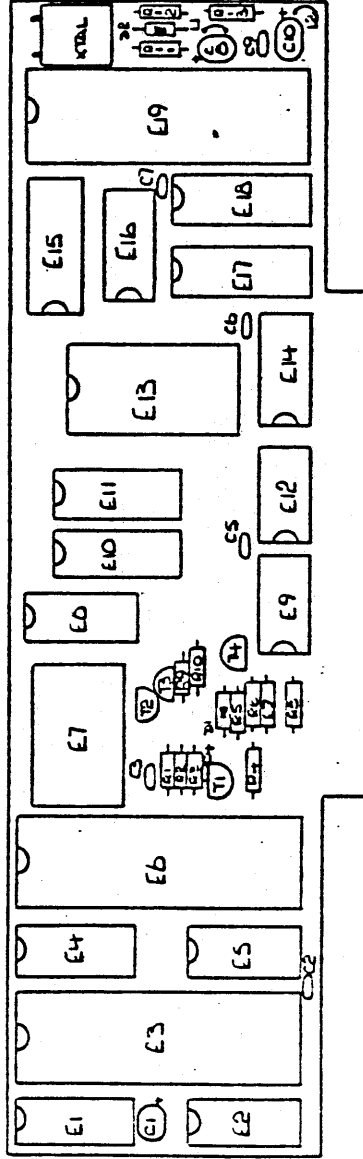
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DATE

DRG. No.

11.4.80



PROCESSOR BOARD

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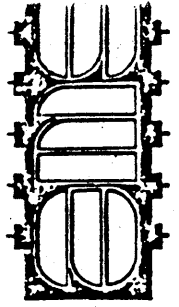
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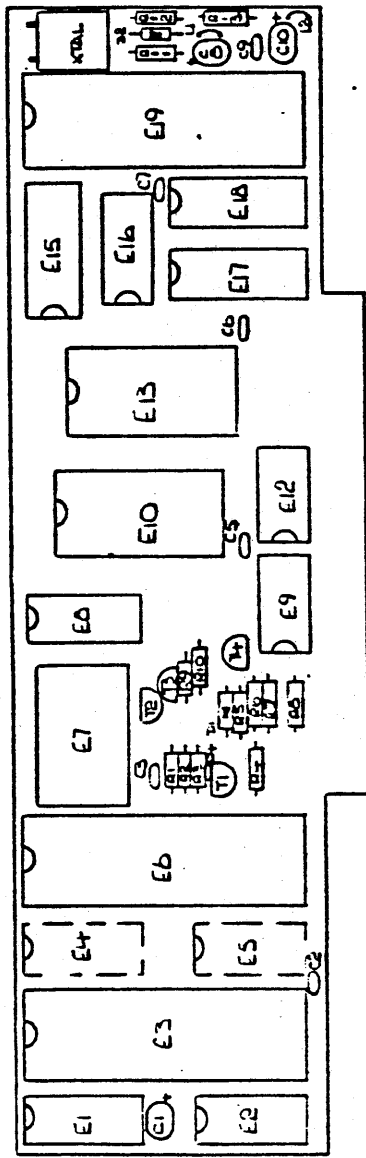
DATE 30.4.80

DRG. No. DMX 23.1

SHT



PCB 134	43
OPTIONAL ADDON	24 2-4
DATE	1-10-81



PROCESSOR BOARD OPTIONS

E2 CONNECTIONS LINK Pin 9 TO Pin 16

No of CARDS	CHANNEL A		CHANNEL B	
	LINK Pin 4 TO	LINK Pin 5 TO	LINK Pin 6 TO	LINK Pin 7 TO
1	Pin 1	Pin 1	Pin 1	Pin 1
2	Pin 1	Pin 16	Pin 1	Pin 16
3	Pin 16	Pin 1	Pin 16	Pin 1
4	Pin 16	Pin 16	Pin 16	Pin 16

No of MEMORIES	LINK Pin 8 TO
1	Pin 1
2	Pin 16

RAM BOARD CHOICE	CHANNEL A LINK Pin 11 TO	CHANNEL B LINK Pin 10 TO
4K RAM 102ms	Pin 1	Pin 1
16K RAM 400ms	Pin 16	Pin 16

PROCESSOR BOARD

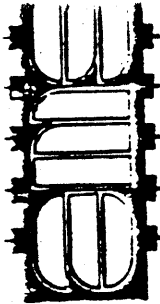
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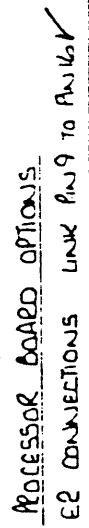
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DATE 30-4-80

DRG. No. DMX23.2
SHT

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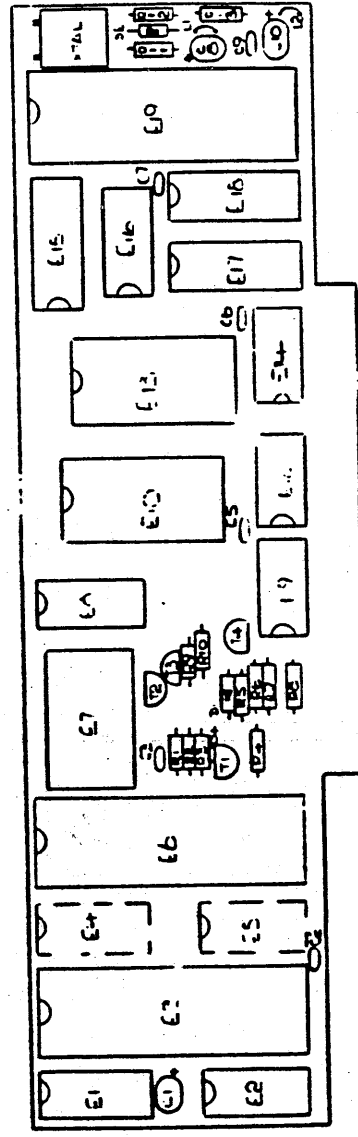


no of wire numbers	1	last	✓
	2	first	

from board choice	channel A link A110 to	channel B link B110 to
4K RAM 102 m S	pin 1	pin 1
6K RAM 108 m S	pin 16 ✓	pin 16

PROCESSOR BOARD

PROJ. ISSUE	2-5 00	1
OFFICIALS ADDRESS	1-2 01	2
1-3 01	1-3 02	3
1-4 01	1-4 02	4



PROCESSOR BOARD OPTIONS
E2 CONNECTIONS LINK Pw9 TO Pw16

No of CODES	CHANNEL A		CHANNEL B	
	LINK Pw4 TO	LINK Pw5 TO	LINK Pw6 TO	LINK Pw7 TO
1	Pw1	Pw1	Pw1	Pw1
2	Pw1	Pw16	Pw1	Pw16
3	Pw16	Pw1	Pw16	Pw1
4	Pw16	Pw16	Pw16	Pw16

No of WORKSTATIONS	LINK Pw8 TO	
	Pw1	Pw16
1		
2		

RAM BOARD CHOICE	CHANNEL A		CHANNEL B	
	LINK Pw1 TO	LINK Pw10 TO	LINK Pw11 TO	LINK Pw12 TO
4K RAM 102 m/s	Pw1	Pw1	Pw1	Pw1
16K RAM 100 m/s	Pw16	Pw16	Pw16	Pw16

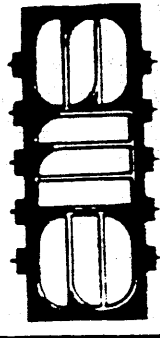
PROCESSOR BOARD

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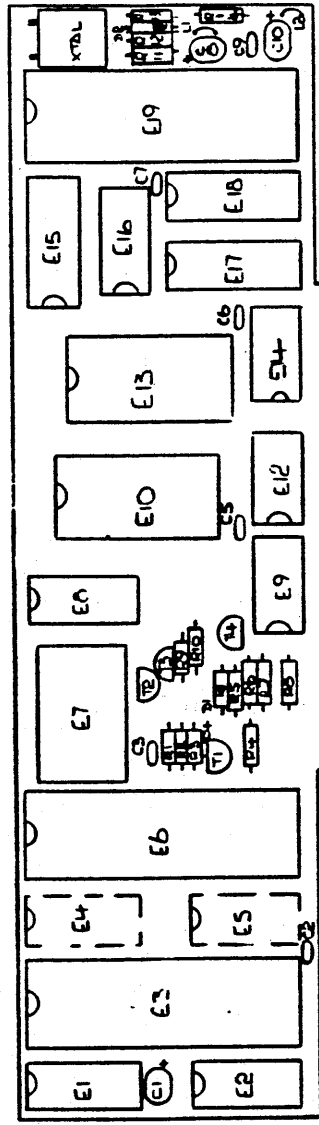
DRAWN
 DATE 30 4 80

DRG. No.
DMX23.3
 SHT 2



advanced music systems, burnley, england.

REV	ISSUE	2-5-80	1
OPTIONAL ADDS	24" H	2	
OPTIONAL ADDS	1-0-81	3	
OPTIONAL ADDS	11-11-82	4	



PROCESSOR BOARD OPTIONS
E2 CONNECTIONS LINK P19 TO P16

No of CHOS	CHANNEL A	CHANNEL B
1	LINK P14 TO LINK P15 TO	LINK P16 TO LINK P17 TO
2	P11	P11
3	P11	P11
4	P11	P11

RAM BOARD CHOICE	CHANNEL A	CHANNEL B
LINK P14 TO LINK P15 TO	P11	P11
LINK P16 TO LINK P17 TO	P11	P11

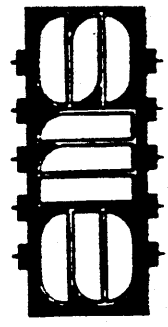
PROCESSOR BOARD

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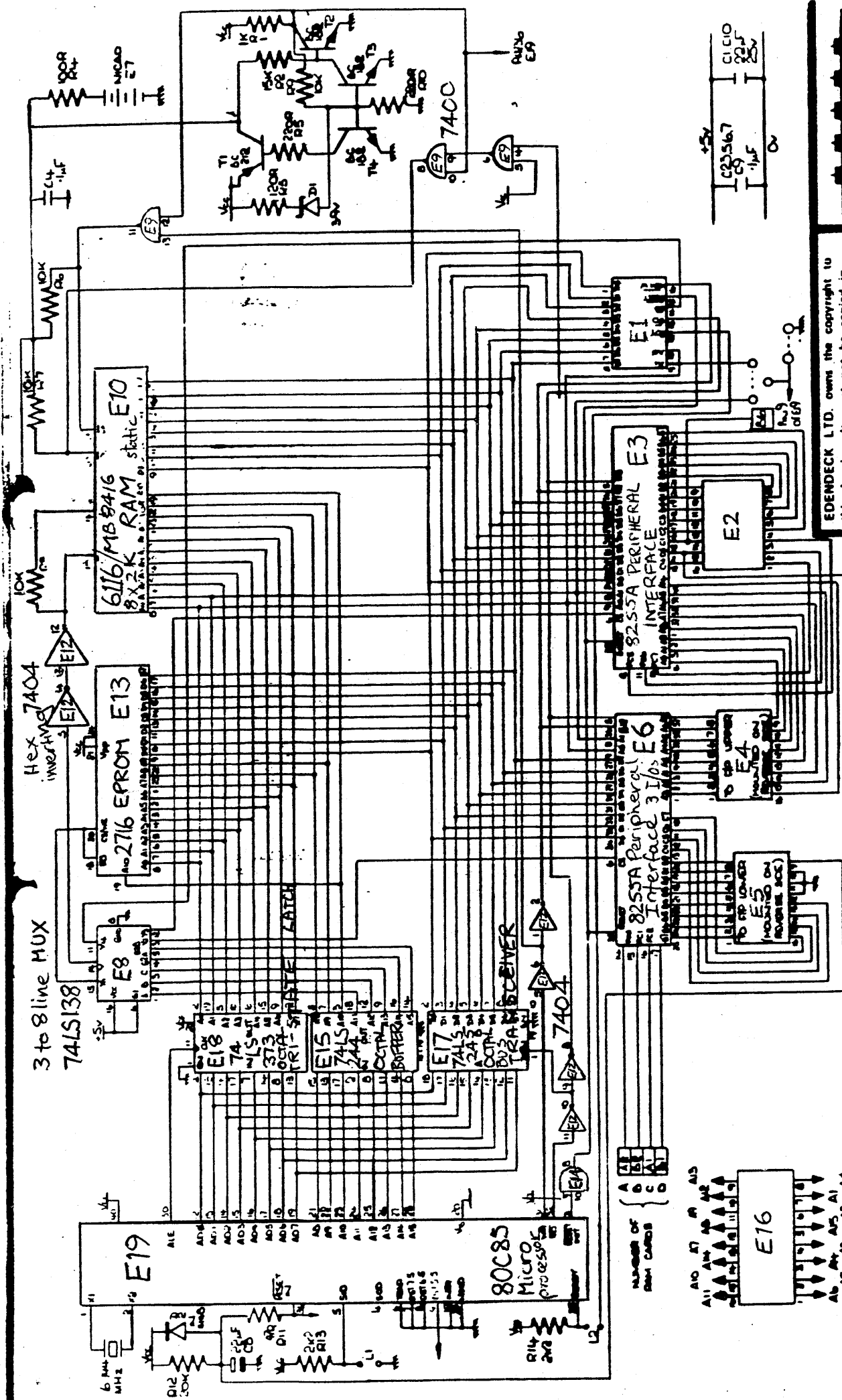
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DRAWN ☒ DATE 30-4-80

DRG. No. DMX23.4 SHT 2



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PRO. No. **DMX 23 4** PART 1

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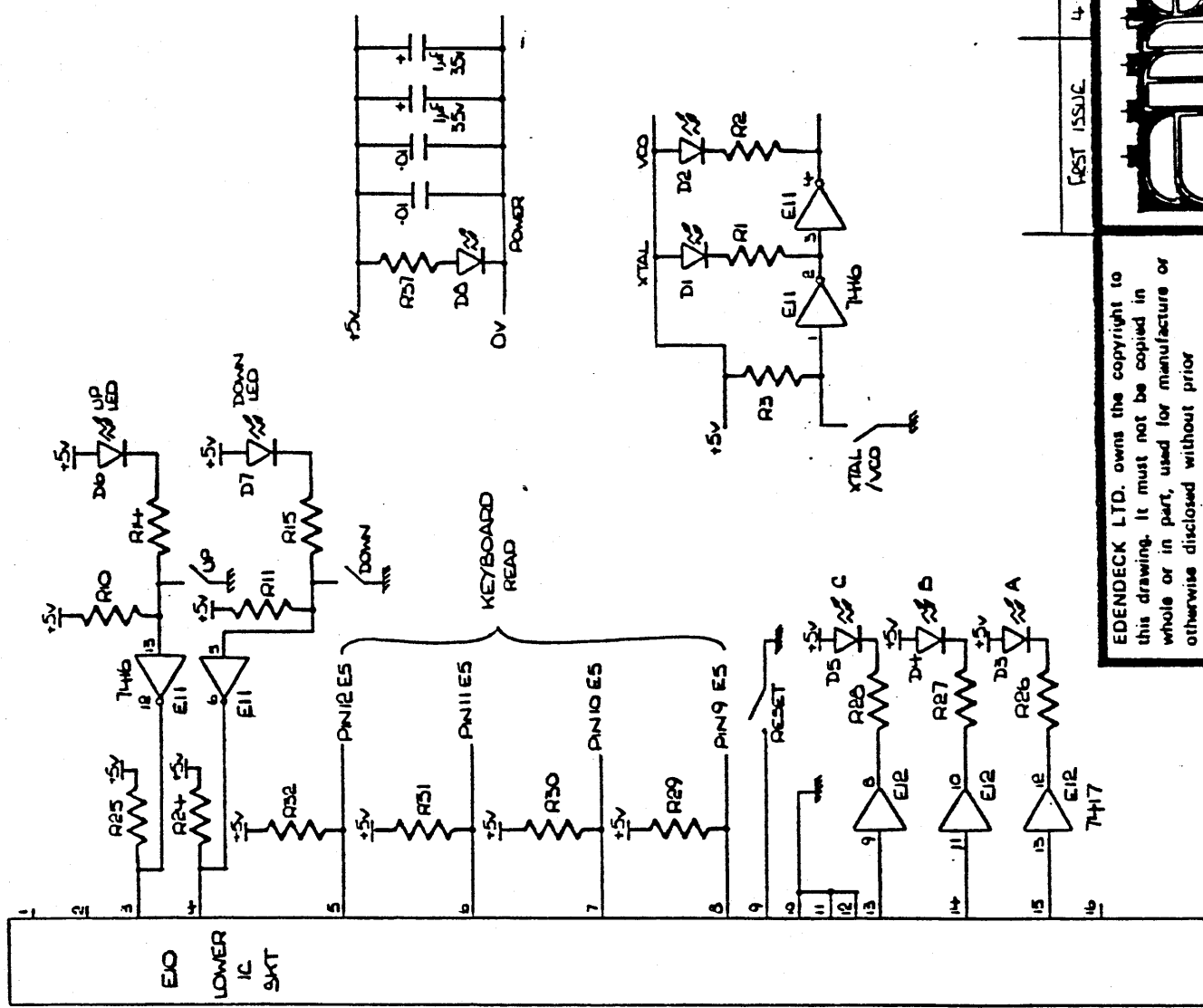
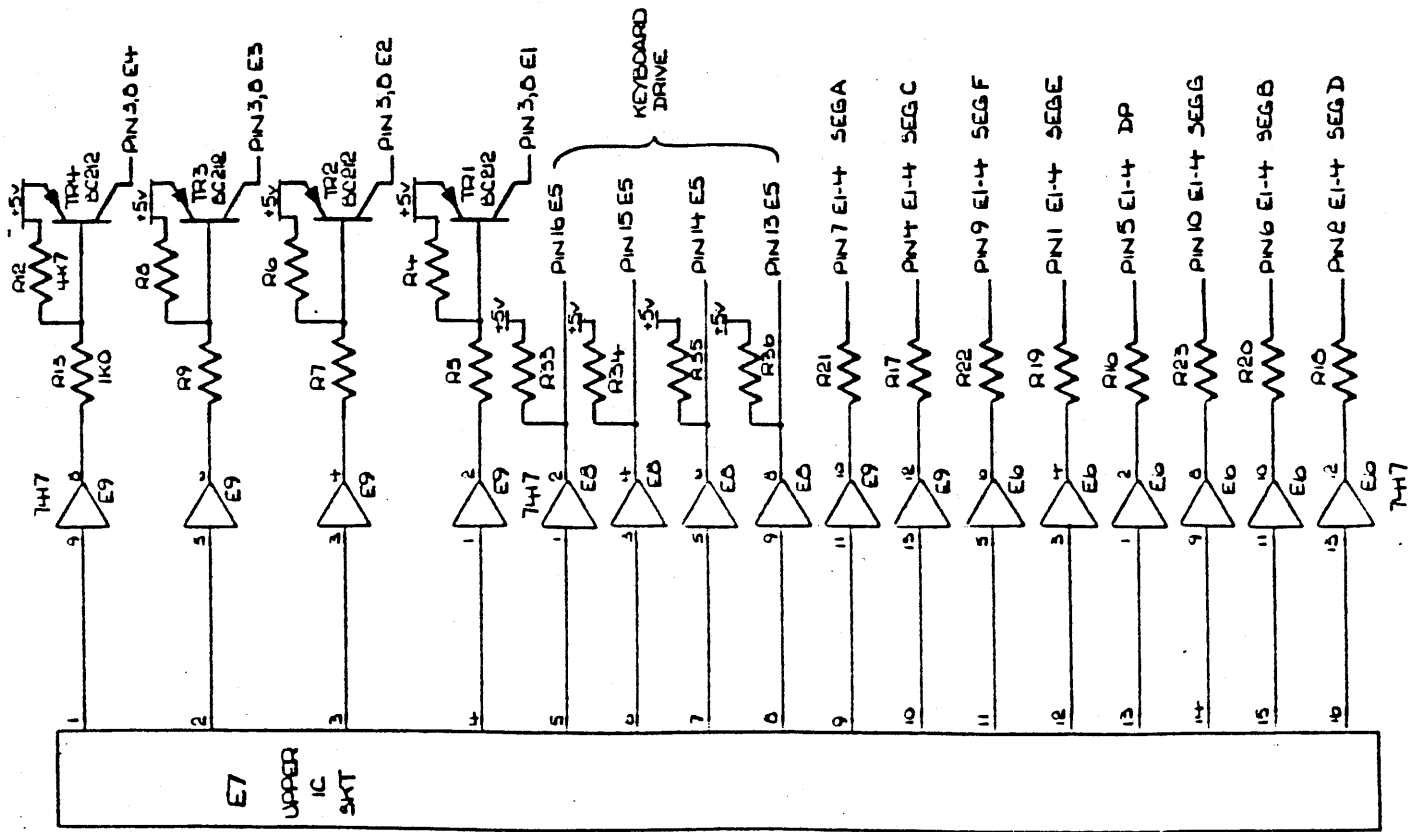
DATE **11-4-80**

PROCESSOR BOARD

ITEM NO.	DESCRIPTION	QTY	REV
1	80C85 Microprocessor	1	1
2	8255A Peripheral	1	1
3	8255A Peripheral Interface	1	1
4	8255A Peripheral	1	1
5	8255A Peripheral Interface	1	1
6	8255A Peripheral	1	1
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100	8255A Peripheral	1	1

DATE **11-4-80**

advanced music systems, burnley, england.



DISPLAY BOARD

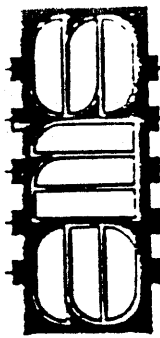
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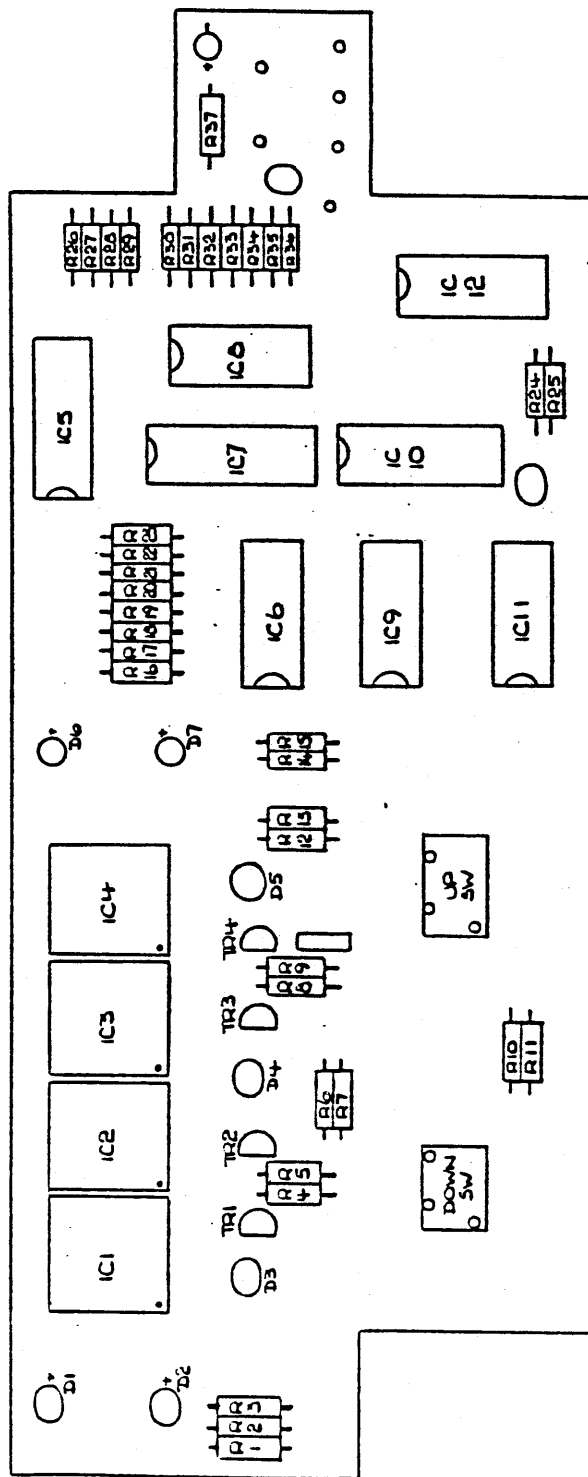
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REV. 15512

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DISPLAY BOARD

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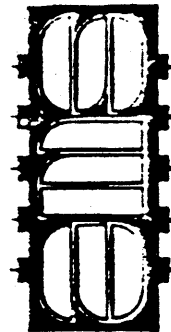
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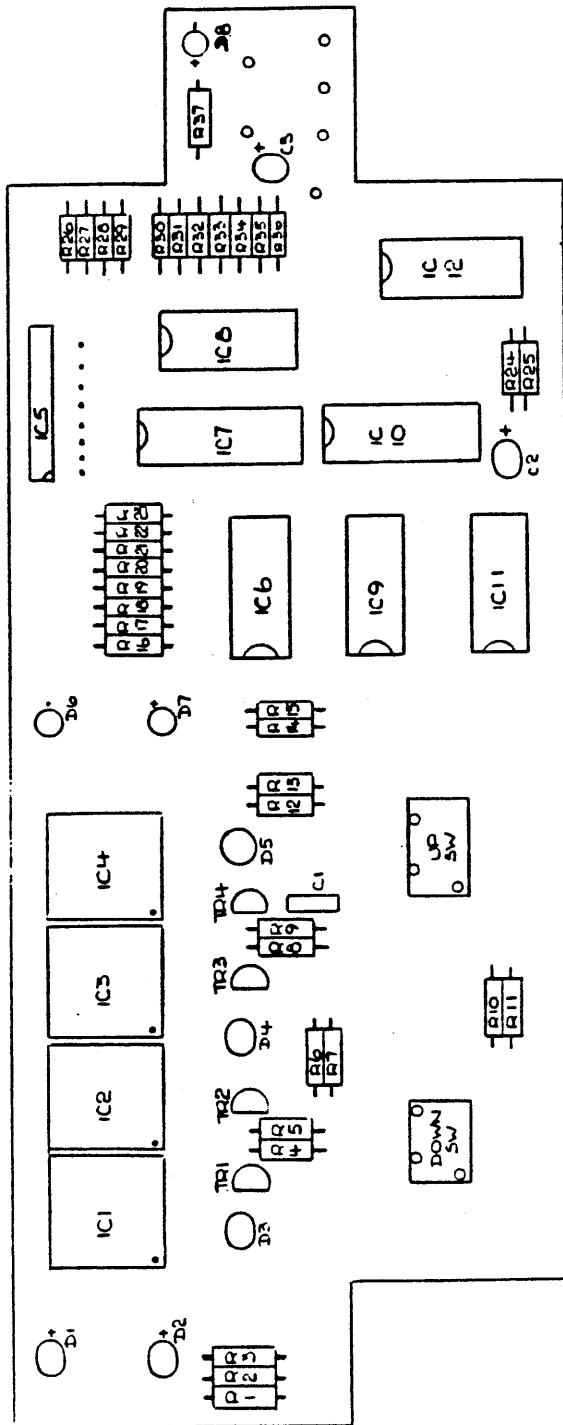
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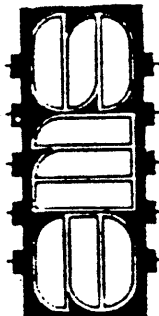
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DRG. No.

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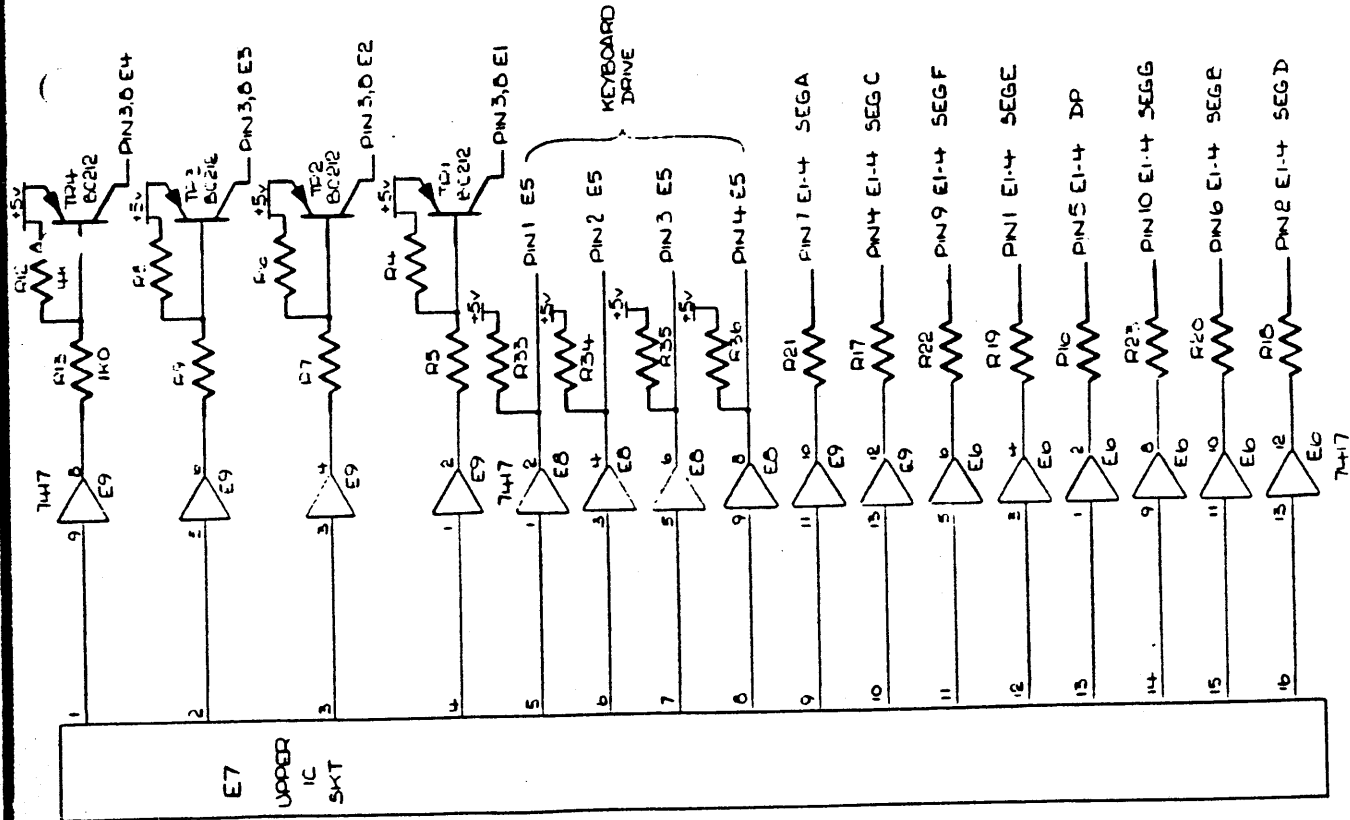
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DISPLAY BOARD



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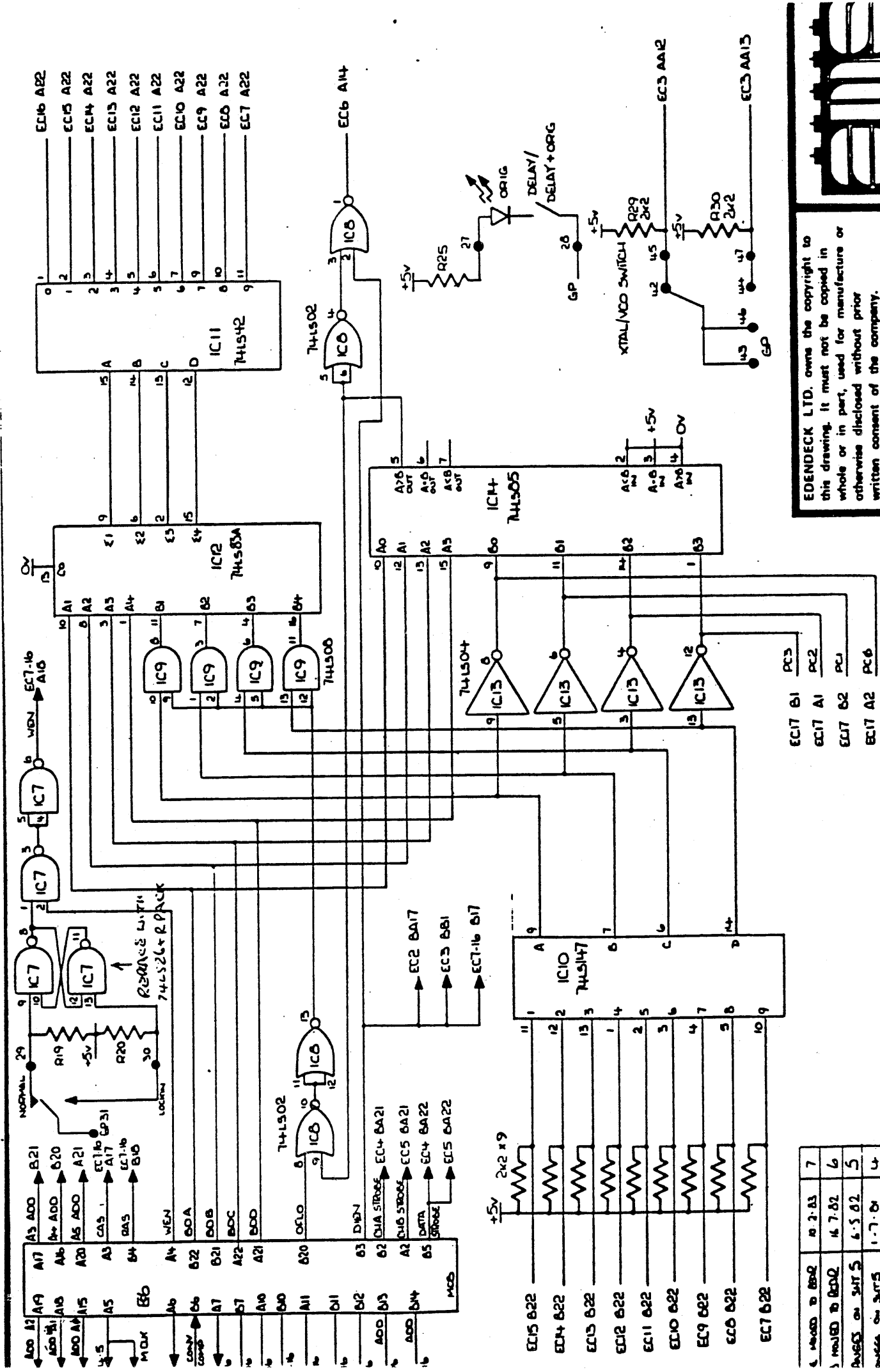
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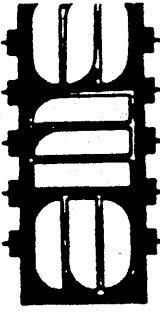
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BASEBOARD LOGIC CIRCUIT DIAGRAM



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DWG. No.

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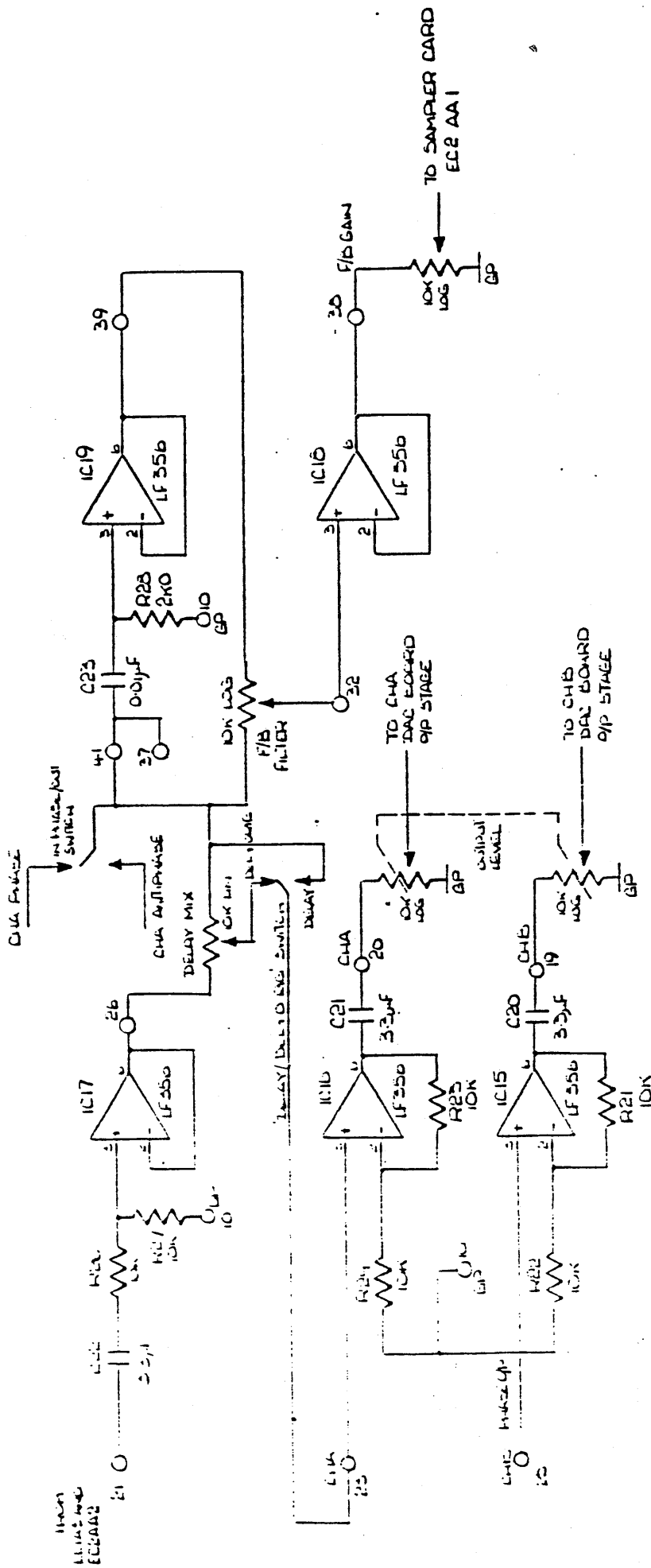
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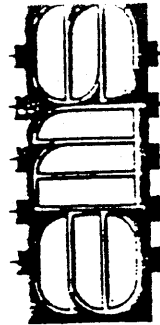
BASEBOARD ANALOGUE CIRC. DIAG
& FRONT PANEL WIRING

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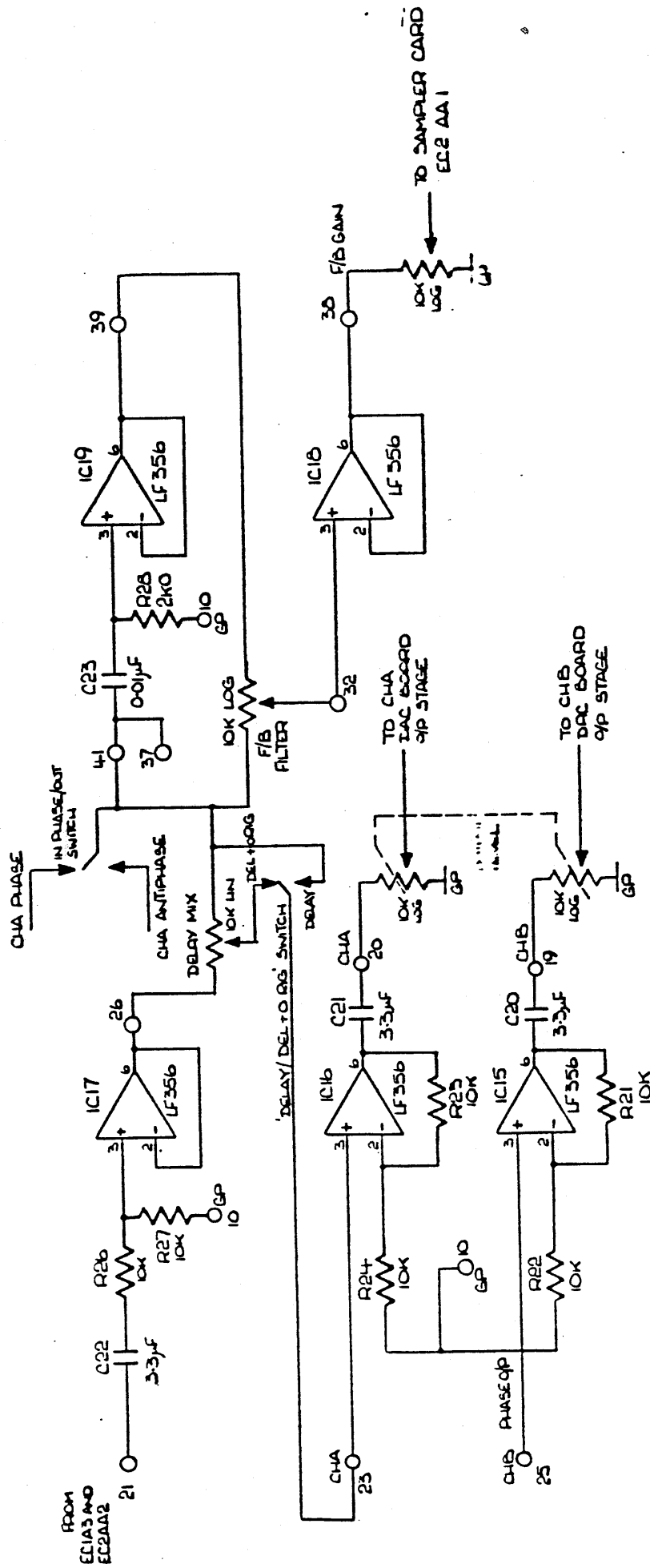
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DATE

DRG. No.



RES. MOUNTED TO BOARD	16.7.82
CHANGES ON SHIT 5	1.5.82
CHANGES ON SHIT 5	1.7.81
CHANGES ON SHIT 5	20.9.80
CHANGES ON SHIT 4.5	5.7.80
FIRST ISSUE	28.9.79



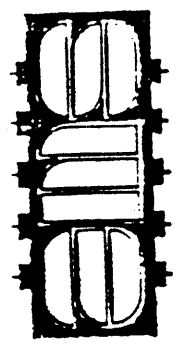
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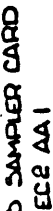
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DATE

DRG. No.



CHANGES ON SH75	1.7.81	4
CHANGES ON SH75	20.9.80	3
CHANGES ON SH75	3.7.80	2
1ST ISSUE	28.9.79	1

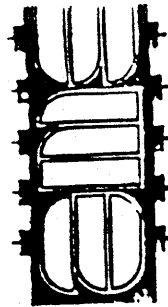


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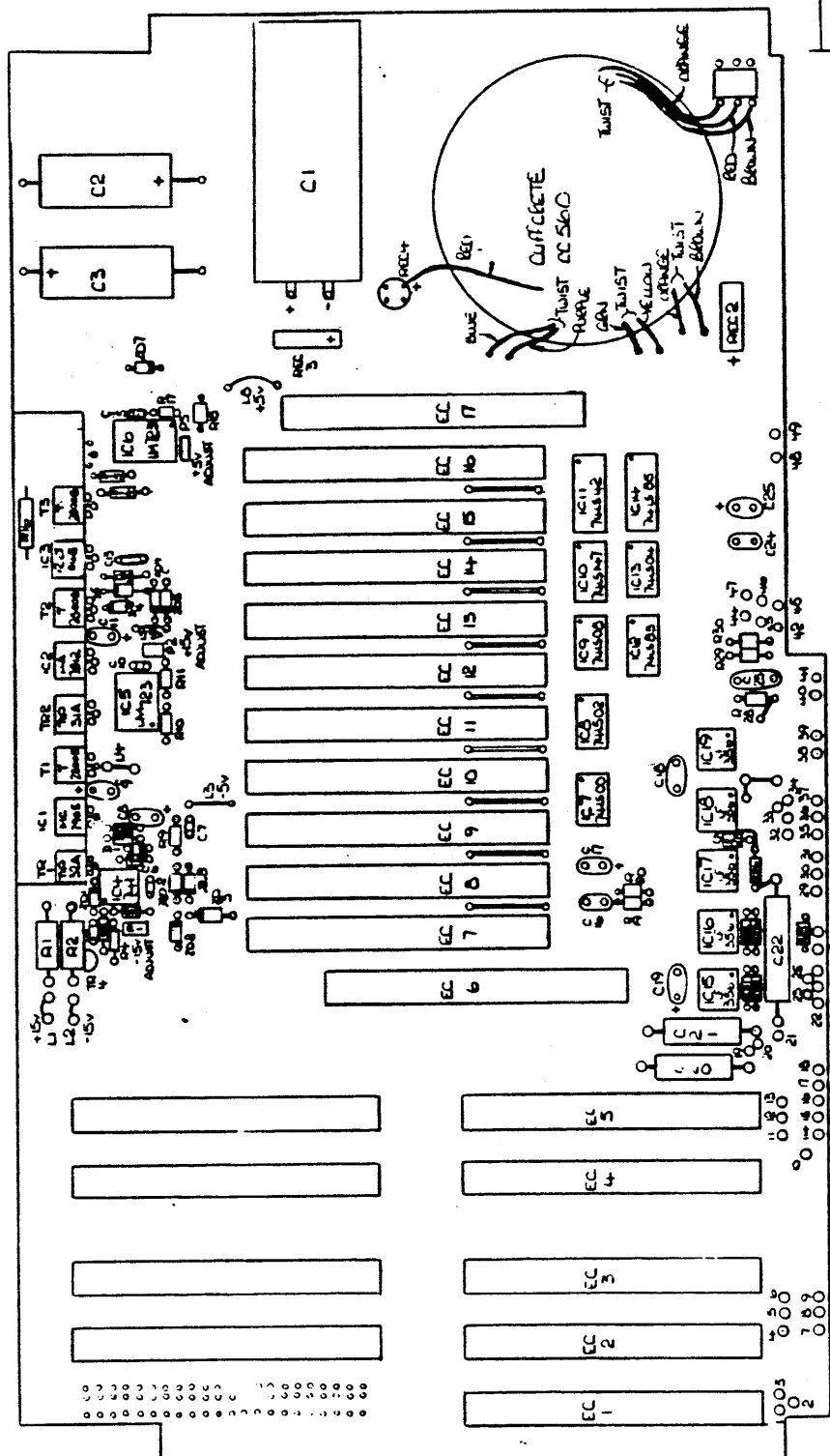
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DATE _____

DRG. No.







USE POWER TO BOARD	10 2 A4	7
W'S MOUNTED TO BOARD	16 7-82	6
CHANGES ON SMTS	6-5 02	5
CHANGES SMT 5	1-7 02	4
CHANGES SMT 5	30-9 00	3
OVERALL SMTS CHANGES	3-7-80	2
FIRST ISSUE	28-9-79	1

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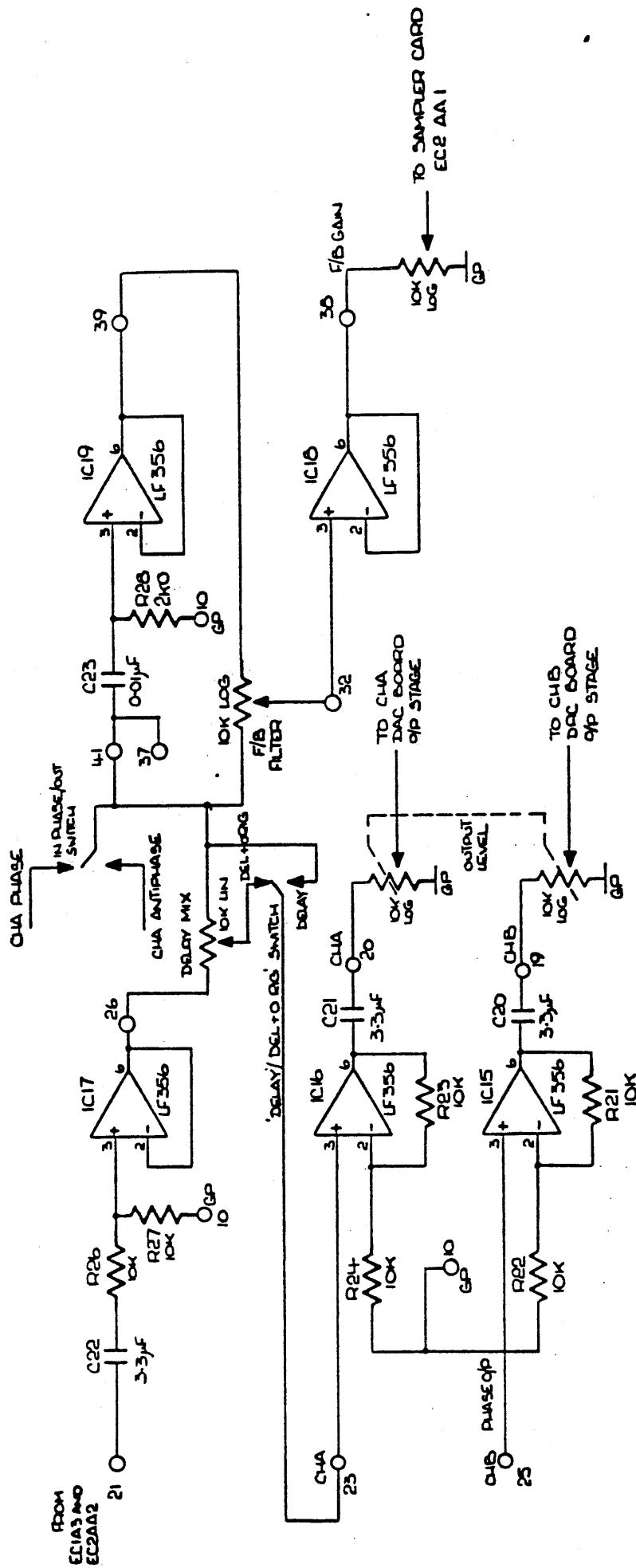
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PRO. No.

DMX MB-2 SMT 4

BASEBOARD

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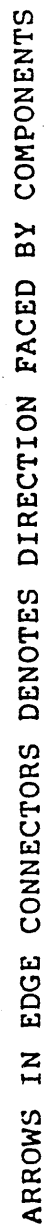
DRG. No.

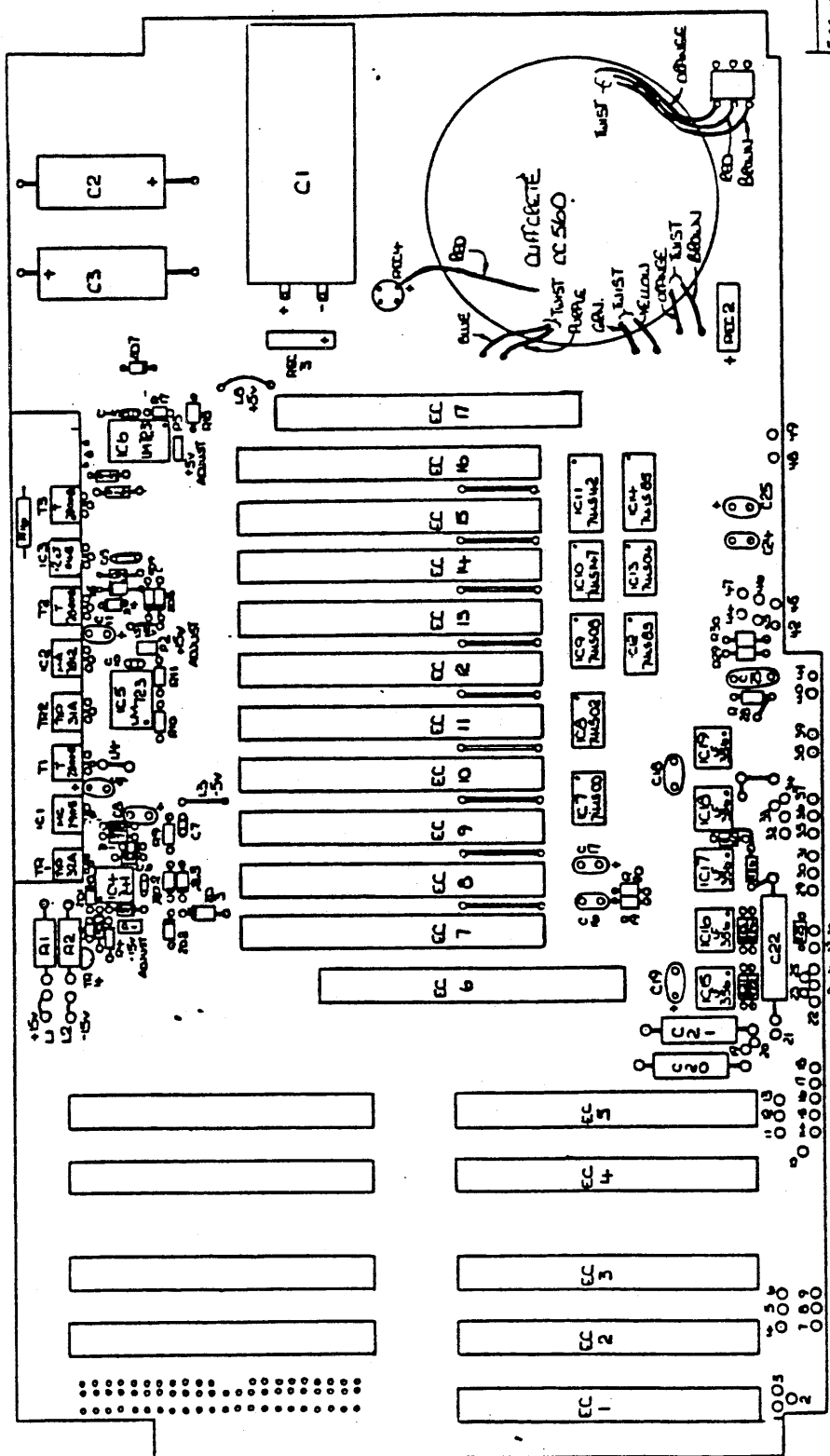


REVISION TO 6002	10-2-03
REVISION TO 6002	14-7-82
CHANGES ON SMT 5	6-5-82
CHANGES ON SMT 5	1-7-82
CHANGES ON SMT 5	20-9-82
CHANGES ON SMT 5	3-7-82
FIRST ISSUE	28-9-79

BASEBOARD ANALOGUE CIRC. DIAG & FRONT PANEL WIRING

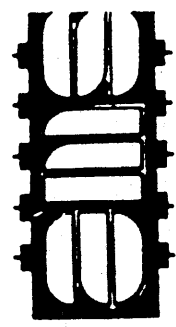
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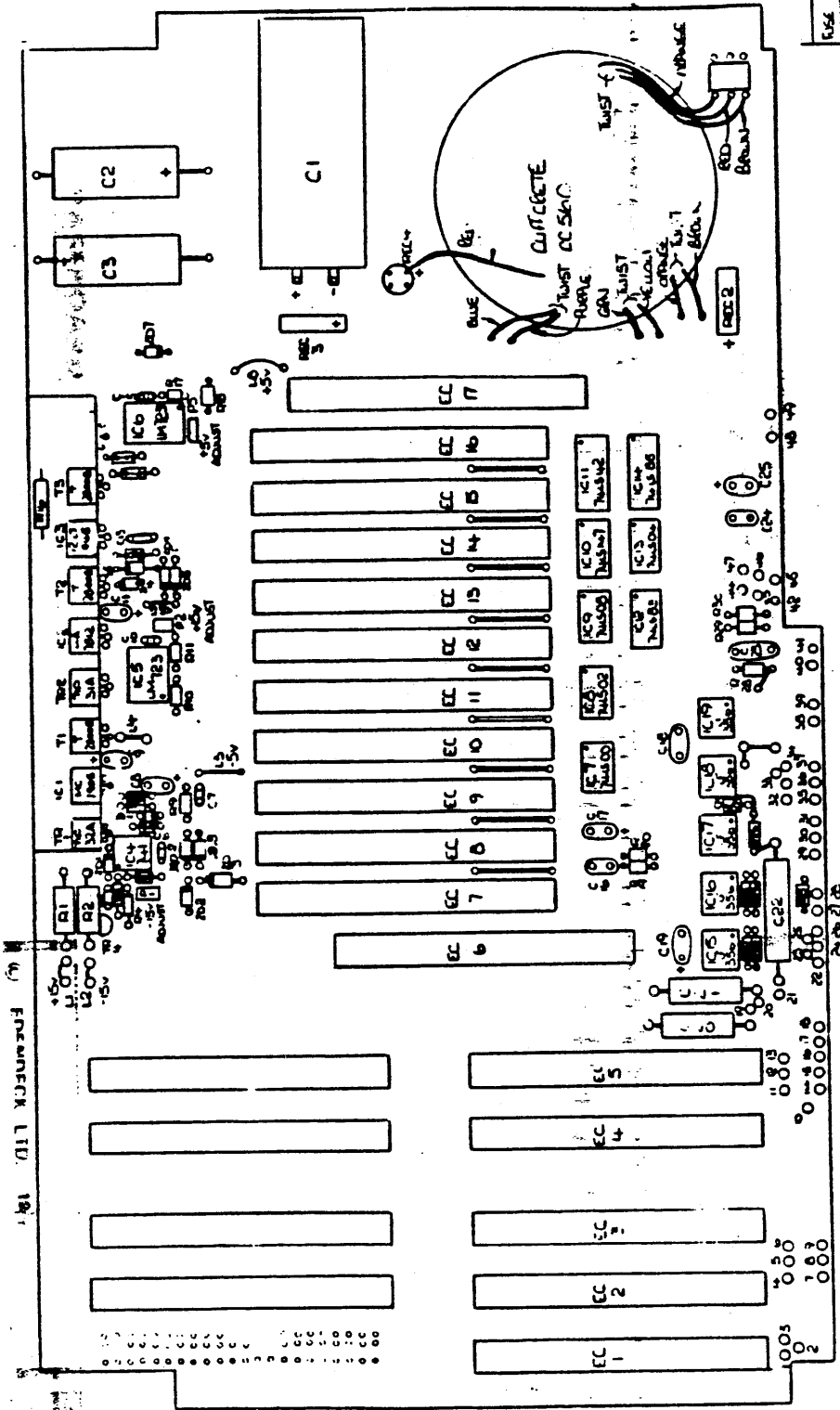


BASEBOARD

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USE MOD TO BDR	10 2 A3
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CHANGES ON SMTS	6 5 A2
CHANGES ON SMTS	1 7 0
CHANGES ON SMTS	30 9 00
CHANGES ON SMTS	3 7 00
FIRST ISSUE	28 9 79



ISS. NO. 10 2 83	7
ISS. NO. 10 7 82	6
CHANGES ON SUTS	5
CHANGES SUTS	4
CHANGES SUTS	3
CHANGES SUTS	2
FIRST ISSUE	1

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DWG. No.

DMX MB.2 0074

REVISION RECORD SHEET : 20.11.61

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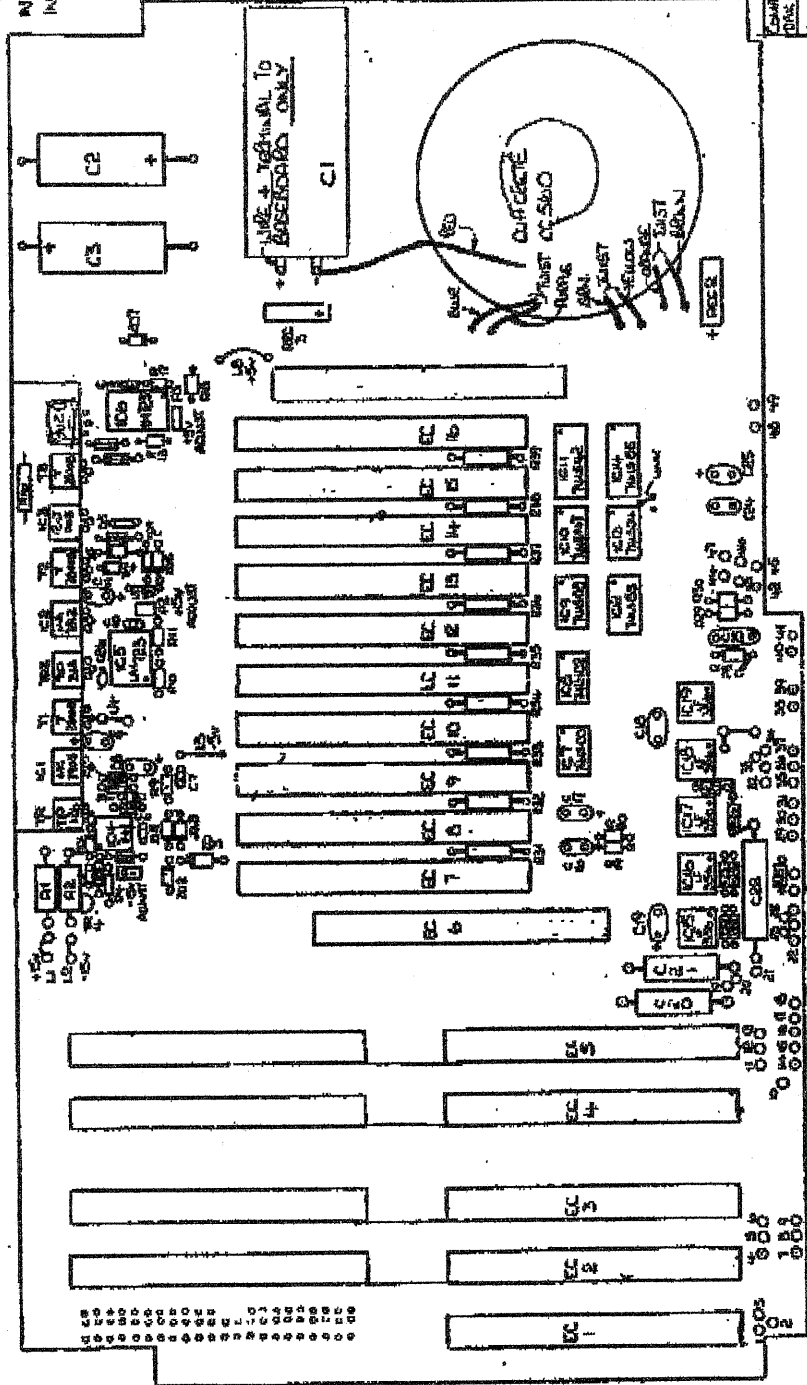
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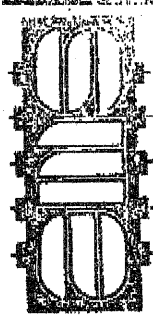
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M25 NUT
M25 3/8 WASHER
INSULATING BUSH
HEATING BRIDGE
DMX 1007
APASIC WARMER
PLAIN BRUSH
M25 NUT



COMPONENTS REQUIRED	25.7.92	3
DMX 1007 (B1)	0.2.93	7
DMX 1007 (B2)	16.7.92	6
DMX 1007 (B3)	16.7.92	5
DMX 1007 (B4)	16.7.92	4
DMX 1007 (B5)	16.7.92	3
DMX 1007 (B6)	16.7.92	2
DMX 1007 (B7)	16.7.92	1



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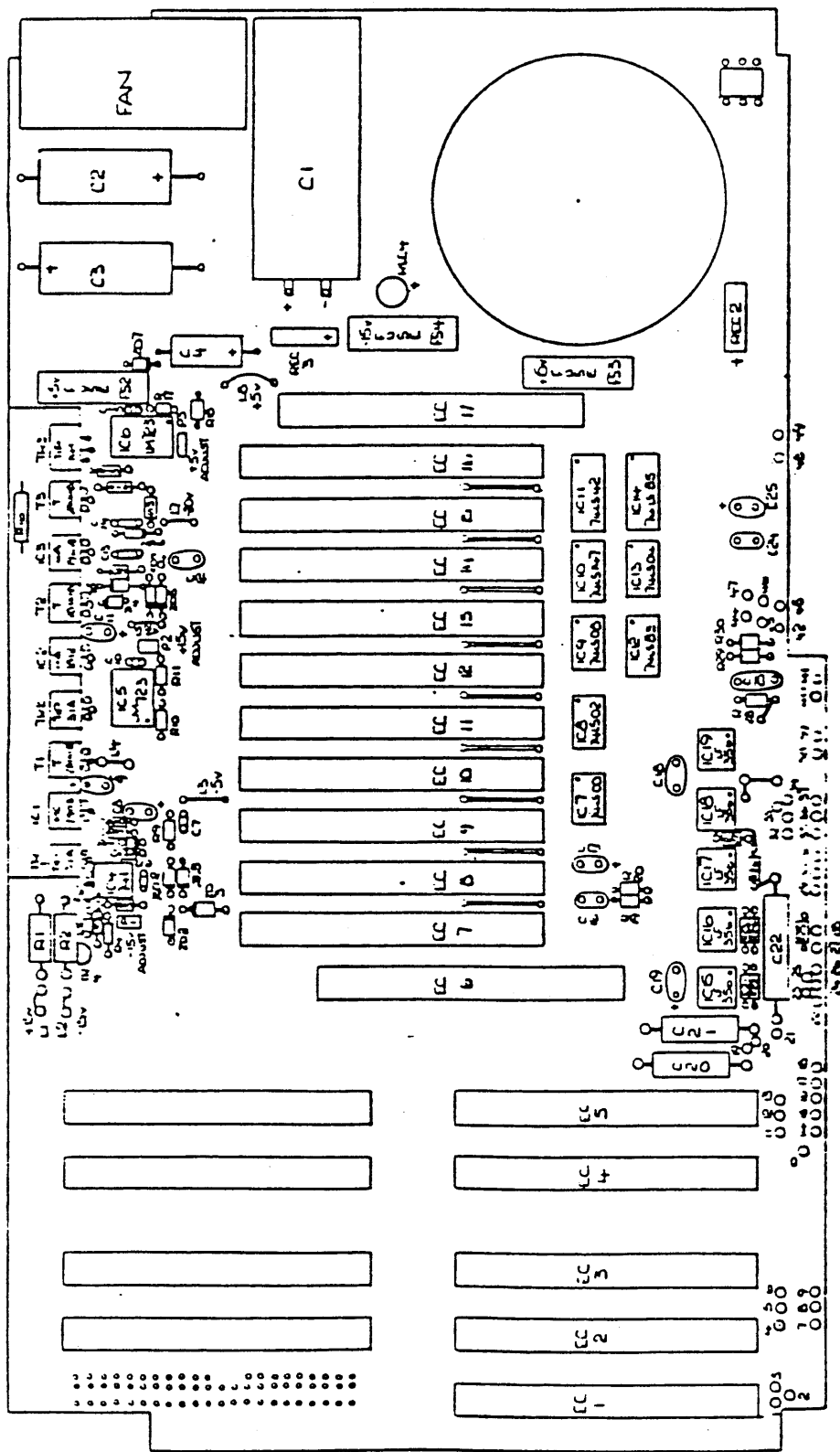
DATE

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BASEBOARD

ORG. No.

DMX MB.2 SHY4



BASEBOARD

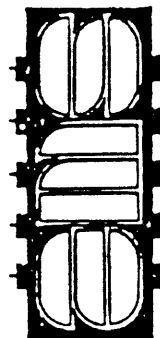
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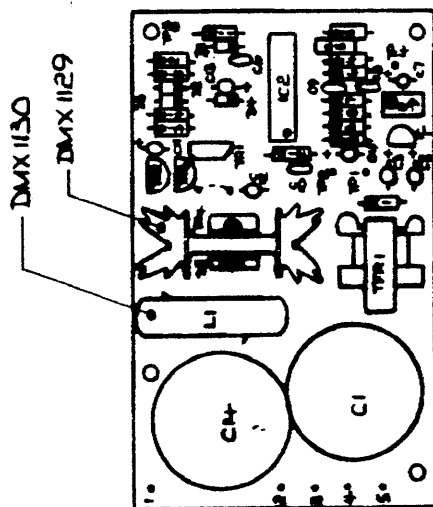
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DATE

DRG. No.



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FIRST	ISSUE	28-9-79		1



SWITCHING P/S BOARD ASSY

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DRAWN

DATE

10-10-03

DRG. No.

DMX70.1

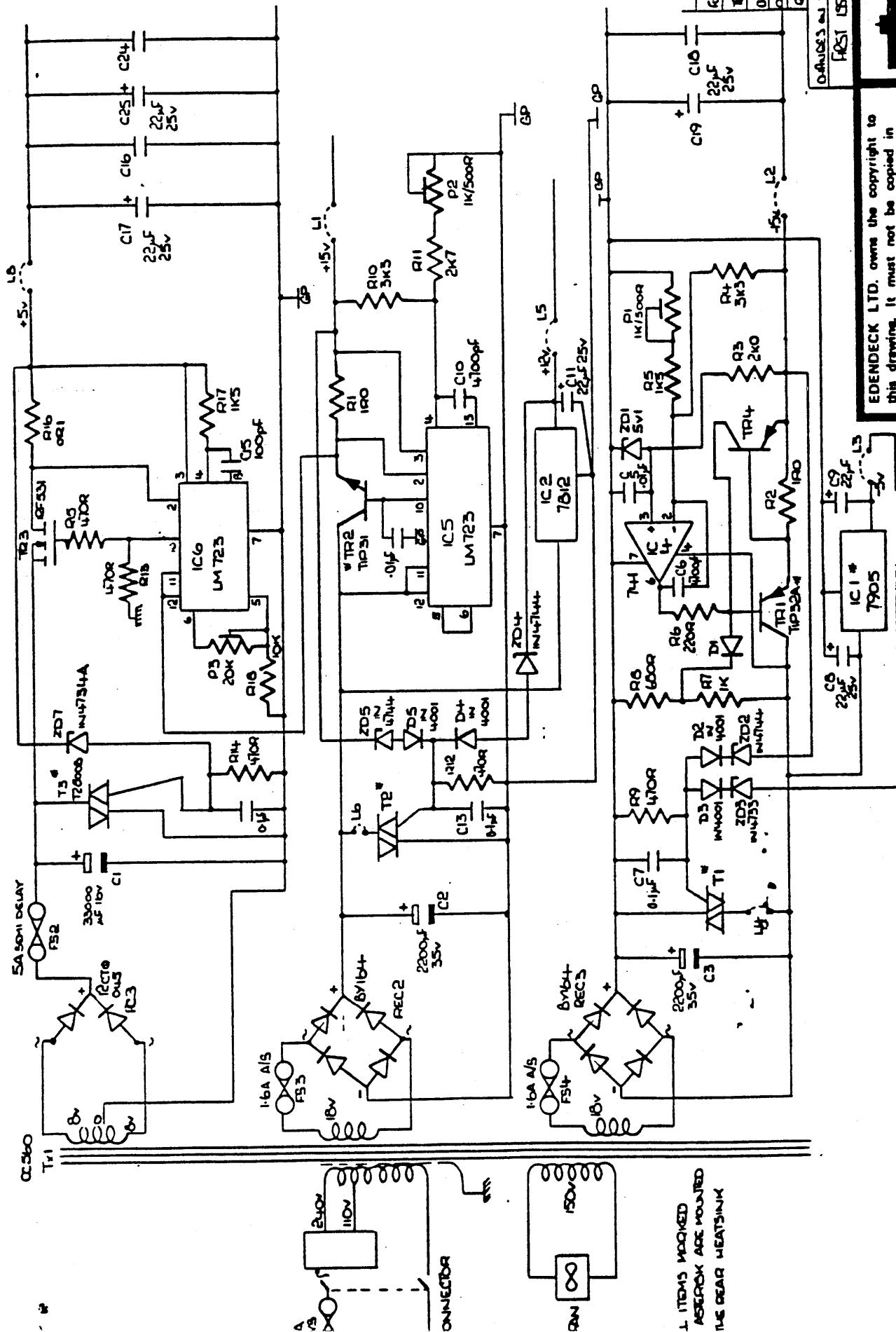
SHT 2

FIRST ISSUE

10-10-03



advanced music systems burnley, england.



6653 M050	10.2 AS
73 M050	4.7.02
04053 M15	25.02
016 M15	1.7.0
046 M15	30.9.00
04053 M15	1.7.00
04053 M15	20.9.79

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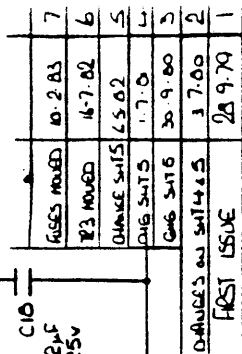


POWER SUPPLIES

Advanced micro systems - anand. 1

DATE

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POWER SUPPLIES

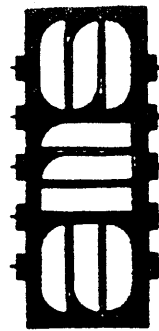
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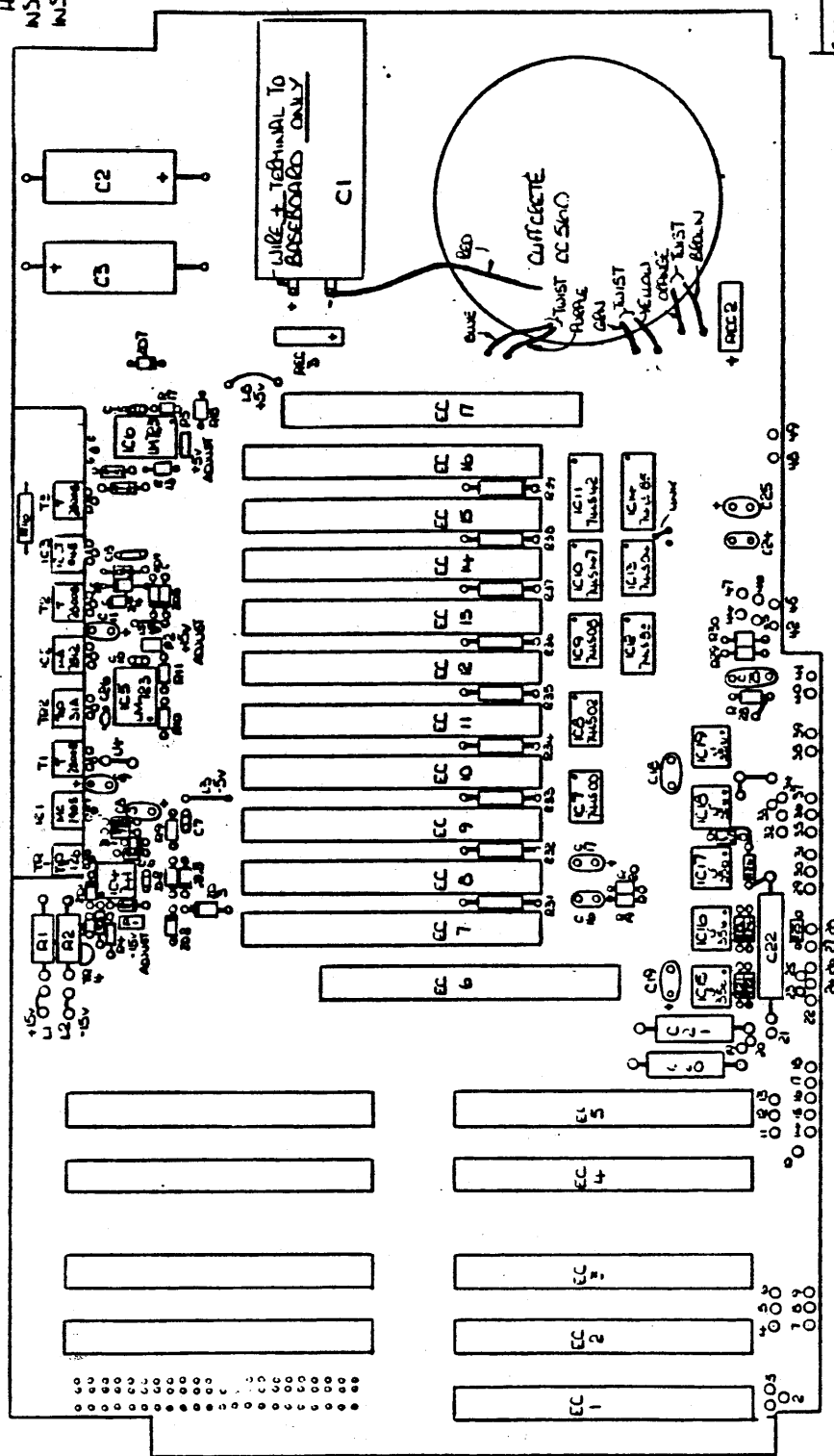
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DMX MB.2 HT3



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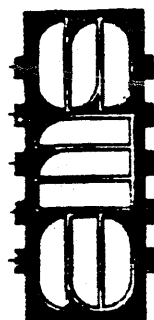
DATE _____

28.9.79

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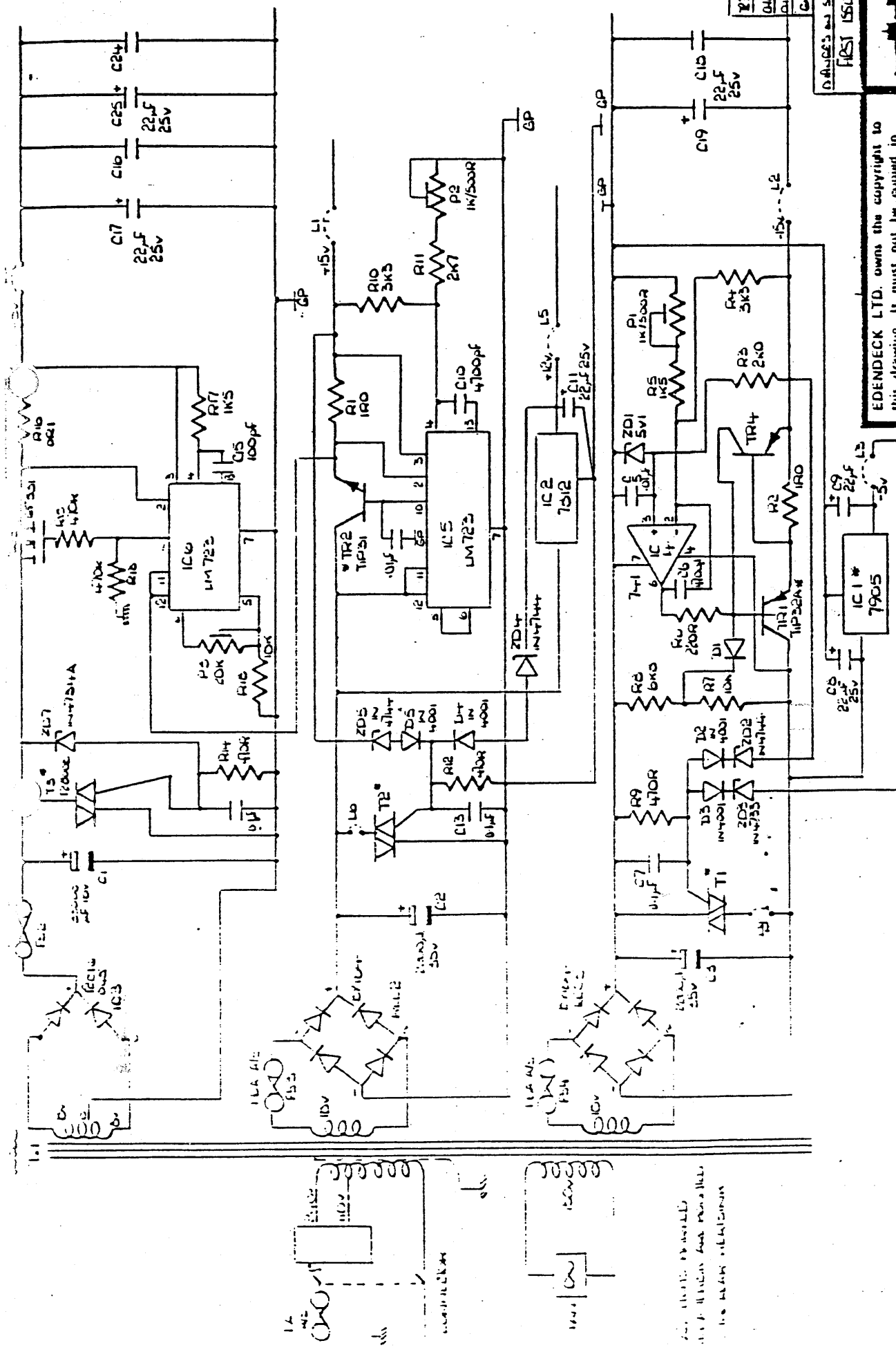
DMX MB.2

SHY 4



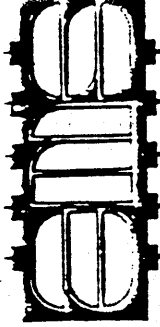
BASEBOARD

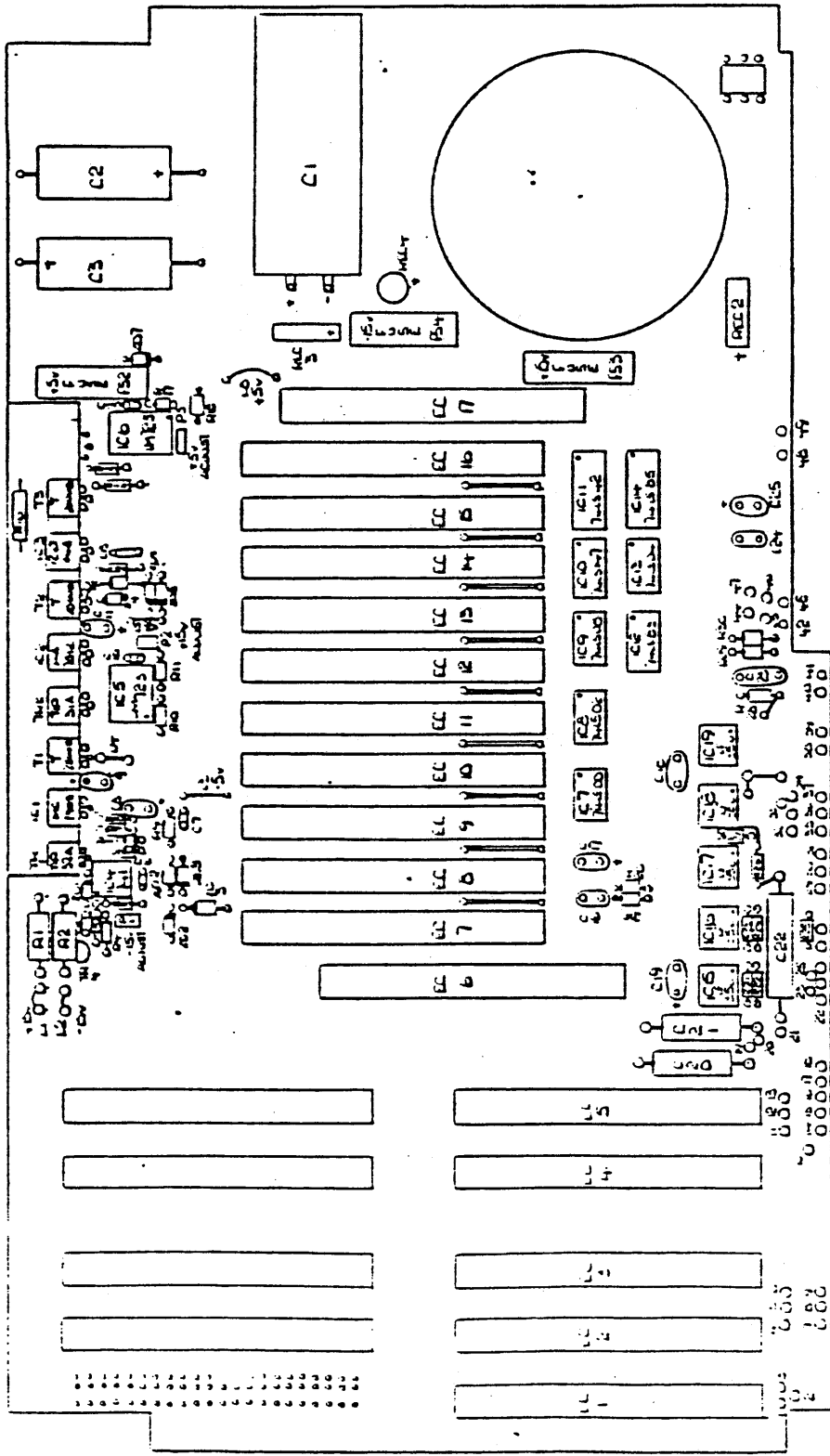
advanced music systems, burnley, england.



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Q3 5415	LS 02
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Q5 5415	LS 02
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Q100 5415	LS 02

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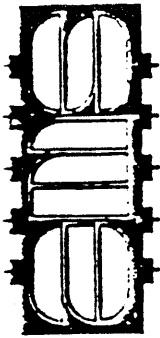
BASEBOARD

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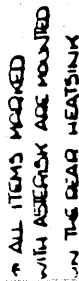
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DATE



W3 MODIFIED TO BOARD	6.7.82	6
CHANGES ON SATS	4.5.82	5
CHANGES SAT 5	1.7.82	4
CHANGES SAT 5	20.9.80	3
ORIGINAL SHAPE DRAW	5.7.80	2
FIRST ISSUE	28.9.79	1



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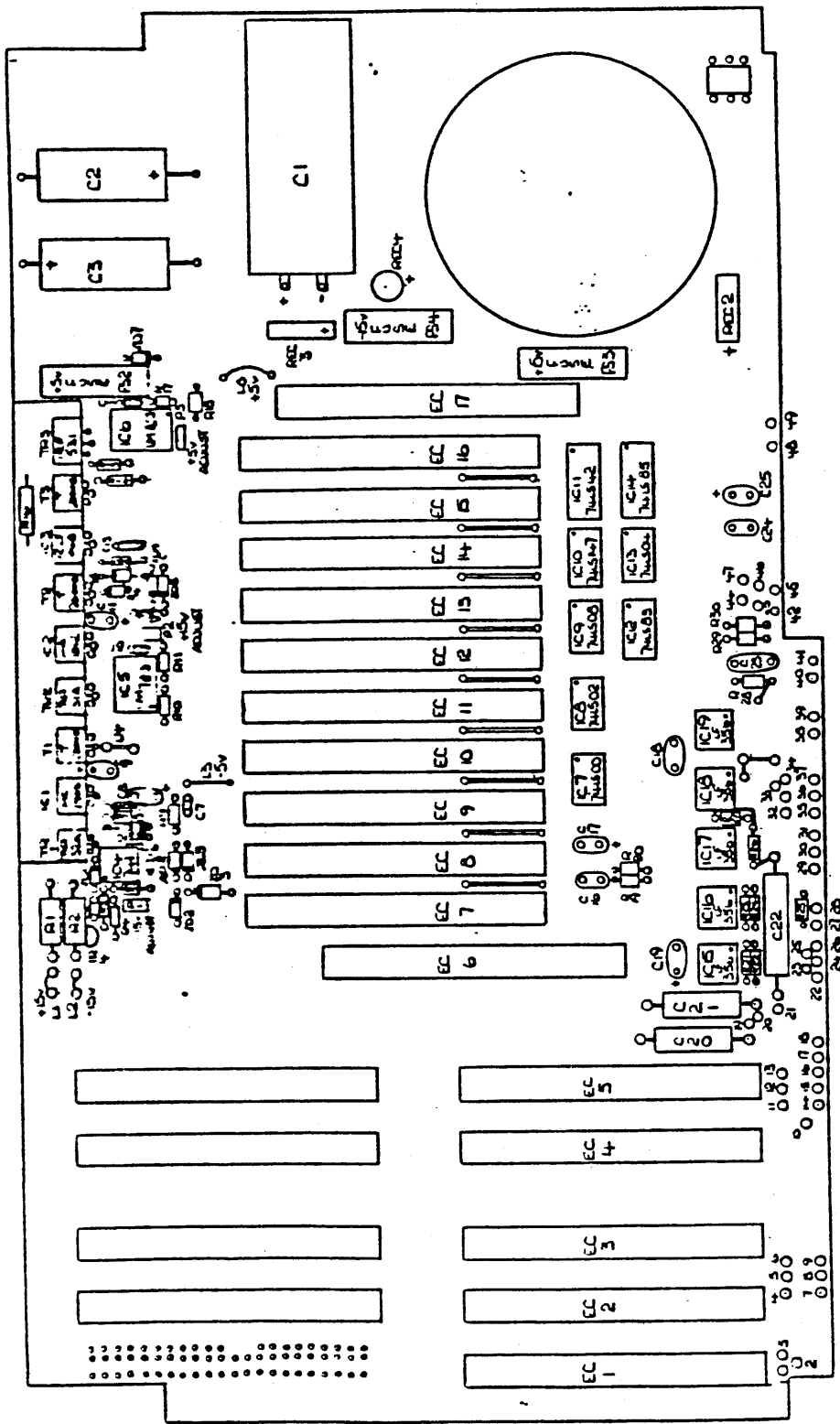
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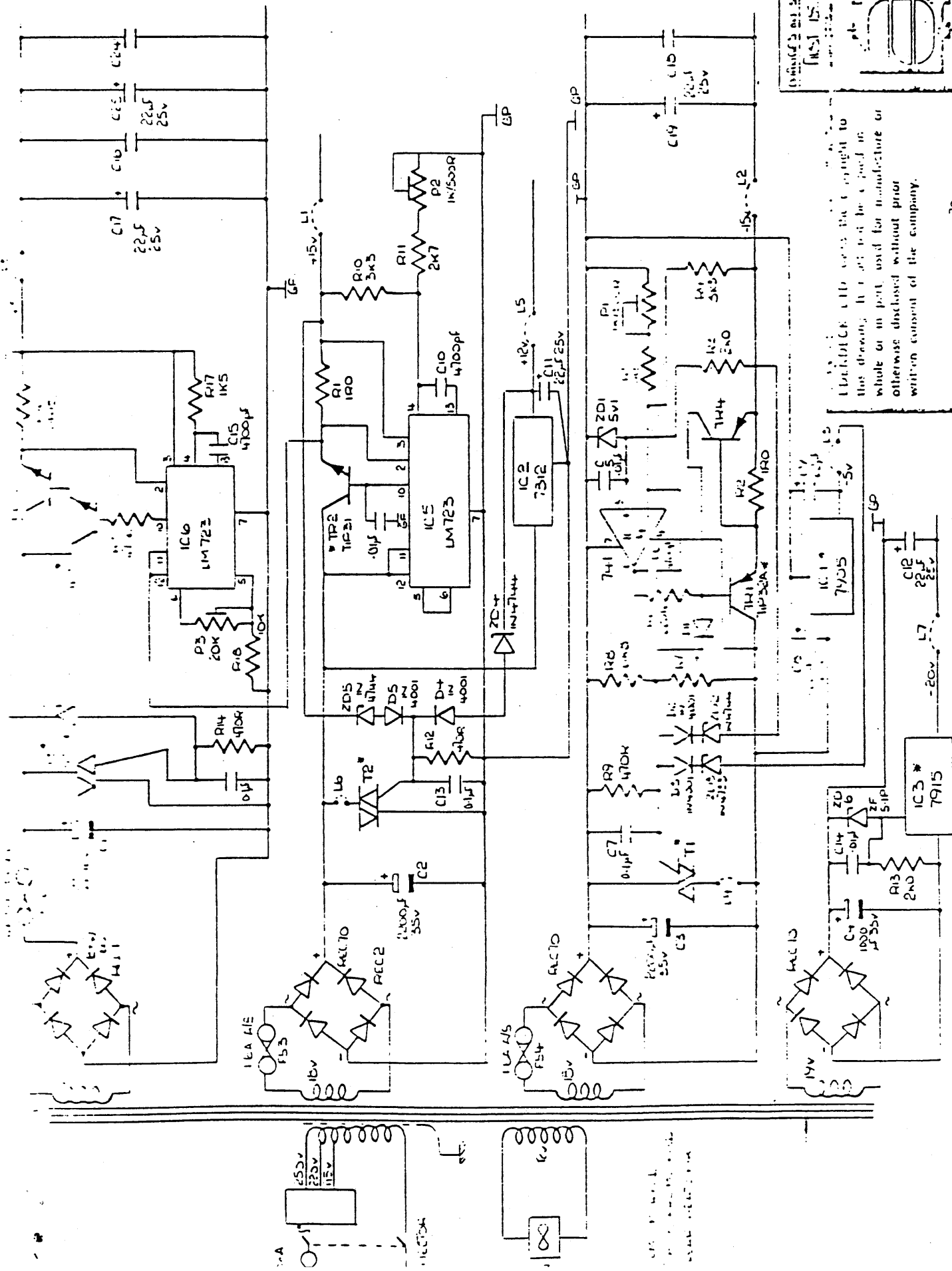
Advanced music systems, Bingley, England.

DATE 20.9.79

DRG. No. DMX MB.2 SHT 4



Change SHT 5	17.8	1
Change SHT 5	30.9.80	2
ORIGINAL SHEET CHANGE	3.7.80	2
FIRST ISSUE	28.9.79	1



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1	10/14/75	J. J. J.
2	10/14/75	J. J. J.



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DATE

10/14/75

URA

